Sunscatter MPPT MCU Powered and controlled by USB through STLink UART. STM32L432KC Nucleo is the microcontroller unit of the PCB. Requires A7 (PA2) and A2 (PA3) to be reserved for STLink UART-USB communication. Can be powered through the following: $\begin{array}{cccc} -1.08-13.20\text{VC} & \text{Coar Power}) & -7-12\text{VDC} & \text{External 9V Supply} \\ -50\text{VDC} & \text{Nucleo USB} & \end{array}$ Praise the sun. A student designed maximum power point tracker custom built for the Longhorn Racing Solar car. Optimized to run the latest and greatest MPPT algorithms. TP102 111.01 MCU_Nucleo_L432KC PWR_FLAG O J101 SW101 9V Supply HEARTBEAT_1 USB D1 VIN SW_Push TRACKING 2 DO C101 GND 100nF RST 028 RESET RESET 3 RST $\overline{-}$ 4 GND (Sale BN D2 E BN D3 NA Errata +5٧ CAN_TX_5 D2 26 × Α7 A6 25 BATTERY_VOLTAGE ERROR 6 D3 v3.2.0

Schematic:

Reannotated all component labels and reverted rescued schematic symbols.

Rearranged schematic and cleaned up component positioning.

Created Array,lib for array-relevant schematic symbols.

Swapped out zener diodes going into the uC from 3.6V to 3.9V.

Updated the LEDs and resistors for Xc,live indicators.

Added Error indicator LED.

Chiffan pine for violation-/current sensors to accomodate STLink UART2. × 7 D4 24 BATTERY_CURRENT A5 Pin X 8 D5 23_ARRAY_CURRENT Α4 VIN: 7-12V -> Input 5V0: 5V -> Output on USB/VIN 3V3: 3.3V -> Output always × 9 SW_DIP_x02 22_ARRAY_VOLTAGE А3 D6 10 D7 21 × Top Left Mounting Hole 11 D8 <u>20</u> PWM Added unity gain filter voltage sensors to accommodate STLink UART2.
 Added fuse on the array side.
 Added unity gain filter to voltage sensors.
 Errata: swapped 0.22uF and 10uF capacitors on CAN circuit to correct positions. TP101 19 PWM_EN +3V3 18 +3V3 ×12 D9 Top Right Mounting Hole H102 CAN_RX 13 D10 AREF 17 ×14 D11 +3V3 H103 Layout:

— Created MPPT—PrimaryPCB.pretty for custom footprints.
— Specified that board should be Zoz copper/ft.
— Re—specified snubber circuit footprints.
— Comprehensive redesign of layout. H103 Bottom Left Mounting Hole ×15 D12 16 X D13 \rightarrow GND Bottom Right Mounting Hole v3.3.0
Schematic:
BoM consolidation to JLCPCB.
BoM merged into schematic component fields.
Removed RC filters from voltage sensors.
Replaced MAX chip with LM356.
Downshifted 3.97 zeners to 3.07
Removed general purpose switching diodes. power_regulation boost_converter +ARR -ARR Layout:

— Moved footprints from MPPT—PrimaryPCB.pretty to Footprints.

— Dropped copper weight down to 1oz/ft.

— Places JLCPCB passives on same side.

Reduced power path area.

— Moved sensor paths away from high power current path.

— Sittching Vlas and secondary GND plane for thermal and EMI improvement. +BATT +BATT -BATT -BATT DRIVE_PWM Schematic: Vout and GNDout pins swapped to correct pins for IESO1_PDS1 (U4),
 EN pin for UCC37321 given an MCU DIO to be SW ambiguous to UCC37321, UCC37322 (U3).
 Maxim Integrated 40075 voltage op amp switched to TI 0PA990 to run on +9VA. File: sunscatter_power_regulation.kicad_sch File: sunscatter_boost_converter.kicad_sch sensors gate_driver Layout:
- Slight changes around power, gate driver routing. ARRAY_VOLTAGE +ARR PWM >PWM v3.4.0 Schematic: ARRAY_CURRENT -ARR PWM_EN PWM_EN BATTERY_VOLTAGE BATTERY_VOLTAGE Broke out schematic into hierarchical sheets and re-enumerated all components. +BATT +BATT DRIVE_PWM DRIVE PWM - Fixed ERC power pin checks. -BATT BATTERY_CURRENT_BATTERY_CURRENT Layout:
- Updated silkscreen. File: sunscatter_sensors.kicad_sch File: sunscatter_qate_driver.kicad_sch can_driver leds CAN_TX HEARTBEAT CAN_RX TRACKING Gary Hallock PWM Matthew Yu CAN_TX CAN_TX Longhorn Racing Solar CAN_RX CAN_RX ERROR Sheet: / File: MPPT.kicad_sch Title: Sunscatter Size: A4 Date: 2022-06-05 Rev: 3.4.0 File: sunscatter_can_driver.kicad_sch File: sunscatter_leds.kicad_sch KiCad E.D.A. kicad (6.0.1) ld: 1/7

Power Regulation

The power regulation circuit manages three supply voltages: +12V, +5V, and +9VA.

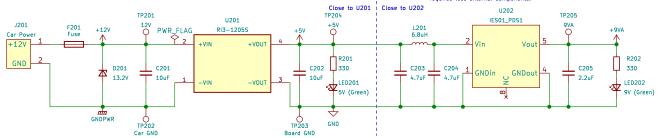
The +12V supply is a 10.8–13.2 VDC input coming from the solar vehicle. This supply is fed through a fixed output buck converter to generate the +5V line, which feeds the STM32 MCU and the isoCAN chip. This +5V line can also be generated from the STM32 MCU, regulating from its +5V USB input or 9V VIN input when the PCB is not hooked up to the solar car.

The +5V supply feeds into boost converter to generate +9VA. This +9VA is produced solely from the +5V source, and is used to power the MOSFET gate driver and all onboard sensors.

Isolated Power Regions:
* Car Power
+ 12V
- GNDPWR
* Device/Board Power
- +/-Arr
- +/-Batt
- 49VA, +5V, +3.3V
- GND

IES0105S09 Cout=2.2uF Imin=12mA

GND is tied across the isolated DC-DC converter to make it non isolated. This is because the sensor input to the STM32 needs to be relative to GND. This is probably overkill compared to a normal Ti SEPIC converter but requires less external components.



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Longhorn Racing Solar

Sheet: /power_regulation/

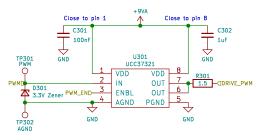
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PWM Gate Driver

Drives the two DC-DC converter MOSFETs.



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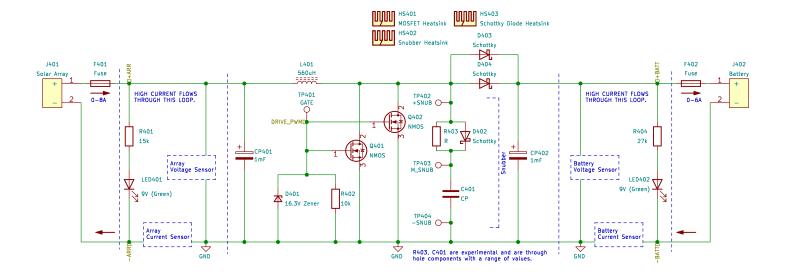
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DC-DC Boost Converter

0-115V to 0-175V boost converter. Maximum 8A current flow.



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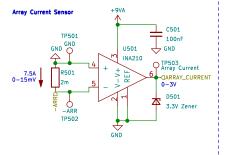
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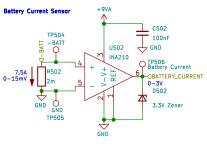
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 Rev: 3.4.0

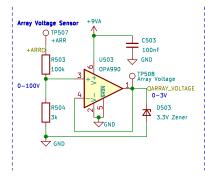
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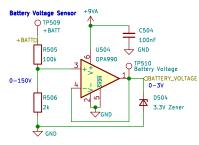
Sensors

2 voltage sensors and current sensors, one of each for the array side and battery side. Used for identifying DC-DC converter operation and informing MPPT operation. 8A support through current sense resistors. 100V / 150V support for voltage sensor scaling.









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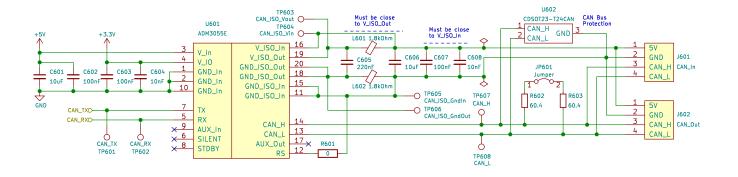
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 Rev: 3.4.0

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CAN

Isolated CAN chip.



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