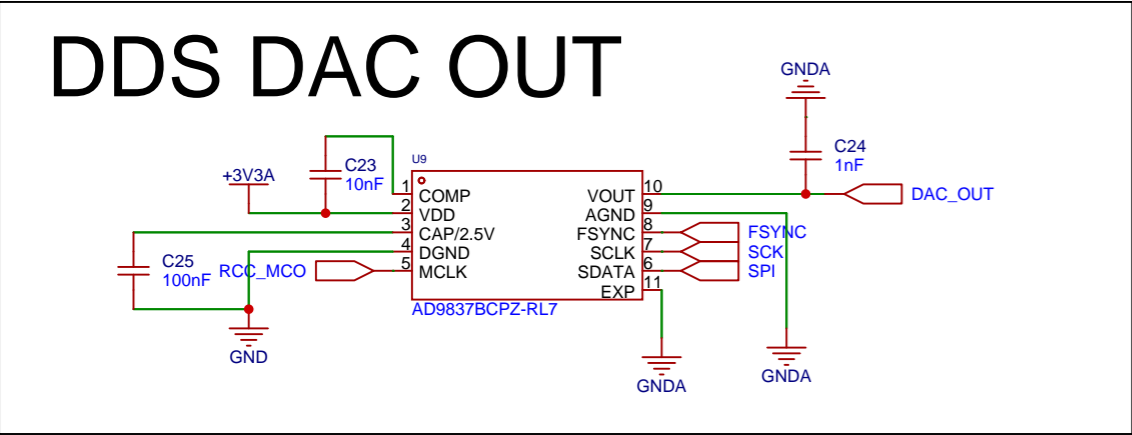
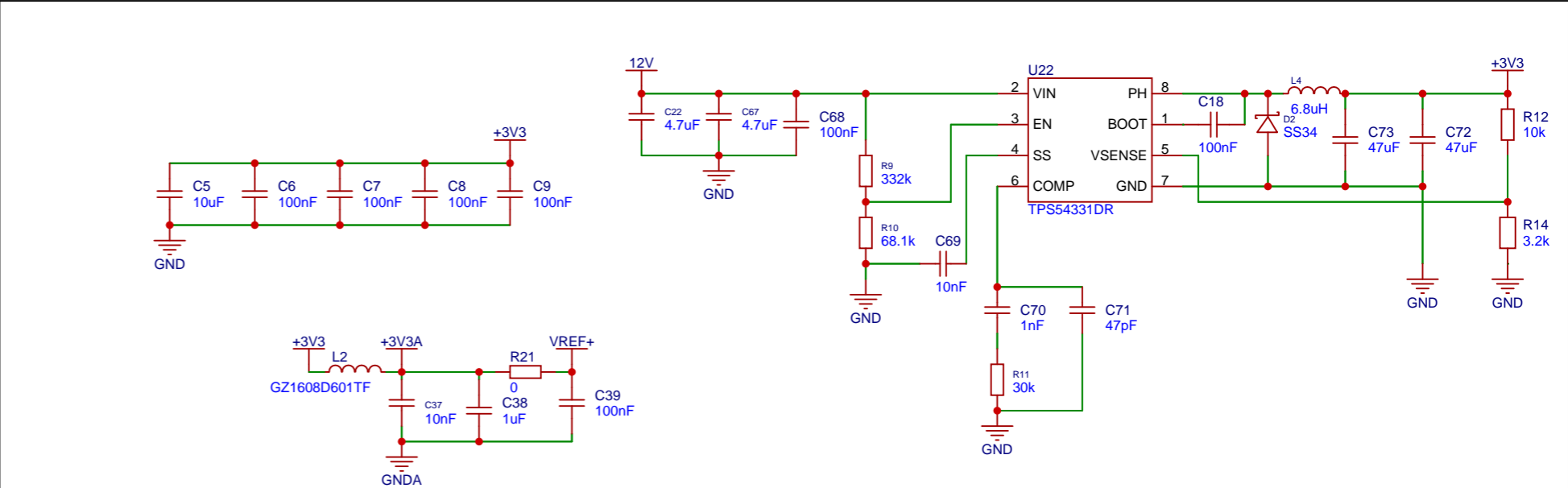


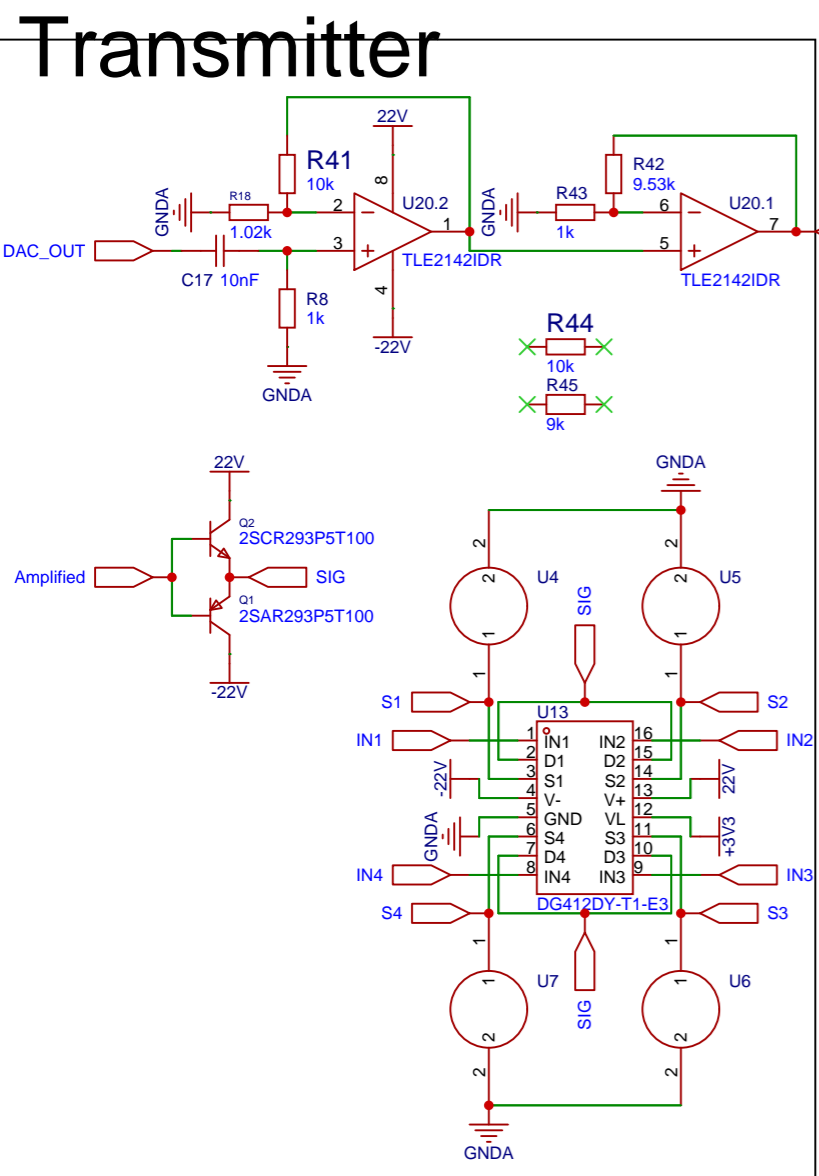
Negative Reference



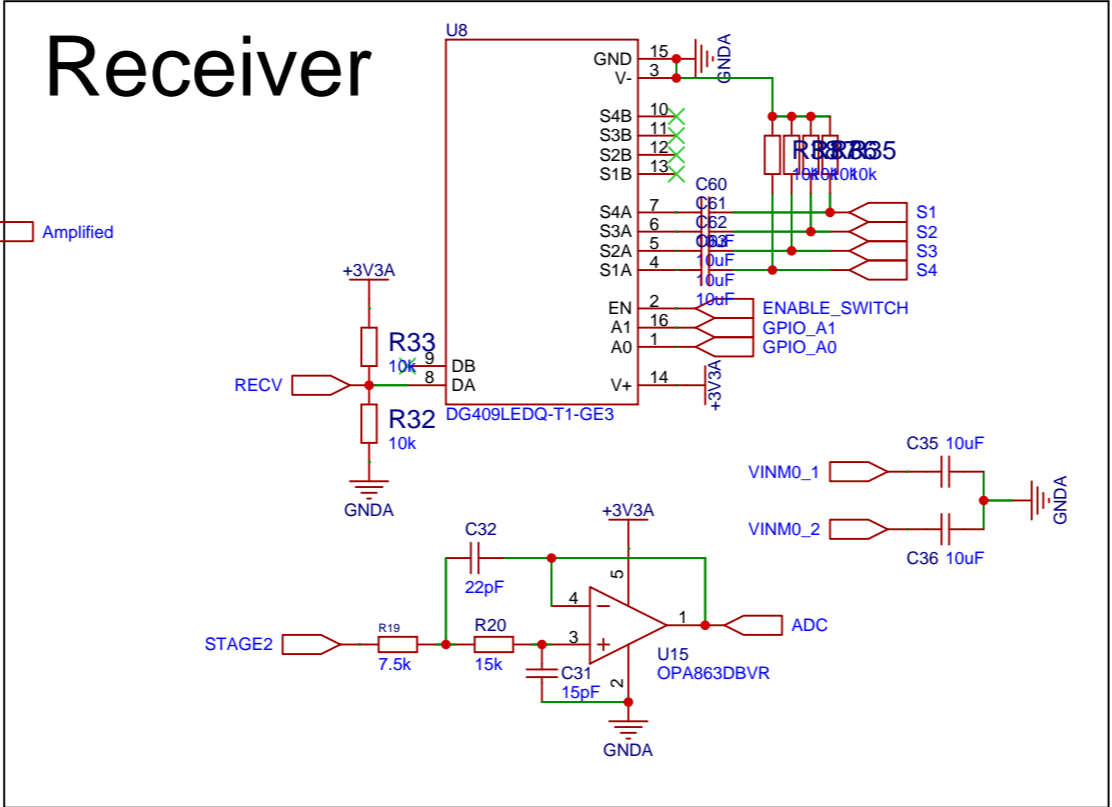
DDS DAC OUT



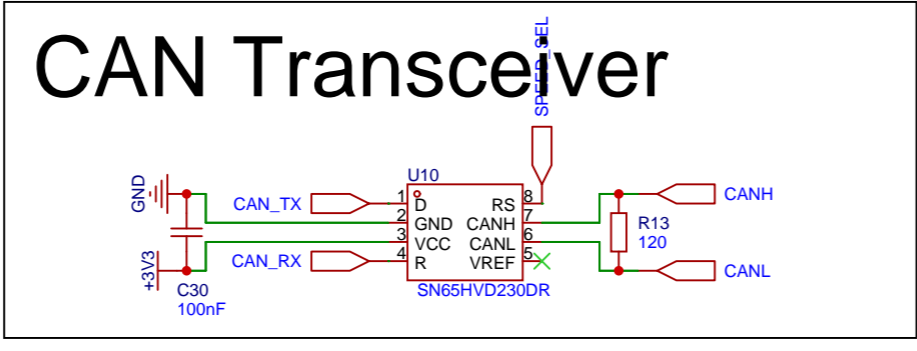
Power



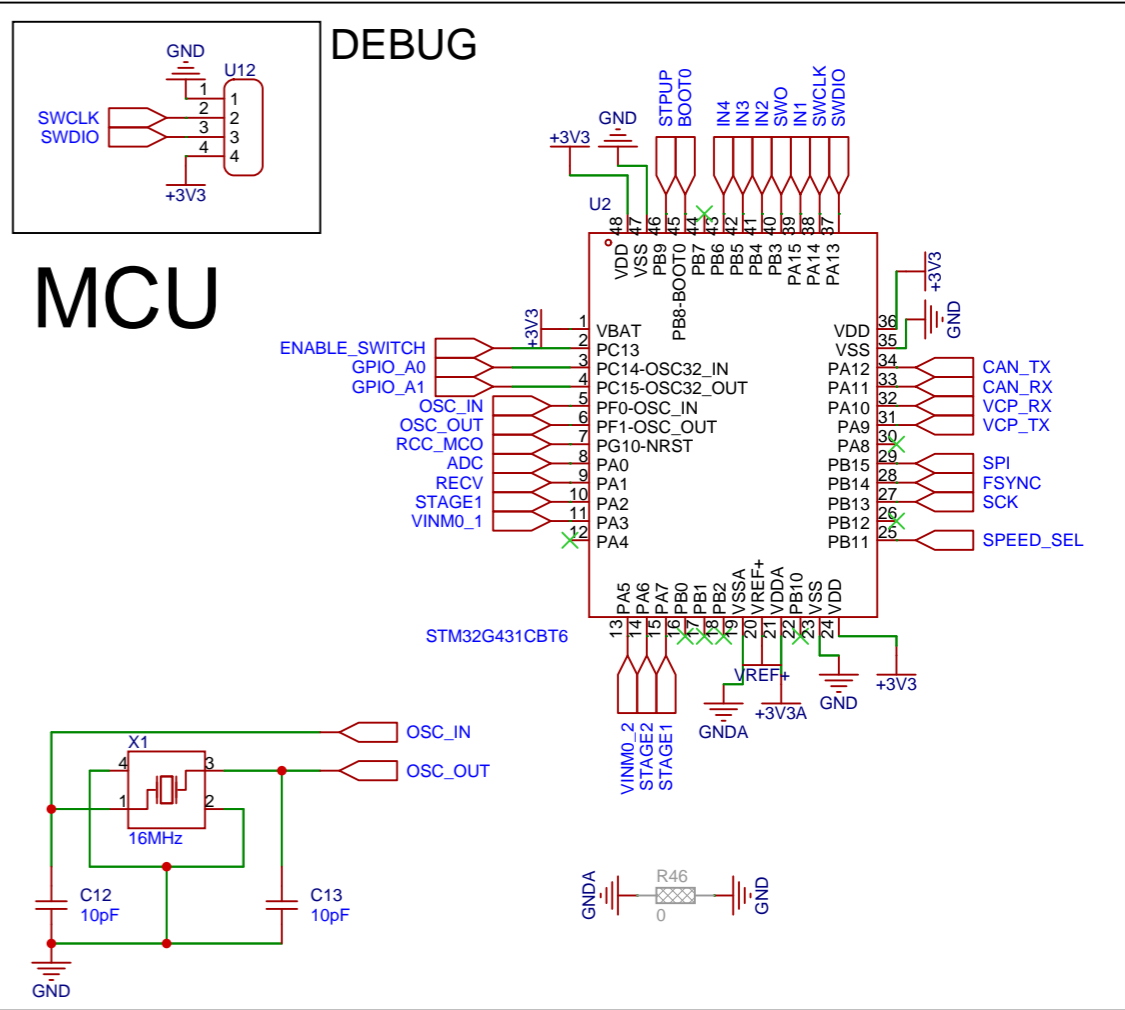
Transmitter



Receiver




CAN Transceiver



DEBUG

MCU

Schematic	sF			Update Date	2025-09-07
				Create Date	2025-08-26
Page	P1			Part Number	JLPCPB-002
Drawn	EasyEDA Pro	RTMR			
Reviewed	EasyEDA Pro				
		VER	SIZE	PAGE	1 OF 1
		V0.1	A4	EasyEDA.com	