Chapter 6

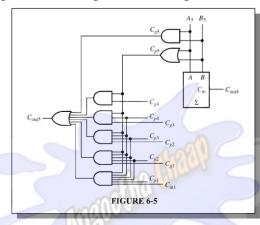
- 2. $A = 0, B = 0, C_{in} = 0$ (a)
 - A = 1, B = 0, $C_{in} = 0$ or A = 0, B = 1, $C_{in} = 0$ or A = 0, B = 0, $C_{in} = 1$
 - $A = 1, B = 1, C_{in} = 1$ (c)
 - A = 1, B = 1, $C_{in} = 0$ or A = 0, B = 1, $C_{in} = 1$ (d) or A = 1, B = 0, $C_{in} = 1$
- 3. $\Sigma = 1$, $C_{\text{out}} = 0$ (a)
- $\Sigma = 1$, $C_{\text{out}} = 0$ (b)
- $\Sigma = 0$, $C_{\text{out}} = 1$ (c)
- $\Sigma = 1$, $C_{\text{out}} = 1$
- When the \overline{Add} / Subt is HIGH, the two numbers are subtracted. 6. (a)
 - (b) When the input is LOW, the numbers are added.

$$A_2 = 1$$
. $B_2 = 1$ $B_2 = 1$ $B_2 = 1$ $B_2 = 1$ $B_2 = 1$

$$C_0 = 0$$
 $C_V = 1$ $C_V = 2_V = 2_V = 10100$

- $t_{p(tot)} = 40 \text{ ns} + 6(25 \text{ ns}) + 35 \text{ ns} = 225 \text{ ns}$
- 12. Full-adder 5: $C_{\text{in5}} = C_{\text{out}} 4$ $C_{\text{out5}} = C_{g5} + C_{p5}C_{g4} + C_{p5}C_{p4}C_{g3} + C_{p5}C_{p4}C_{p3}C_{g2} + C_{p5}C_{p4}C_{p3}C_{g2}C_{g1} + C_{p5}C_{p4}C_{p3}C_{p2}C_{p1}C_{\text{in1}}$

The logic to be added to text Figure 6-18 is shown in Figure 6-5.



See Figure 6-7. A = 1001 A = 1111 A = 1110 A = 1100 A = 0101 B = 0100 B = 1111 B = 0010 B = 0011 B = 1100 A < BA = BA > BFIGURE 6-7 **19.** $X = \overline{A_3 A_2 A_1} A_0 + A_3 \overline{A_2} A_1 \overline{A_0} + A_3 A_2 \overline{A_1} \overline{A_0} + A_3 \overline{A_2} A_1 A_0$ See Figure 6-9. 00 01 00 0 (1) 0 0 0 0 0 11 (1) 0 0 0 (1 1) $X = \overline{A_1} \overline{A_2} \overline{A_1} A_0 + A_3 A_2 \overline{A_1} \overline{A_0} + A_3 \overline{A_2} A_1$ FIGURE 6-9 See Figure 6-11. A_0 A A_2 A 0 2 3 HIGH 4 5 -HIGH 6 7 HIGH 8 9 FIGURE 6-11 0 1 6 9 4 4 4 8 0 Pin 2 is for decimal 5, pin 5 is for decimal 8, and pin 12 is for decimal 2. The highest priority input is pin 5. The completed outputs are: $\overline{A_2 A_2 A_3 A_4} = 0111$. which is binary 8 (1000). binary 1111100000 binary 1111111111 1000010000 gray gray binary 1000000000 gray

22.

26.

1010101010

0000001110 binary (c) 0000001001

See Figure 6-12.

