Summary Exercise - Week 1

Due Oct 6 at 11:59pm **Allowed Attempts** 2

Points 35

Questions 35

Time Limit 360 Minutes

Instructions

There's a six-hour time limit.

Take the Quiz Again

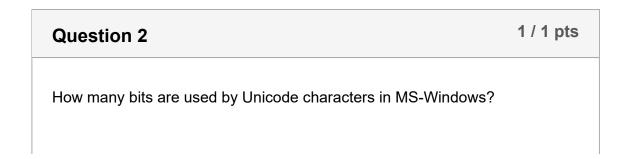
Attempt History

	Attempt	Time	Score
LATEST	Attempt 1	55 minutes	35 out of 35

Score for this attempt: 35 out of 35

Submitted Oct 1 at 4:45pm
This attempt took 55 minutes.

	Question 1	1 / 1 pts
	Is the expression $X \vee (Y \wedge Z)$ equivalent to $(X \vee Y) \wedge (X \vee Z)$ possible inputs of X, Y, and Z?	for all
Correct!	True	
	False	



Correct!	16
	© 8
	O 24
	0 7

	Question 3	1 / 1 pts
	The integer range of standard ASCII codes is:	
	-128 to +127	
	0 to 270	
	0 to 65,535	
Correct!	0 to 127	

	Question 4	1 / 1 pts
	The three most basic operators in Boolean algebra are AND, OR, a	and NOT.
Correct!	True	
	False	

Question 5	1 / 1 pts

	A doubleword (on x86 systems) is 32 bits.	
rect!	True	
	False	
	Question 6	/ 1 pts
	Three hexadecimal digits can be used to represent 12 binary bits.	
ect!	True	
	False	
	Question 7	/ 1 pts
	The ASCII codes for alphabetic letters are smaller than for decimal dig	
	True	
	False	
	Question 8	/ 1 pts
	Which of the following binary values is equivalent to hexadecimal 4A2	B?
	0100 1010 0010 1101	

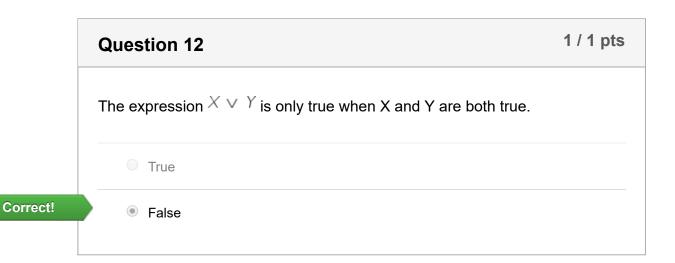
Correct!	© 0100 1010 0010 1011
	0110 1011 0010 1001
	0100 1010 1011

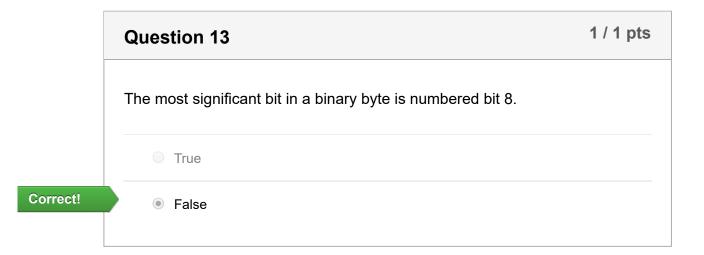
	Question 9	1 / 1 pts
	If an integer's sign bit is 1, the integer is positive.	
	True	
Correct!	False	

	Question 10	1 / 1 pts
	Which of the following is the binary translation of signed decimal -3	3?
	O 11100011	
	0 10101011	
Correct!	11011111	
	11001100	

Question 11	1 / 1 pts
A program is considered portable if it	

Correct!	can be executed on multiple platforms.
	onone of the above
	can be rewritten in a different programming language without losing its meaning.
	can be quickly copied from conventional RAM into high-speed RAM.





Question 14 1 / 1 pts The two's complement of an integer is formed by doing which of the following?

	changing the highest bit to a 1
	adding 2 and reversing the bits
Correct!	reversing (inverting) the bits and adding 1
	 adding 1 and reversing the bits

	Question 15	1 / 1 pts
	A quadword is 4 bytes.	
	True	
Correct!	False	

	Question 16	1 / 1 pts
	Which internal bus uses binary signals to synchronize actions of attached to the system bus?	all devices
	address bus	
	data bus	
	○ I/O bus	
	cache bus	
Correct!	control bus	
	city bus	

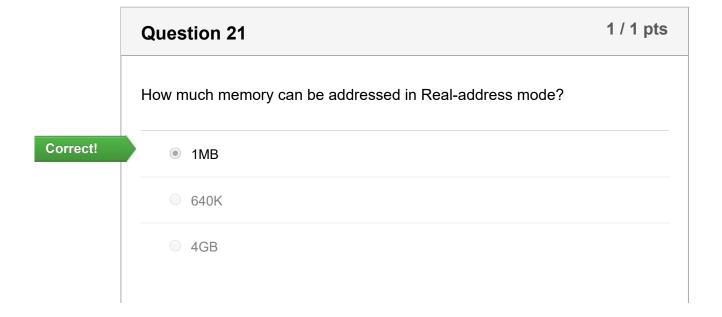
Question 17	1 / 1 pts
What is the name of the lowest 8 bits of the EDX register?	
O DH	
onot listed	
○ DX	
DL	
	What is the name of the lowest 8 bits of the EDX register? DH not listed DX

What is the name of the time delay in a CPU caused by differences between the speed of the CPU, the system bus, and memory circuits? • wait state

Correct!

wait cycle	
spin cycle	
dead cycle	
bicycle	
memory write cycle	

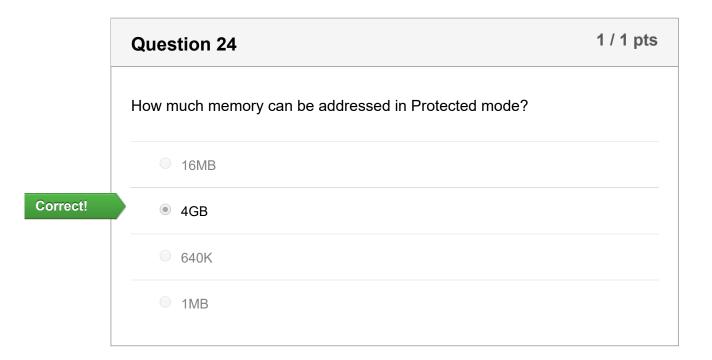
Correct! Question 20 1/1 pts List the three primary steps of the instruction execution cycle, in sequential order. fetch, memory read, execute fetch, decode, memory write fetch, decode, execute decode, fetch, execute



16MB				

Correct!	Question 22	1 / 1 pts
	The four parts of a CPU are:	
	address bus, registers, control unit, arithmetic logic unit	
	clock, registers, control unit, arithmetic logic unit	
	 clock, memory unit, control unit, instruction fetch unit 	
	data bus, memory unit, control unit, arithmetic logic unit	

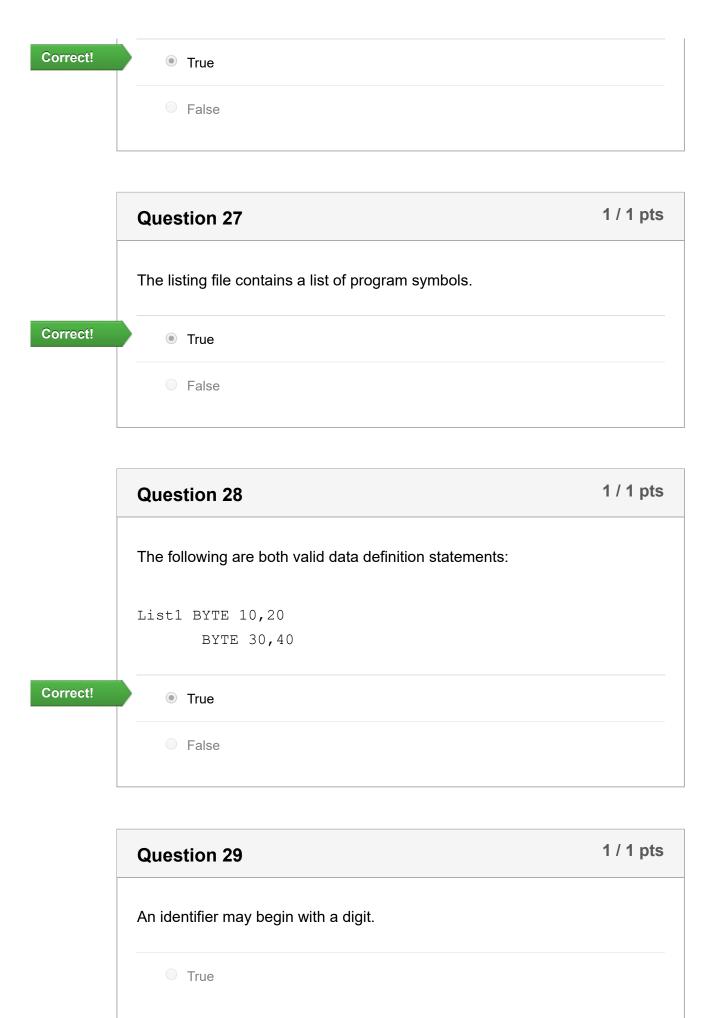
	Question 23	1 / 1 pts
	Which flag is set when an unsigned value is too large to fit into a de operand?	estination
	overflow	
	o red flag	
	auxiliary carry	
	sign	
Correct!	carry	
	checkered	



A High-speed memory that reduces the frequency of access by the CPU to conventional memory is called | system memory | | local memory | | cache memory | | virtual m

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Question 26

The following is a valid data definition statement:
str1 \
BYTE "This string is quite long!",0
```



False

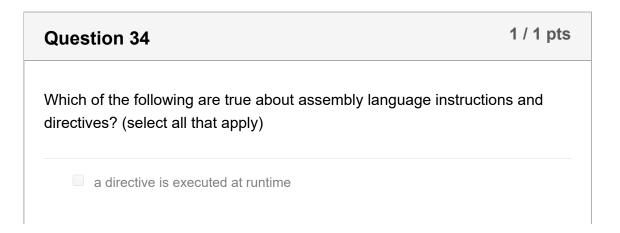
In the AddSub program in Section 3.2, the exit statement calls which predefined MS-Windows function to halt the program? HaltProgram ExitProcess OS_Return Exit_Program

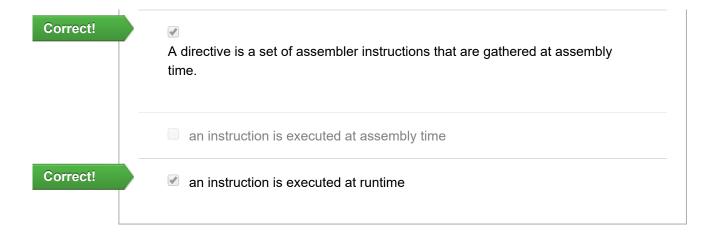
	Question 31	1 / 1 pts
	Which letter choice shows the memory byte order, from low to high of the following data definition?	address,
	BigVal DWORD 12345678h	
	56h, 78h,12h,34h	
	34h,12h,78h,56h	
	2h,34h,56h,78h	
Correct!	● 78h,56h,34h,12h	

Question 32 1 / 1 pts

	The EQU directive permits a constant to be redefined at any point in a program.
	True
Correct!	False

In the following data definition, assume that List2 begins at offset 2000h. What is the offset of the third value (5)? List2 WORD 3, 4, 5, 6, 7 2002h 20008h 20008h 20004h 200006h 200006h





	Question 35	1 / 1 pts
	Which directive identifies the part of a program containing instruction	าร?
	○ .stack	
	o .prog	
Correct!	.code	
	.product	
	○ .data	
	.net	

Quiz Score: 35 out of 35