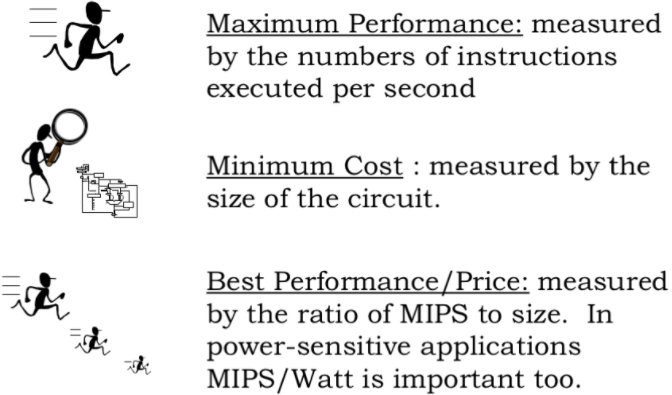
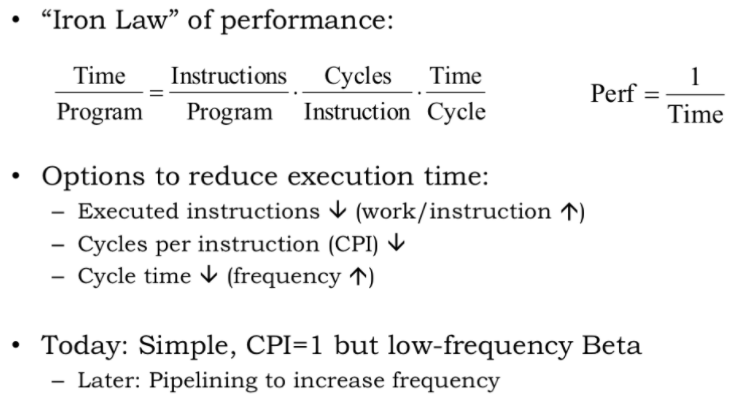
# 课件

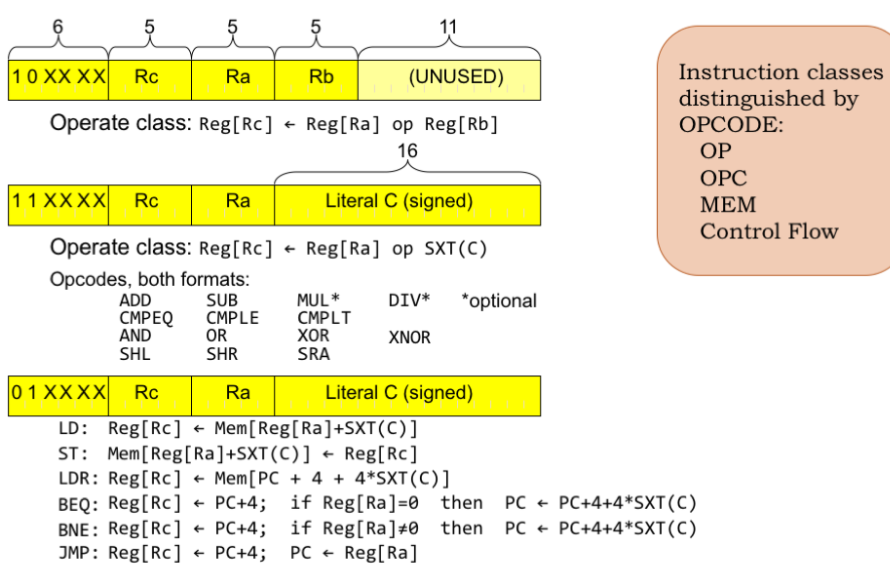
## CPU设计权衡



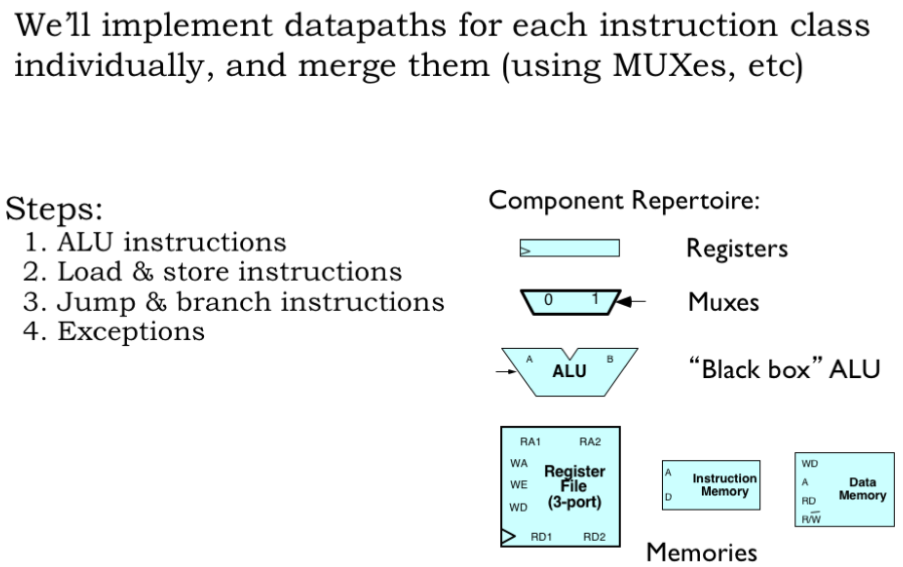
## 处理器性能



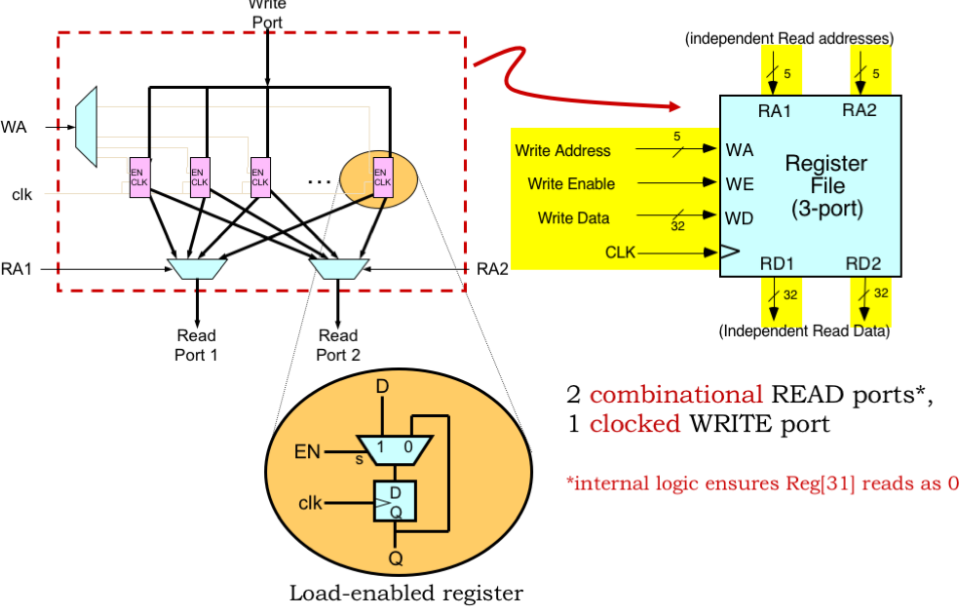
## 提示：Beta指令集



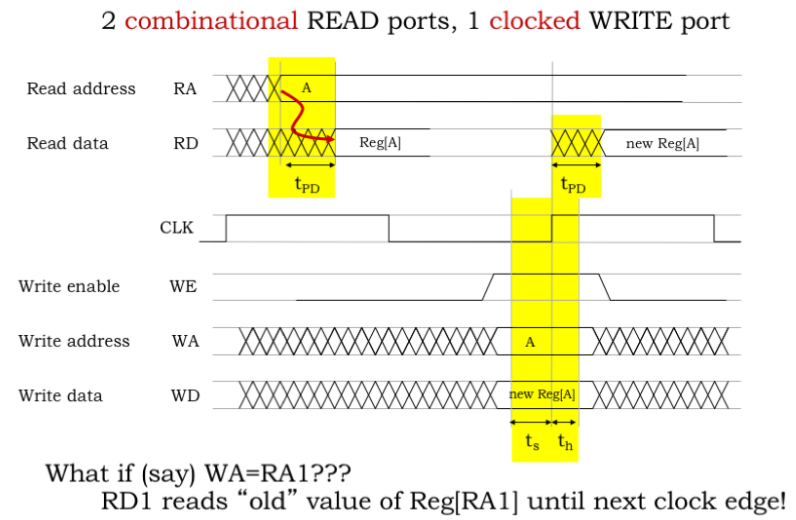
## 方法：提升特性



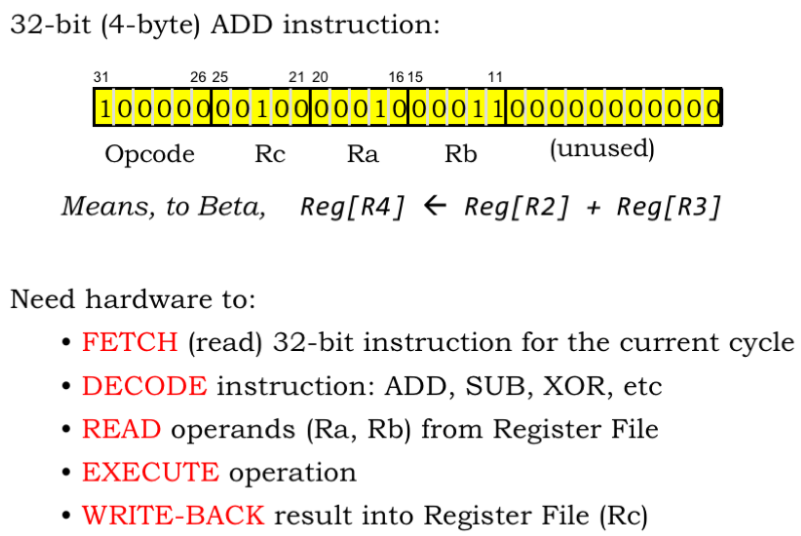
## 多端口寄存器文件



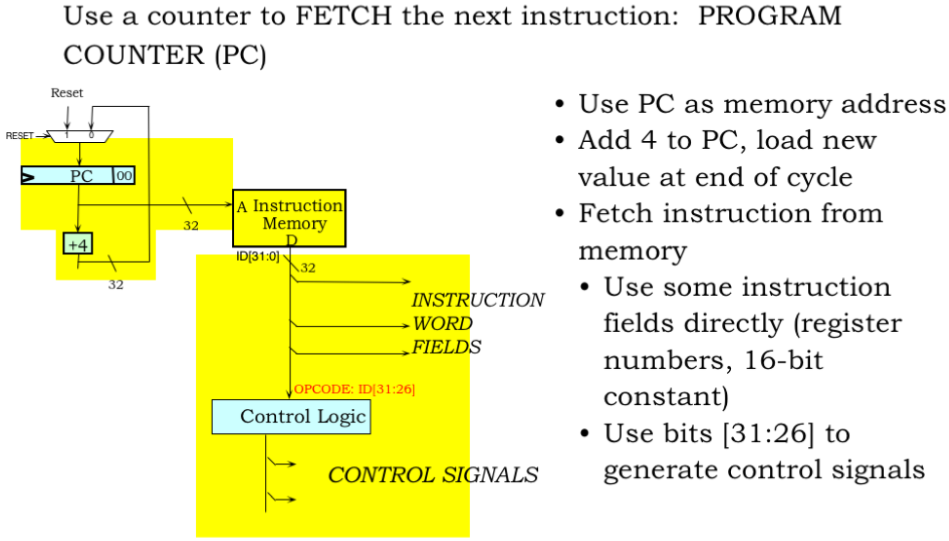
## 寄存器文件时序



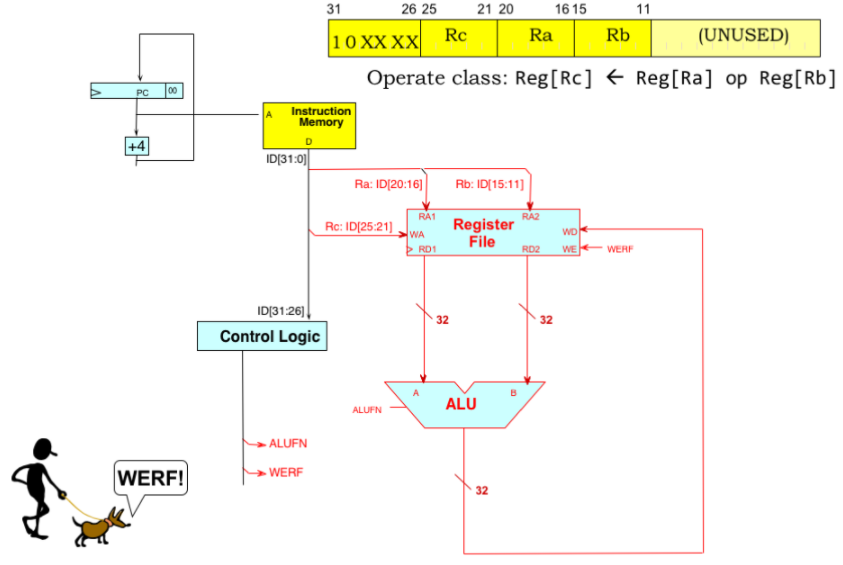
## ALU指令



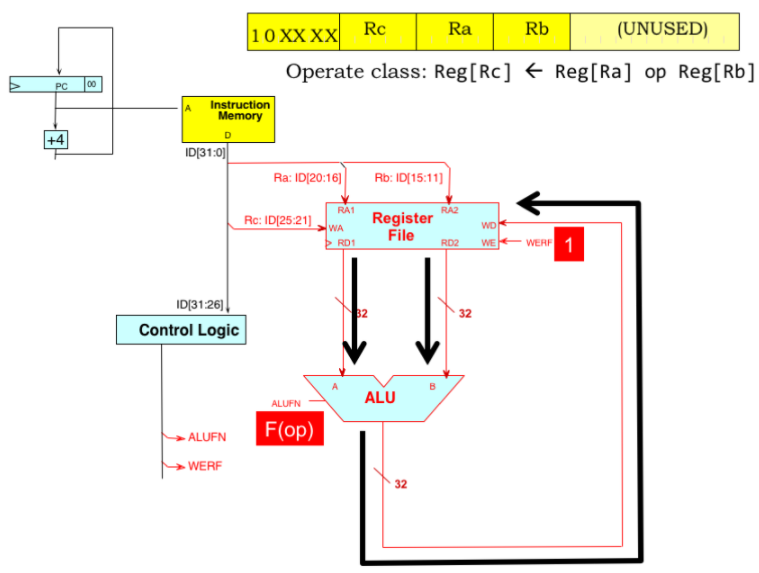
## 指令获取/解码



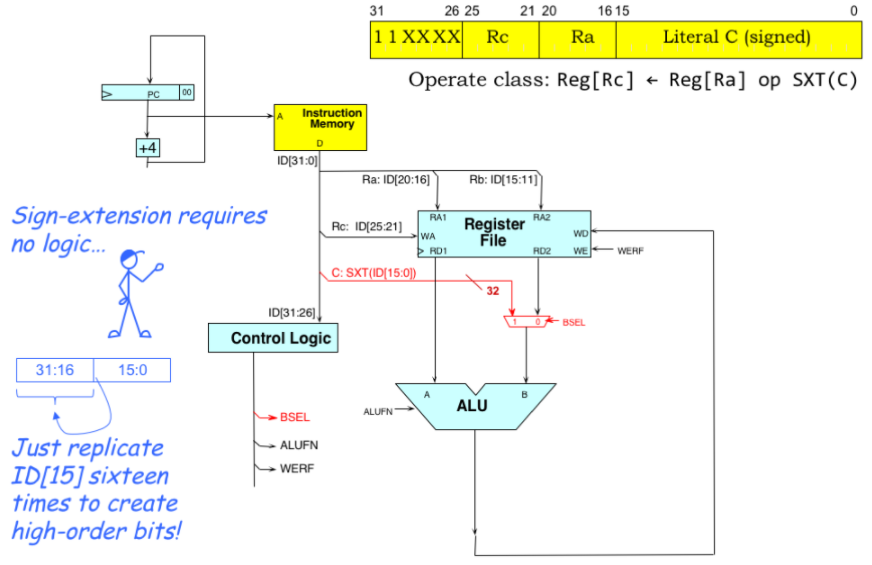
## ALU OP 数据路径1



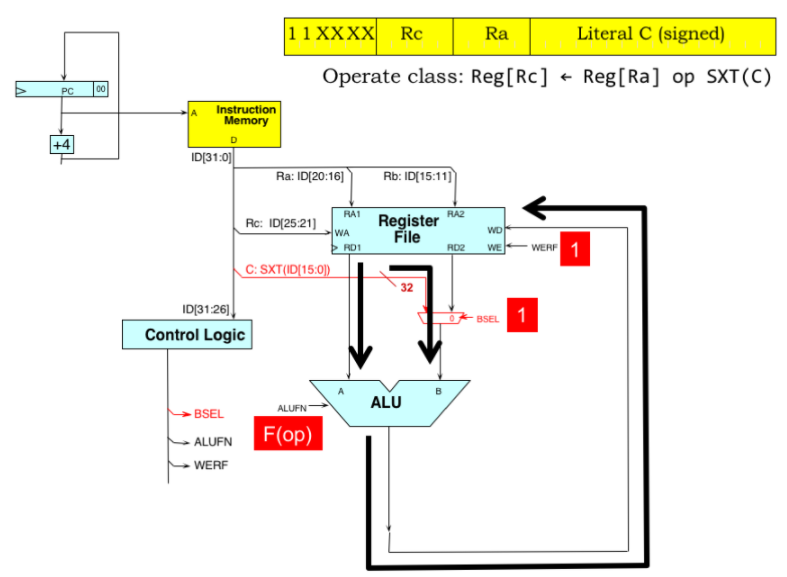
## ALU OP 数据路径2



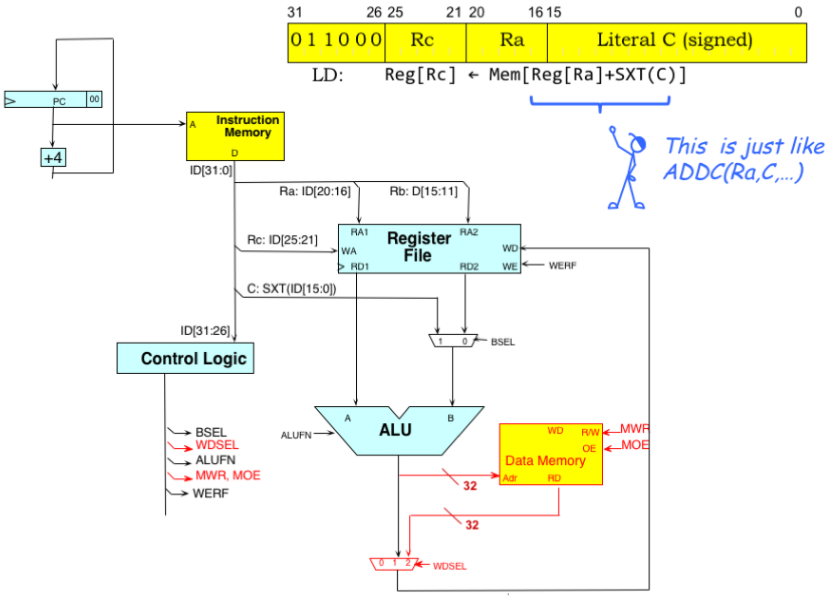
## ALU操作（带有常量）1



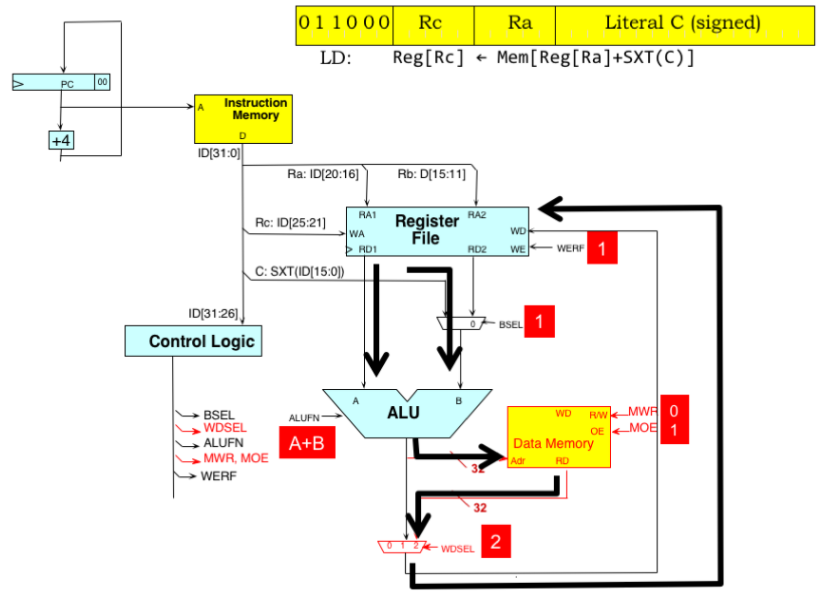
## ALU操作（带有常量）2



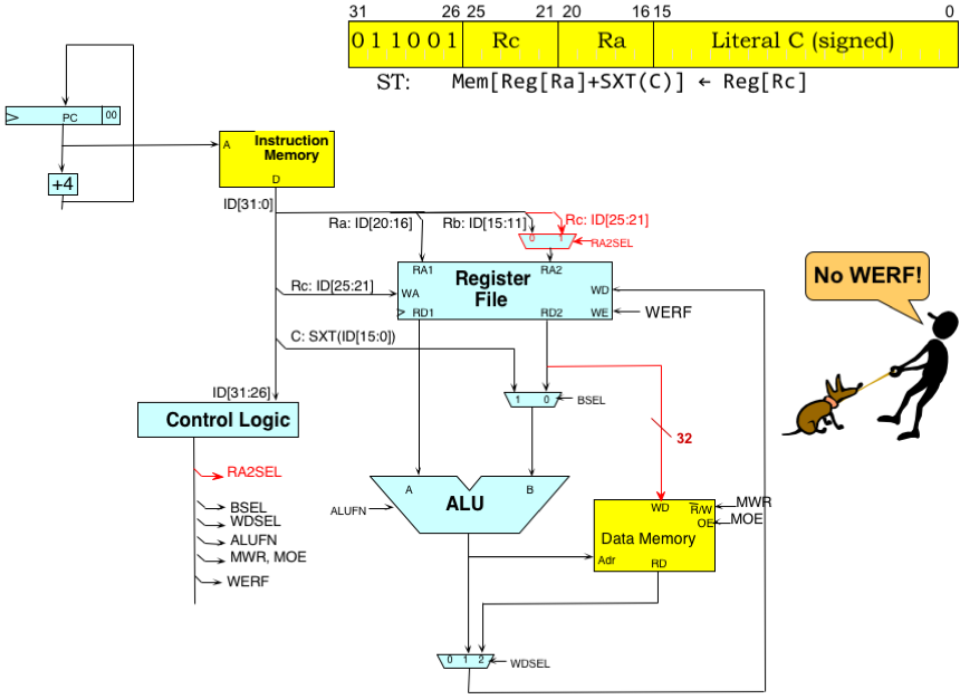
## load指令1



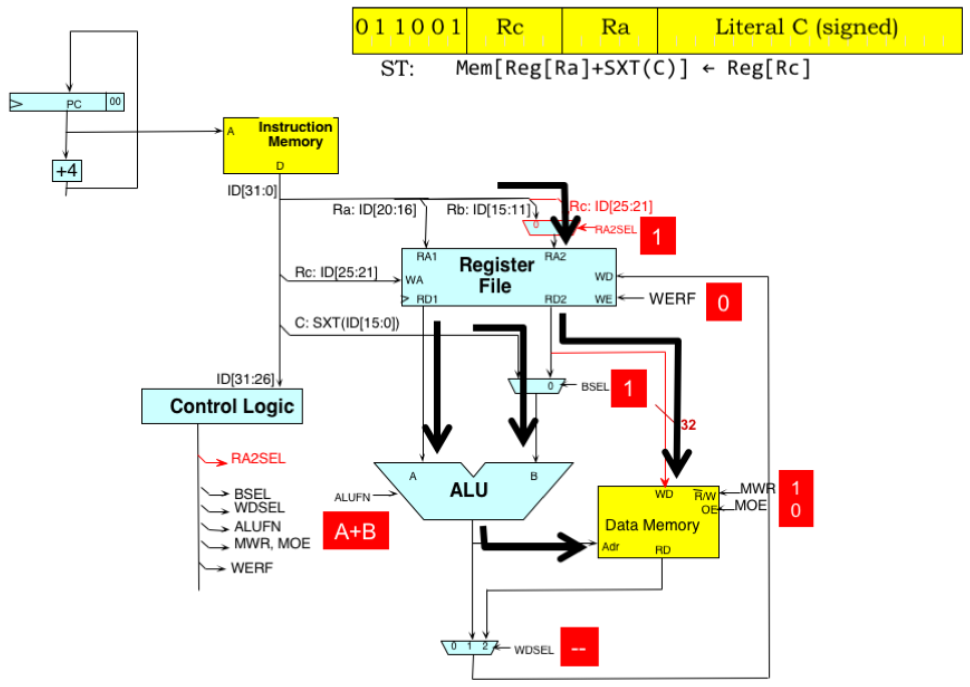
## load指令2



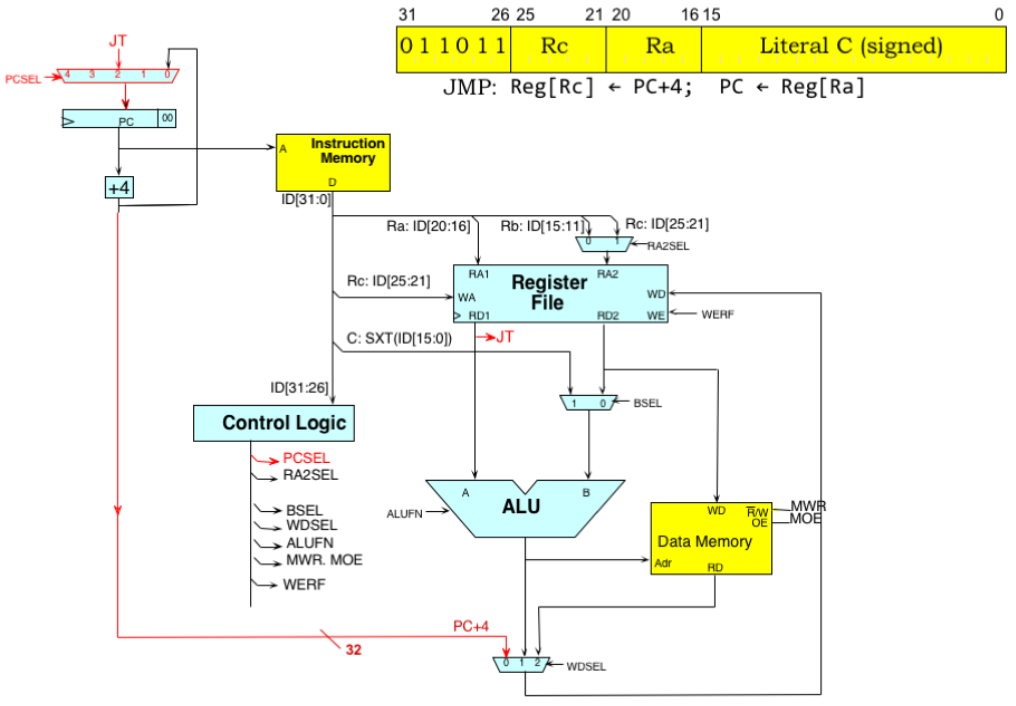
## store指令1



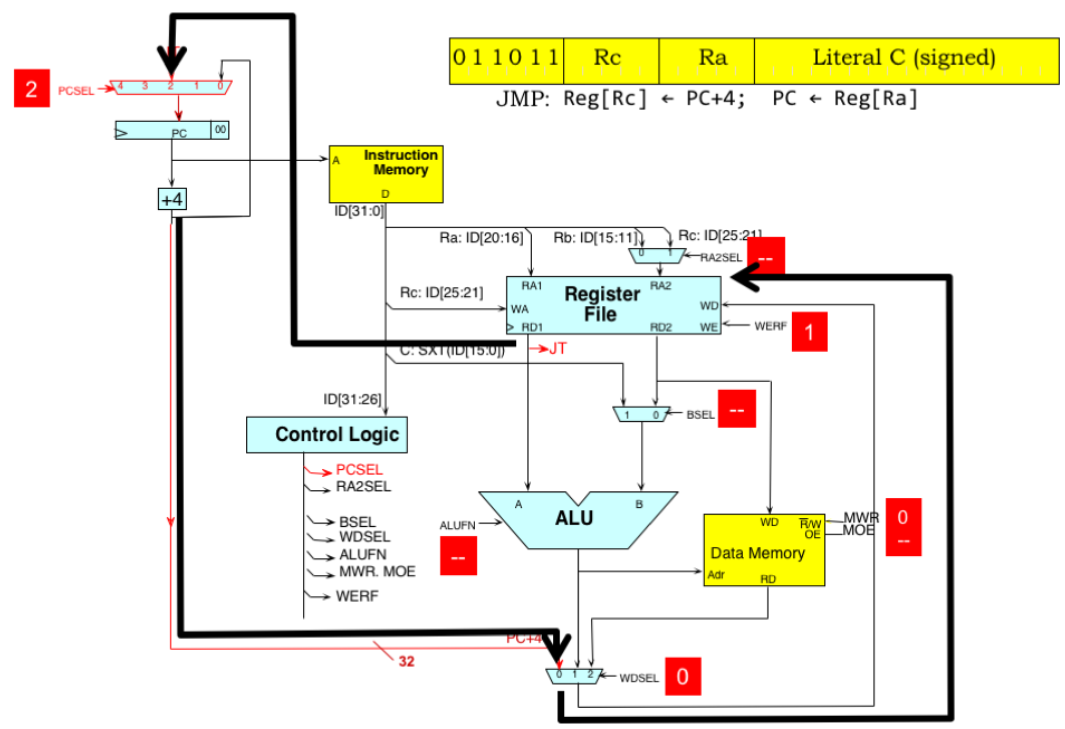
## store指令2



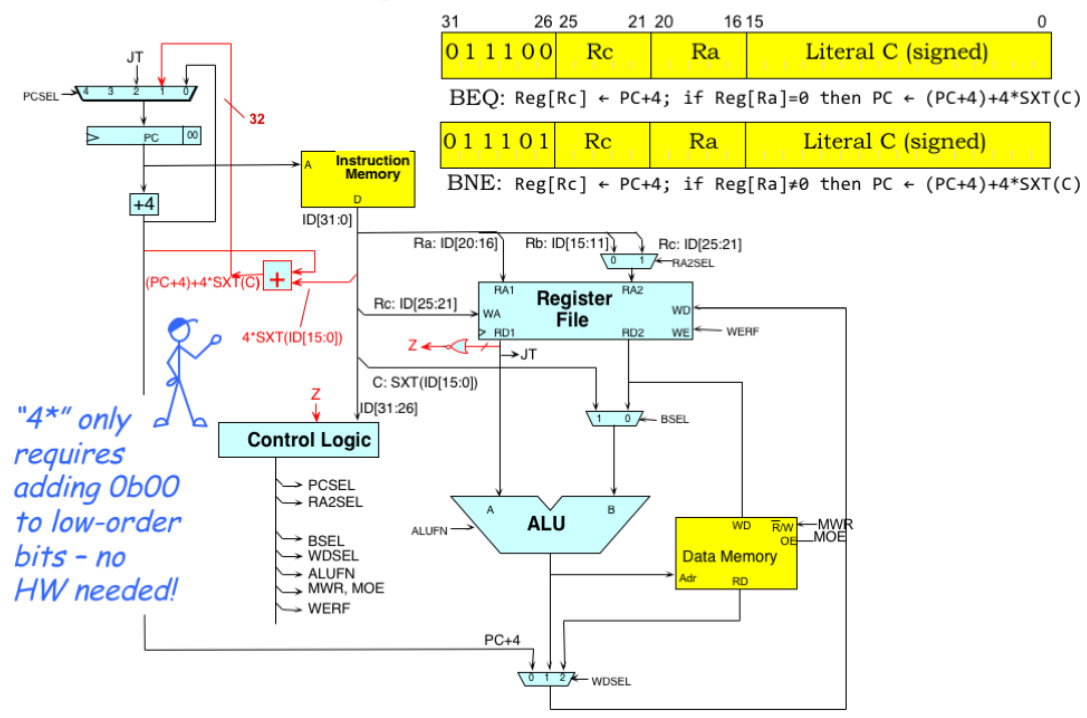
## JMP指令1



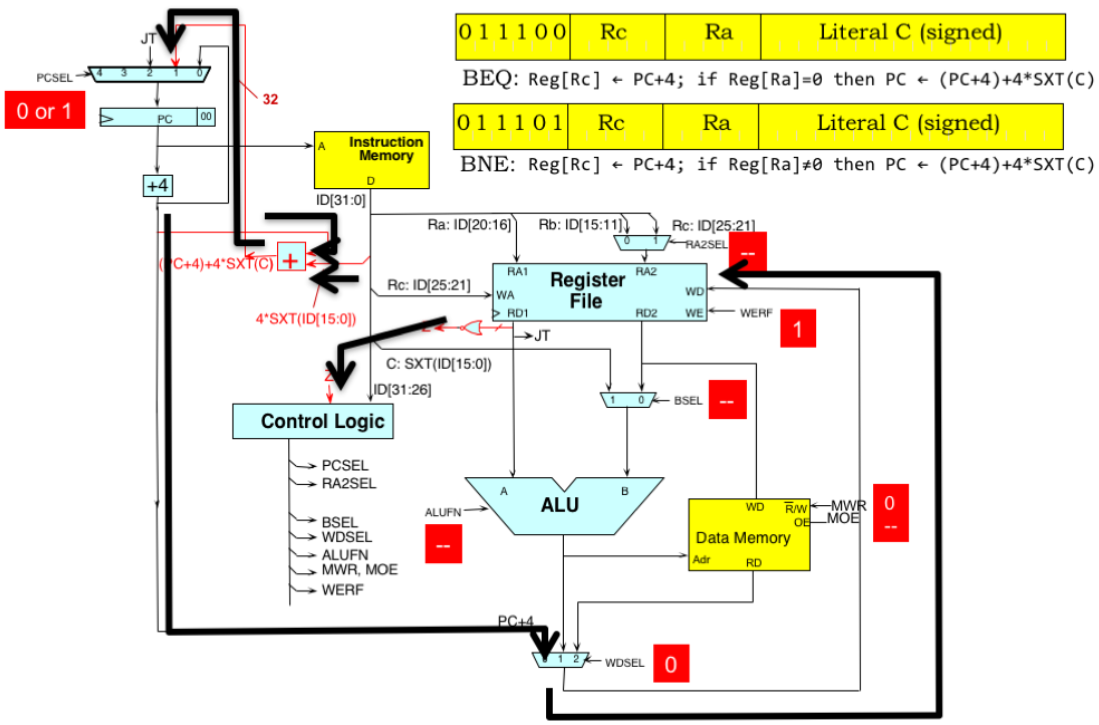
## JMP指令2



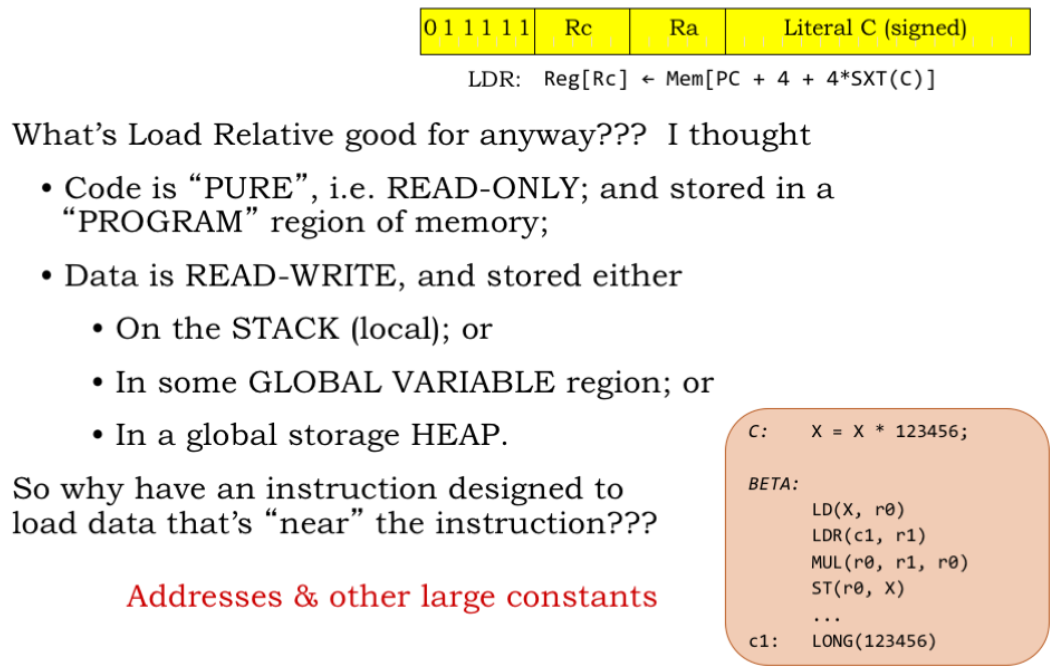
## BEQ/BNE指令1



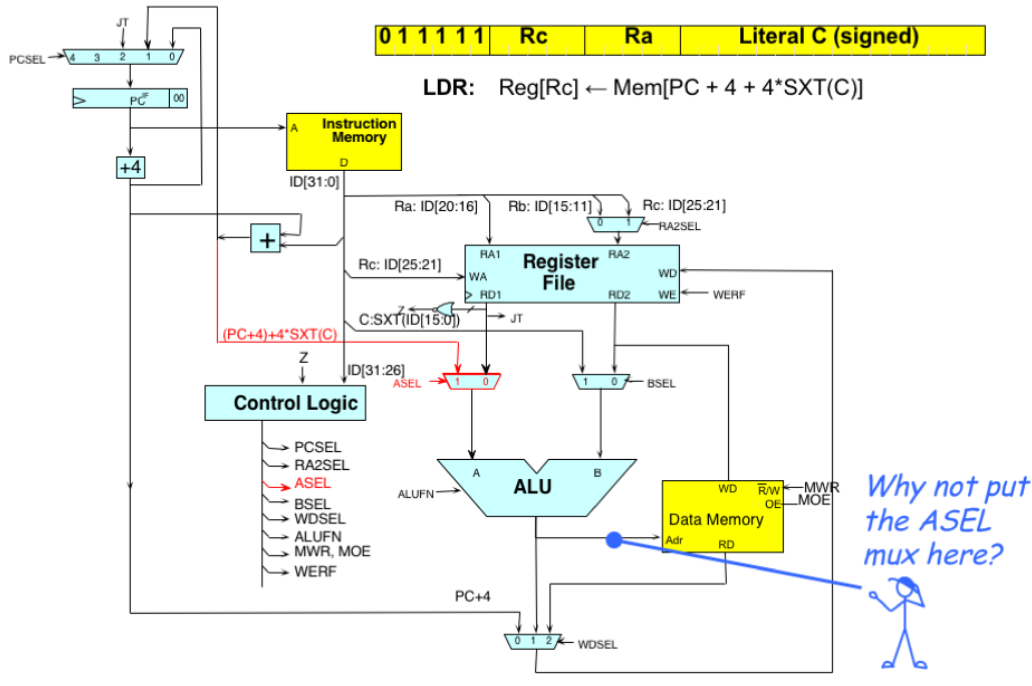
## BEQ/BNE指令2



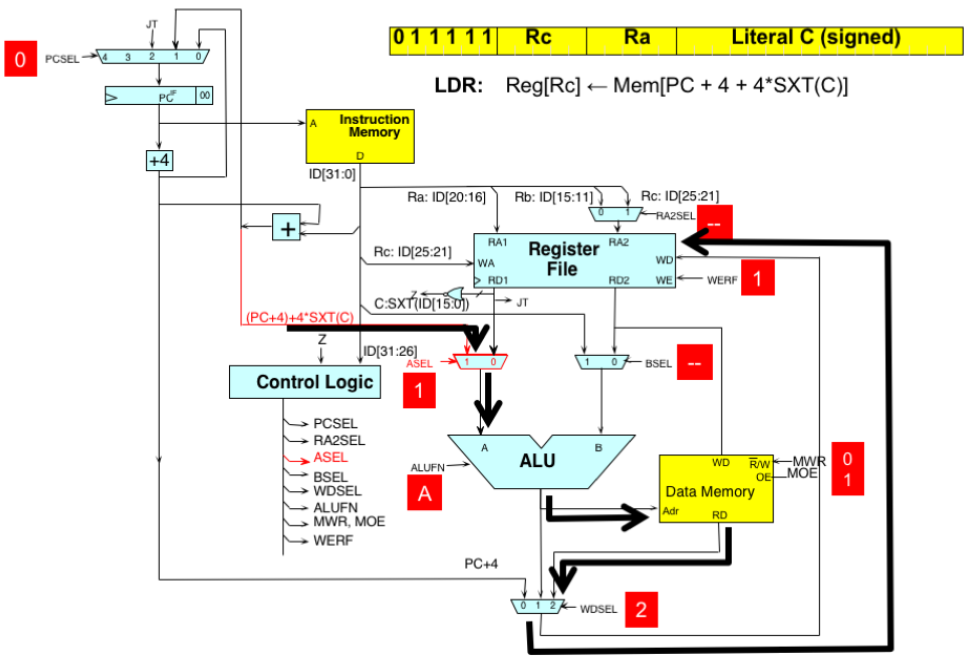
## LDR



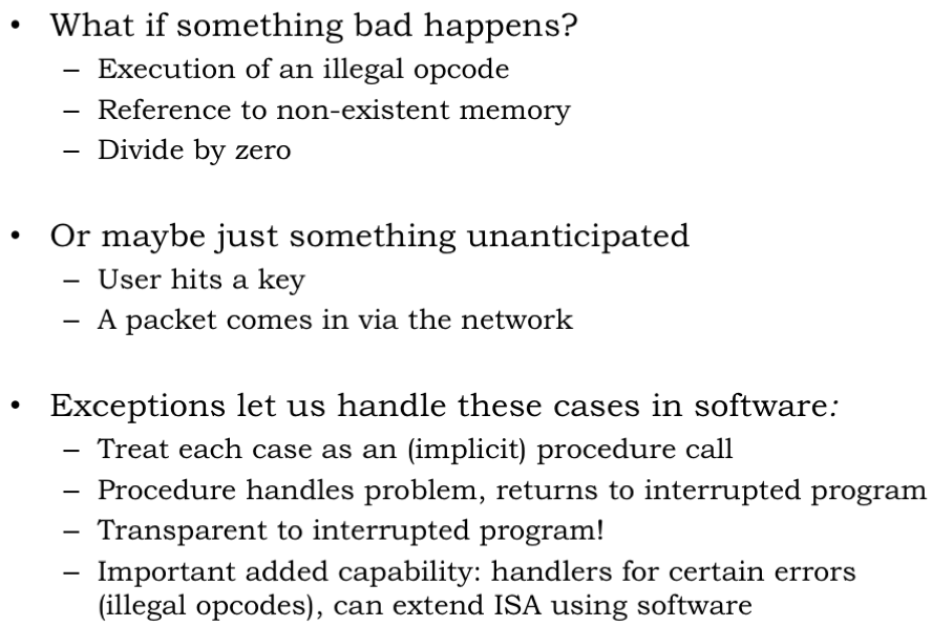
## LDR指令1



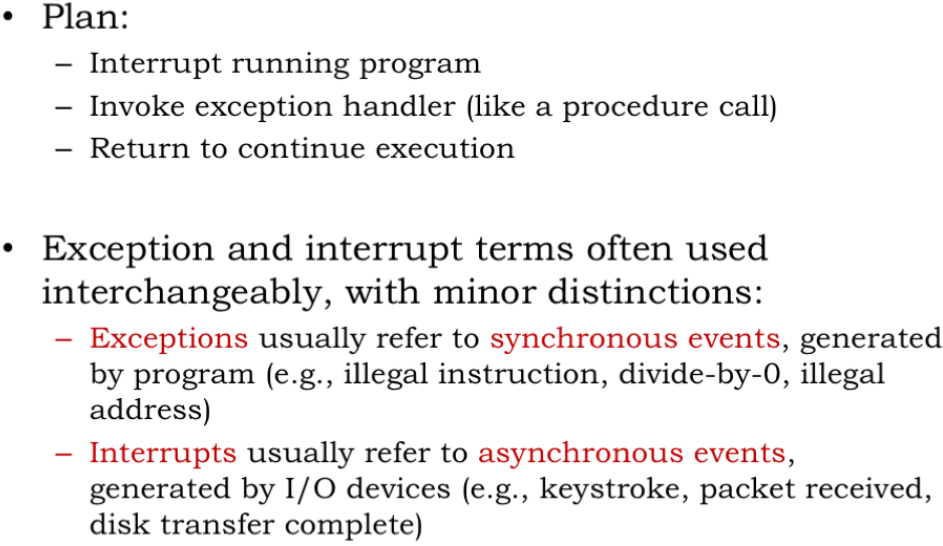
## LDR指令2



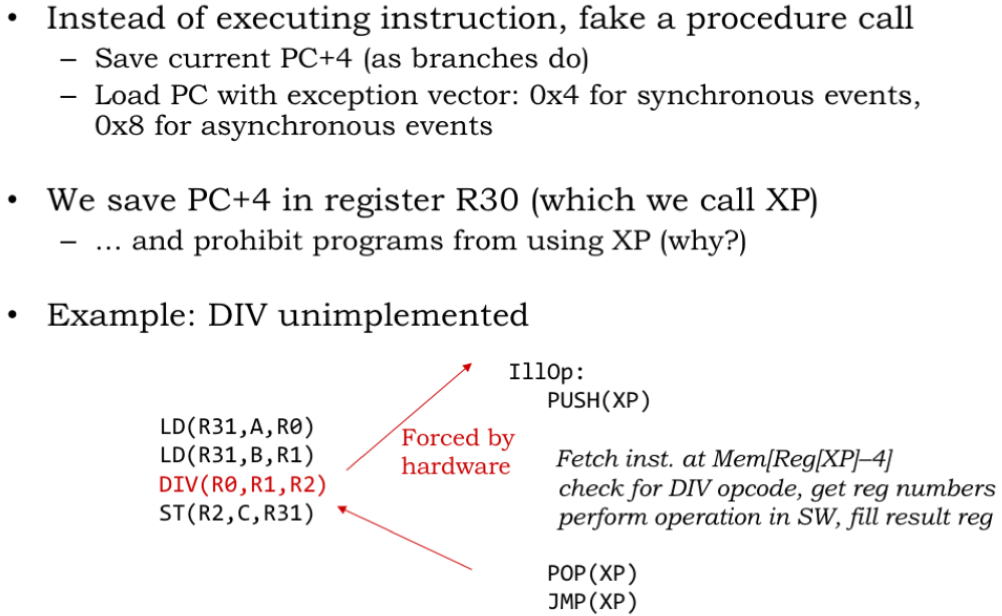
## 异常



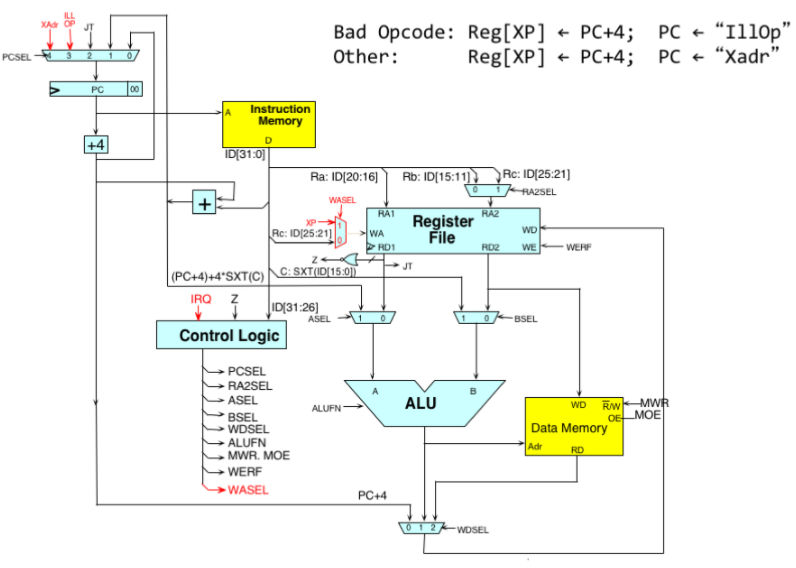
## 异常处理



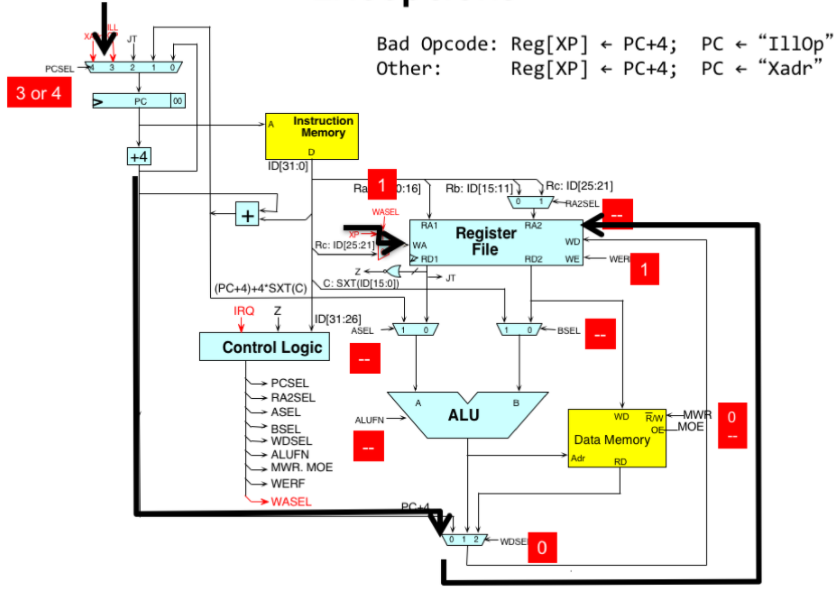
## 异常实现



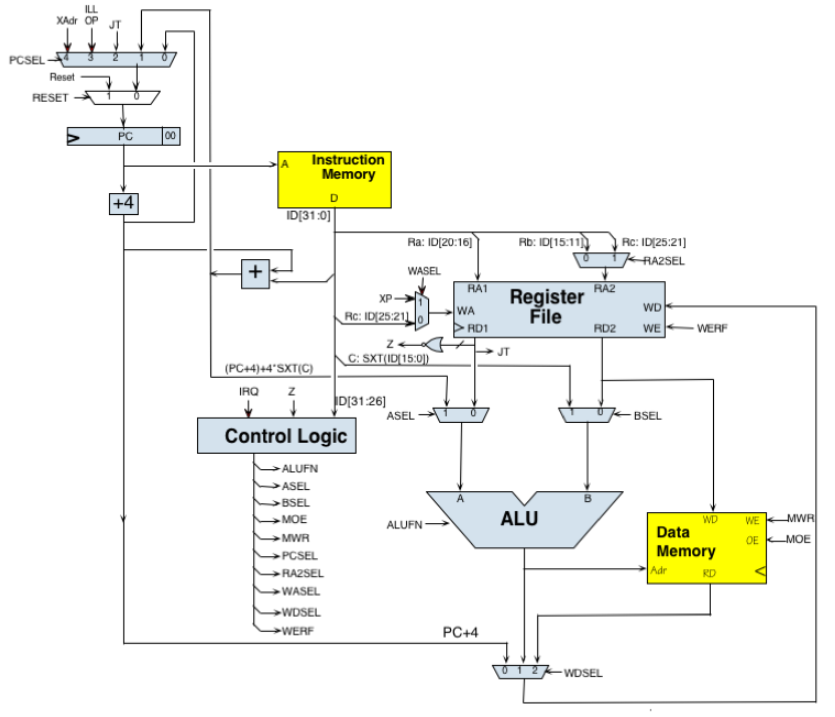
## 异常1



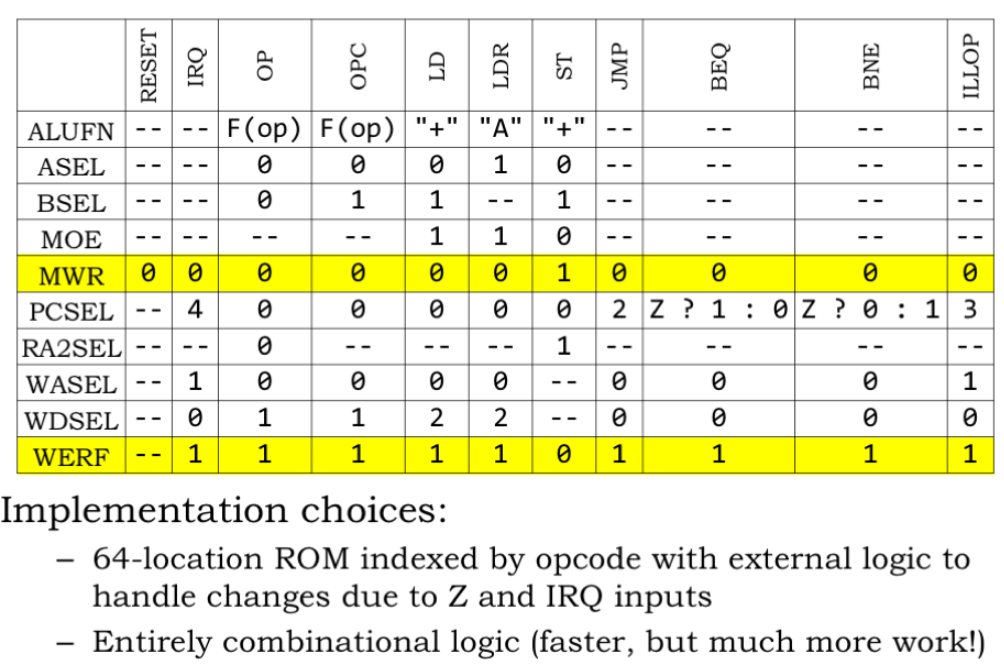
## 异常2



## Beta：我们的最终答案

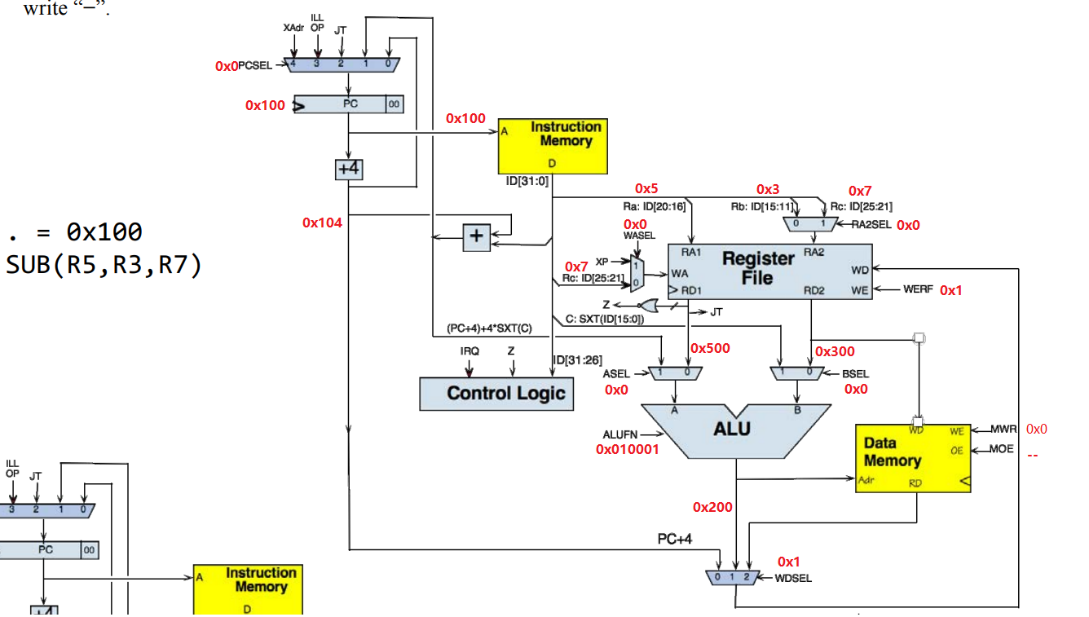


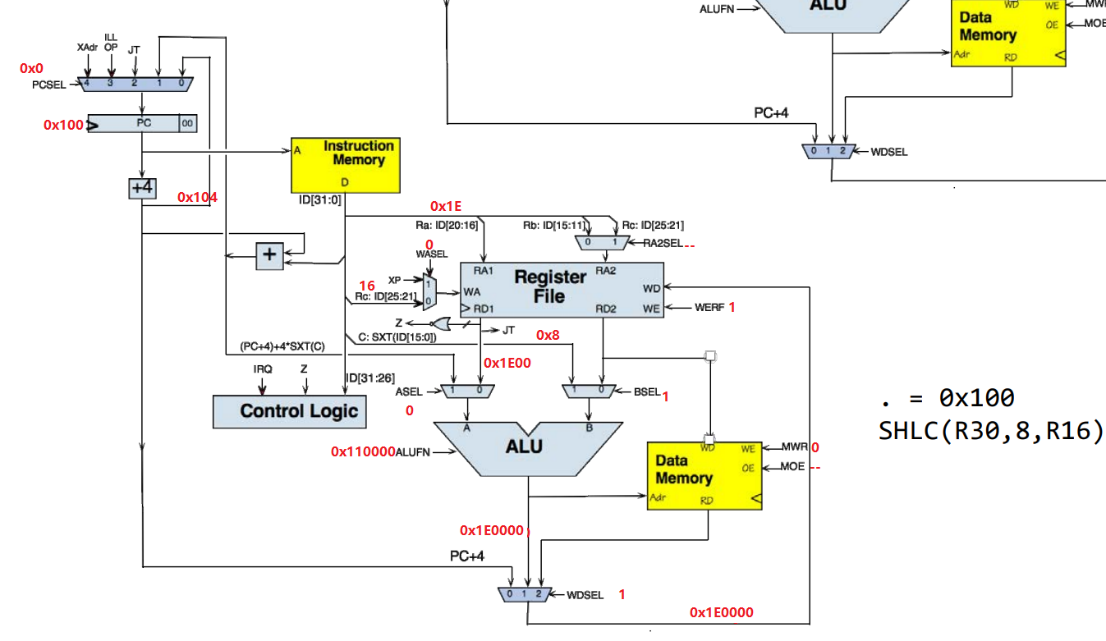
## 控制逻辑

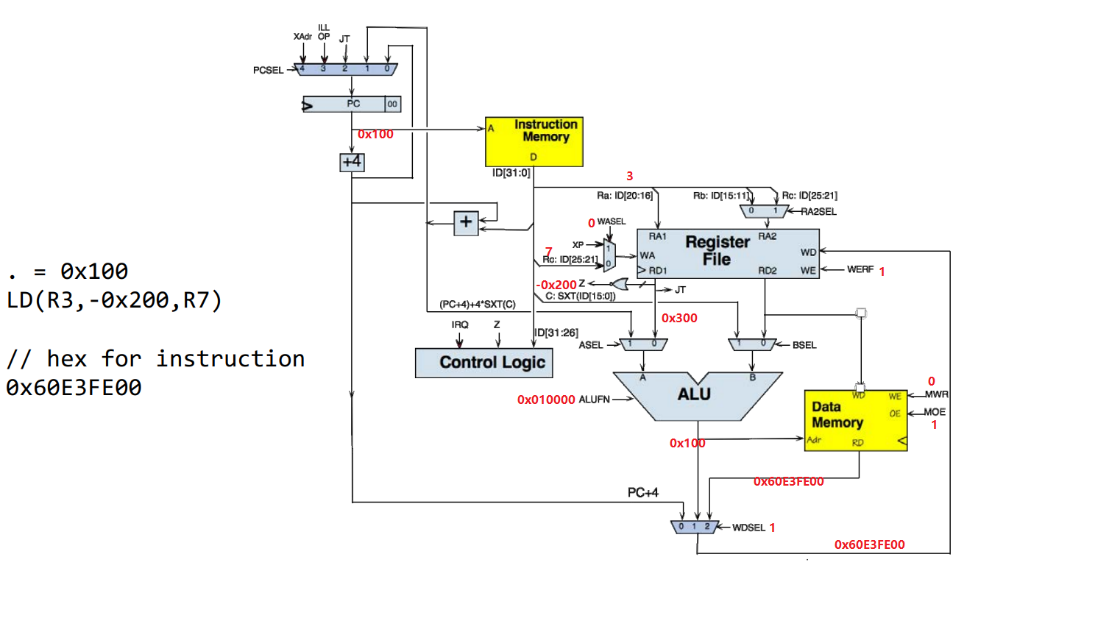


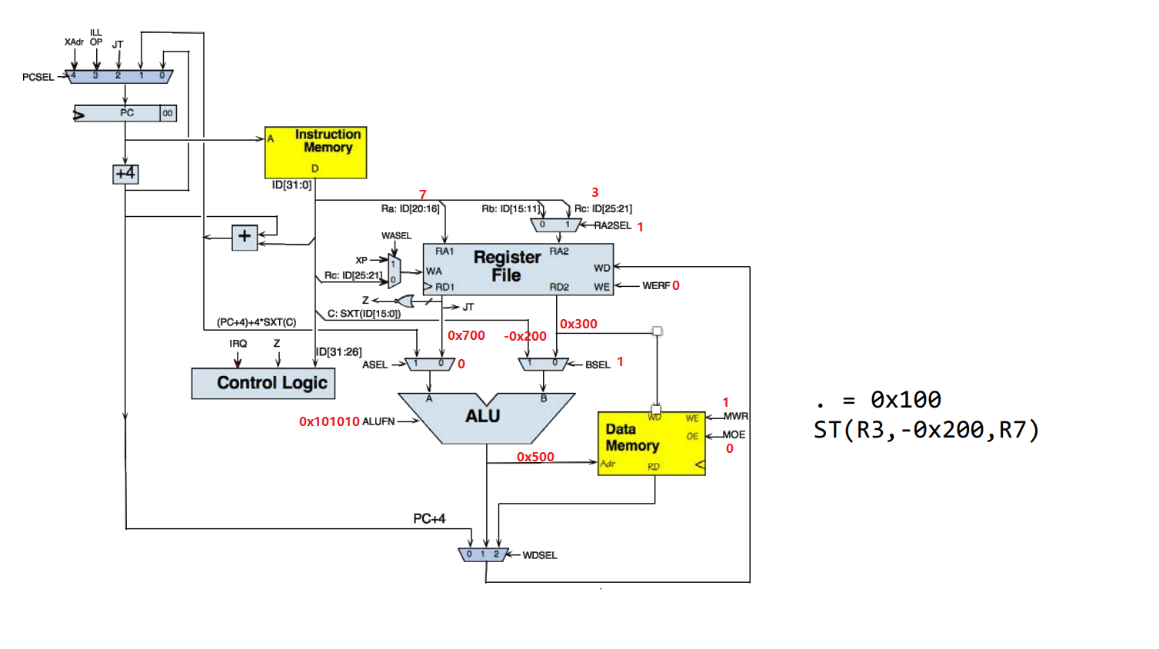
# 问题

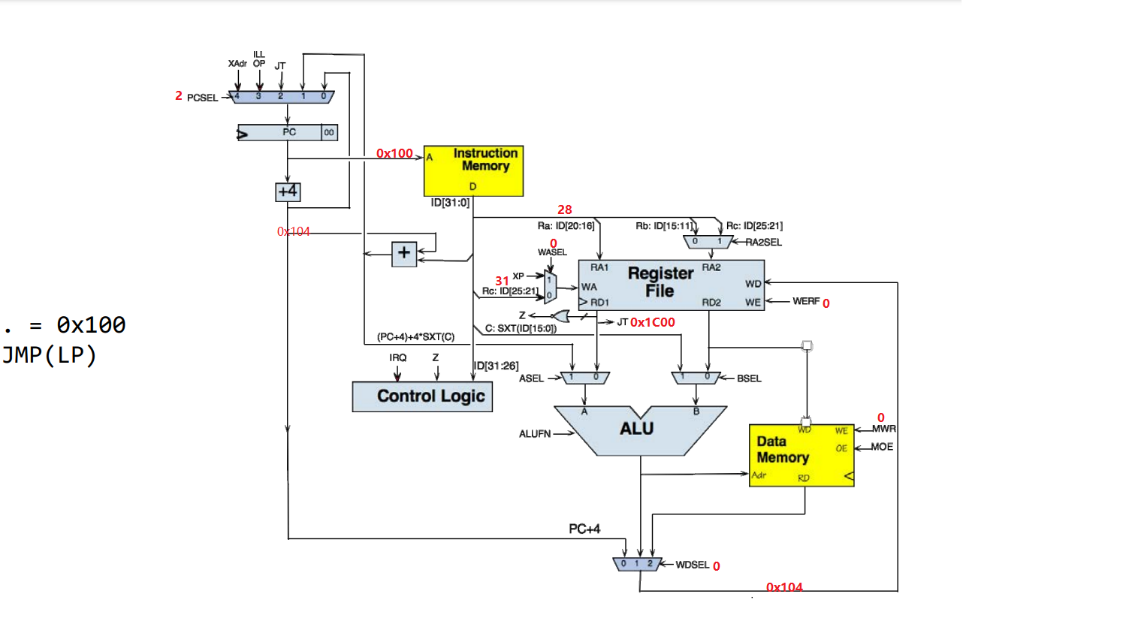
## 问题1

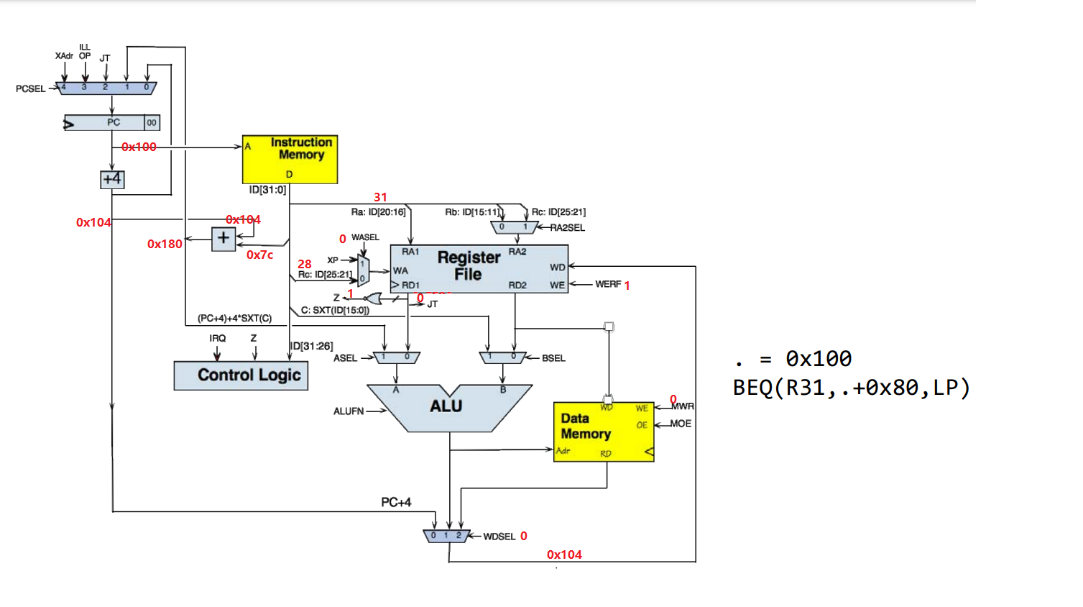




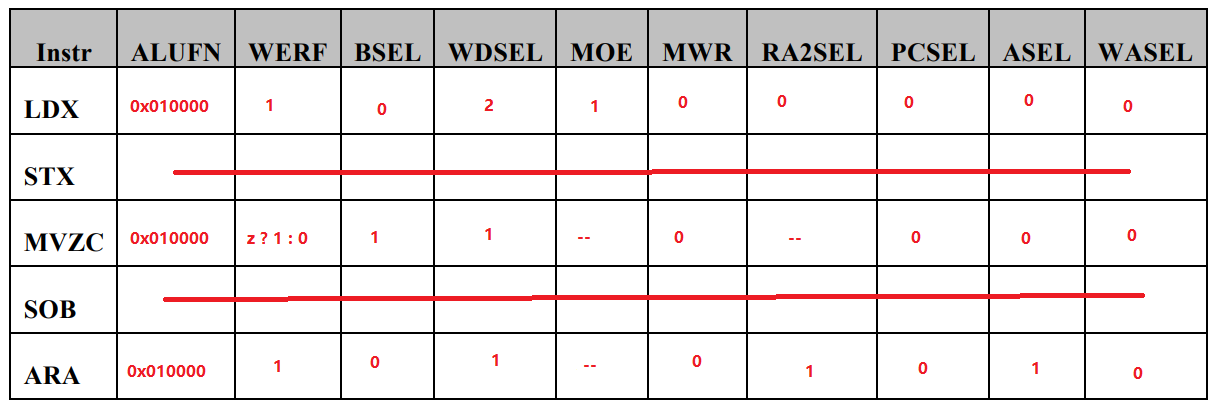








## 问题2



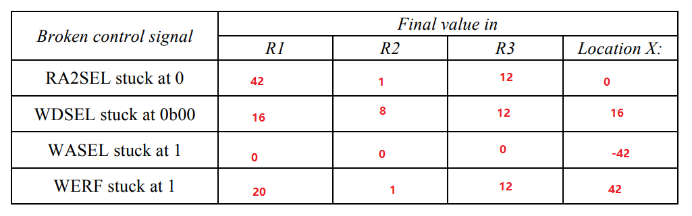
## 问题3

A：0x6003

B：8

C：0x6005

## 问题4



## 问题5

A：1

B：none

C：3、4

D：1

## 问题6

A：0x87654321、0x1、0xC

B：0x10、0x8、0xC

C：A、1、--、2、1、0、0、0、0