

20. For the final step, we modified our circuit from step 19 to appear as shown below in figure 12. We then checked each combination of selector inputs and recorded the outputs in the table below.

C	B	A	Output
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	0

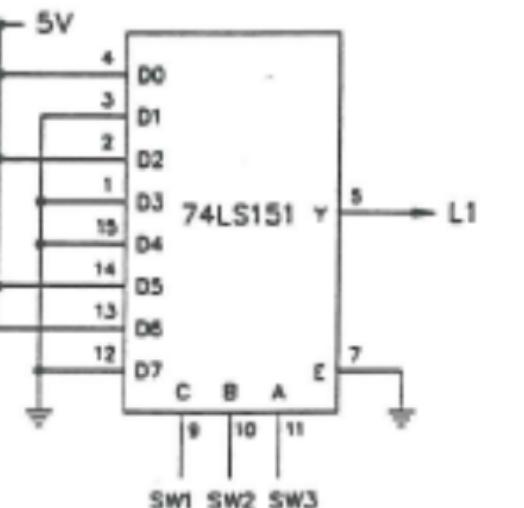


Fig 12

Discussion: Our experiments went smoothly and no errors are expected. Any errors would most likely be a result of small mistakes in wiring circuits or issues with wire connections.

Conclusion: In this experiment we gained practice with basic elements of memory and how they can be implemented. The subtle differences in the operation of different flip-flops and latches, such as the master-slave SR flip-flop and the master-slave JK flip-flop, became more clear as we put them into practice. Potential issues that can arise with such operations were also made more

Format Picture

Crop

Top

0 px

Bottom

0 px

Left

0 px

Right

0 px

Rotation

Degree

0°

Alt Text

Description