YESHENG LIANG

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EDUCATION

Tsinghua University   2019 – 2024
B.Eng. in Computer Science and Technology (GPA: 3.75)   Beijing, China

University of California, San Diego   2024 – expected 2026
Master in Computer Science and Engineering   La Jolla, CA, USA

RESEARCH EXPERIENCE

Research Assistant   Aug. 2023 - Jan. 2024
Xtra Computing Group, National University of Singapore   *Advised by* **Prof. Bingsheng He**
- Proposed a novel method Tree-as-a-Prompt to train decision trees by interacting with language models.
- Responsible for proposing ideas, system design, and implementation.

Research Assistant   Jan. 2022 – Mar. 2023
Storage Research Group, Tsinghua University   *Advised by* **Prof. Youyou Lu**
- Explored user-level scheduling methods in hybrid transactional/analytical processing (HTAP) systems.
- Achieved lower latency of AP while maintaining high throughput of TP.
- Responsible for system design and implementation.

Research Assistant   Sept. 2020 – Jan. 2021
Institute of Trustworthy Networks and Systems, Tsinghua University   *Advised by* **Prof. Jiliang Wang**
- Explored indoor positioning approaches with new generations of Wi-Fi NICs capable of Fine Timing Measurement (FTM).
- Responsible for writing a program to interact with NIC drivers, retrieve FTM statistics, and perform positioning.

SELECTED PROJECTS

Whiz Reader An iOS PDF reader that provides convenient interactions for looking up words and translating documents. Garnered 35,000+ purchases on the App Store and received 600+ ratings, with an average of 4.5. [App Store]

Tree Hollow Official iOS client for an unofficial student forum of Tsinghua University. Used by more than 20,000 students from several universities. [App Store]

Museum+ An iOS app utilizing AR to boost museum touring experience. Participated in the official Apple student developer contest in China. [Docs (Chinese)]

MkDocs Blogging Plugin An open-sourced blogging plugin for the static site generator MkDocs. Reached 6,000+ downloads per month on PyPI and got 80+ stars on GitHub. [GitHub]

RISC-V CPU A five-stage pipelined RISC-V CPU implemented with SystemVerilog, running on FPGAs. Supports RISC-V privilege stack including privileged instructions, virtual memory, exceptions, etc., and is capable of running a simple educational operating system (uCore). Receives full marks in the course. [GitHub]

DBMS A built-from-scratch database management system that supports most of the basic SQL and advanced features including indexing and table joins. [GitHub]

Config Service An online code runner for flexible config distribution. Provided by ByteDance, this is a project of the Software Engineering course (team leader, personal grade: A+). [Link]

HONORS AND AWARDS

- Academic Excellence Scholarship, Tsinghua University, 2023.
- Academic Improvement Scholarship, Tsinghua University, 2022.
- Social Work Excellence Scholarship, Tsinghua University, 2020.

SKILLS

Languages	C/C++, Python, Rust, Go, Swift, JavaScript, Bash, SQL, VHDL, SystemVerilog
Frameworks	UIKit & SwiftUI, React, Django, Flask, Gin
Tools	Git, LaTeX, Docker, Make, CMake, Bazel

Last updated on **October 13, 2024**