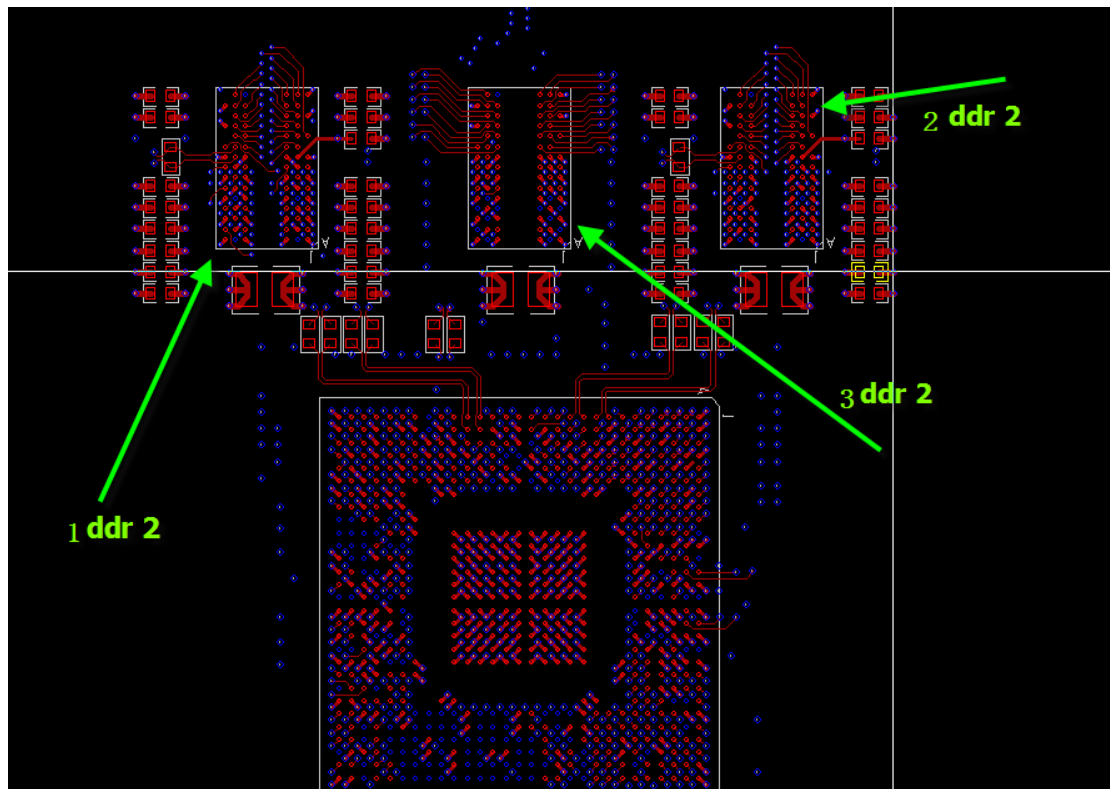
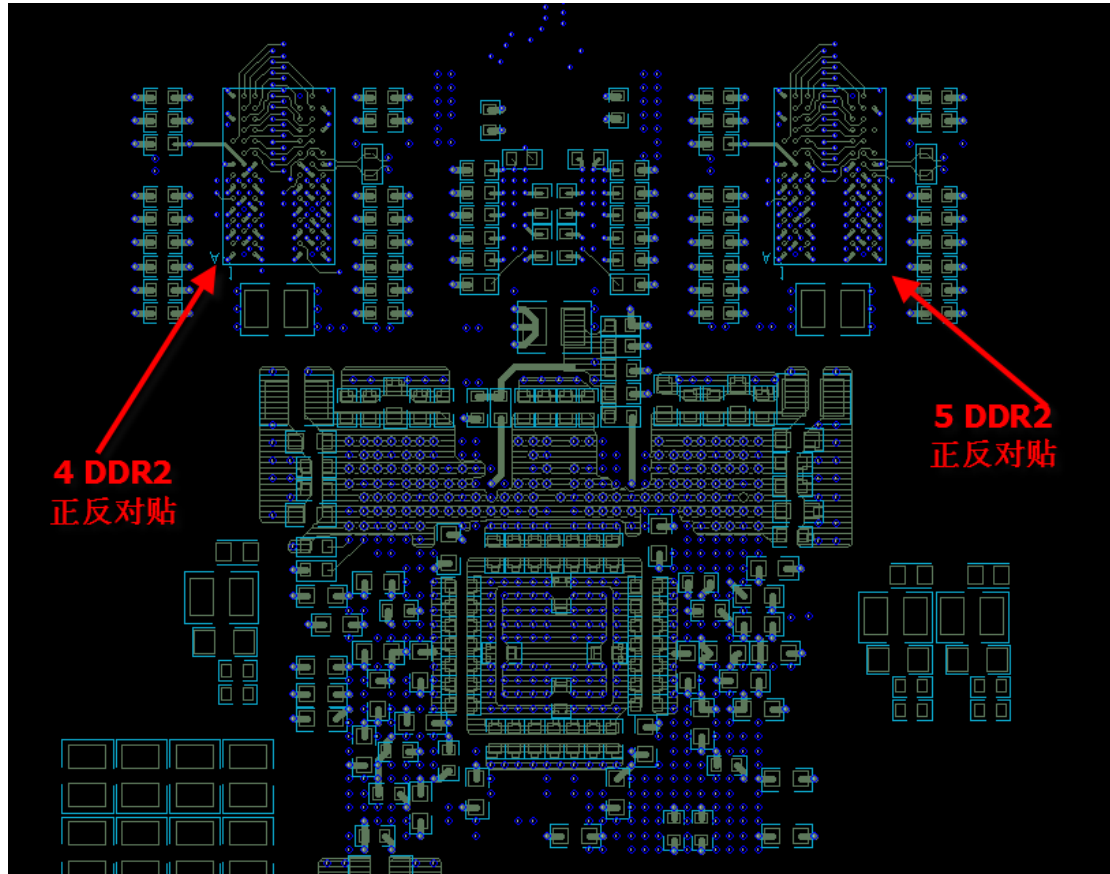


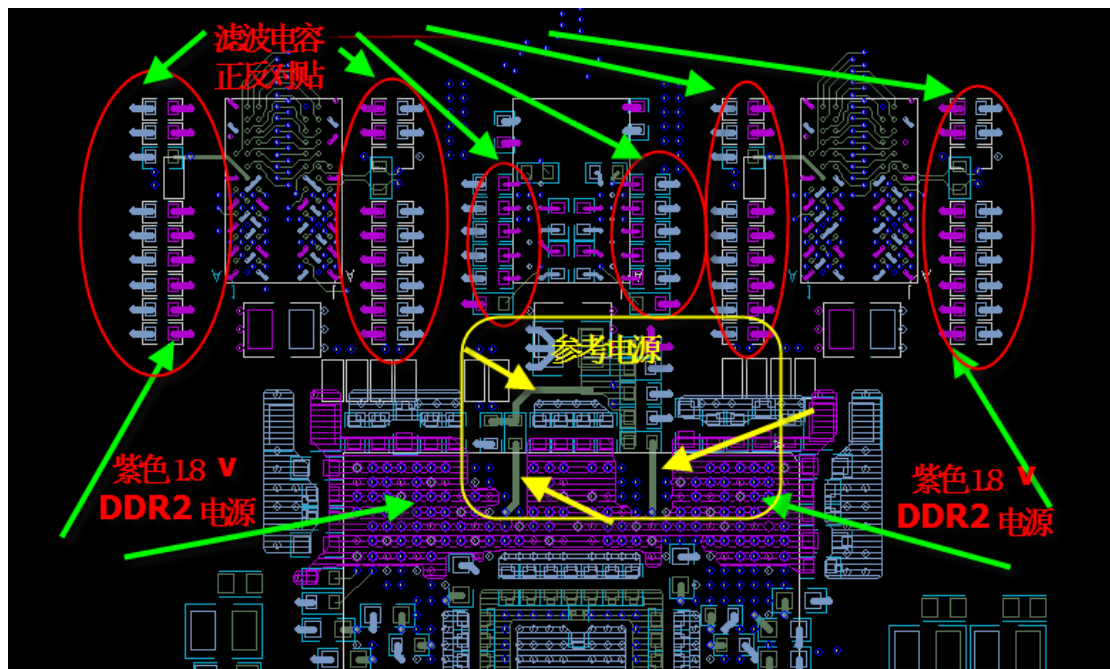
1. TOP 层布局



2. Bottom 布局

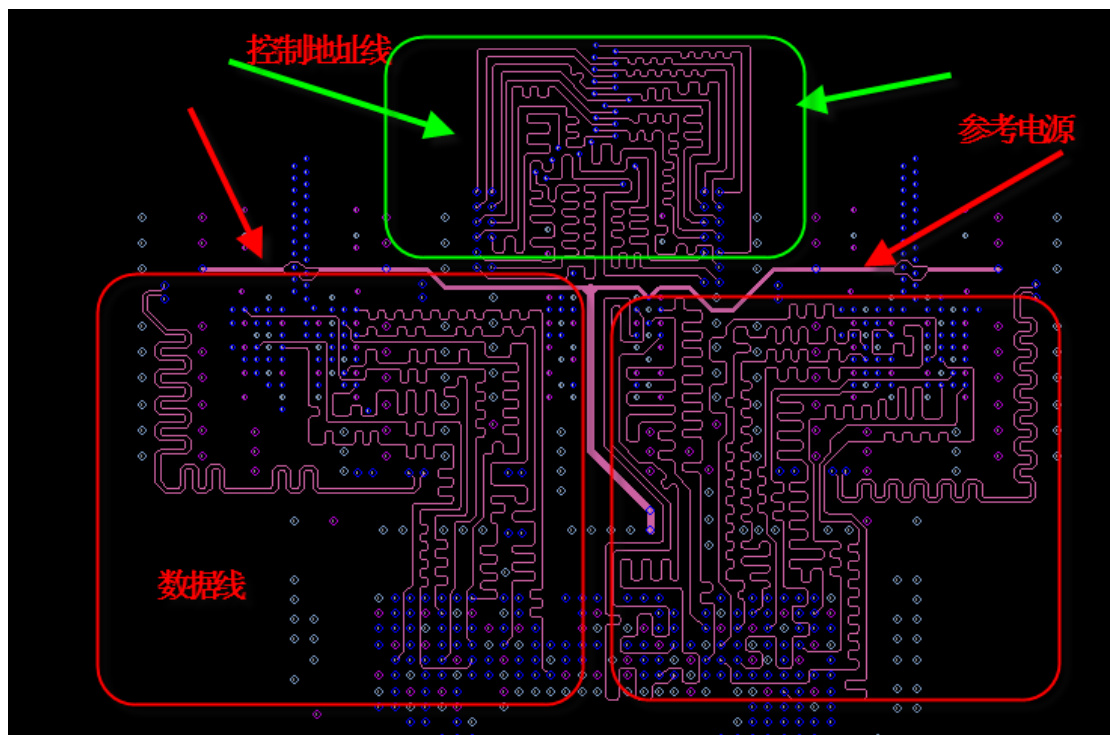


3. 底层滤波电容；

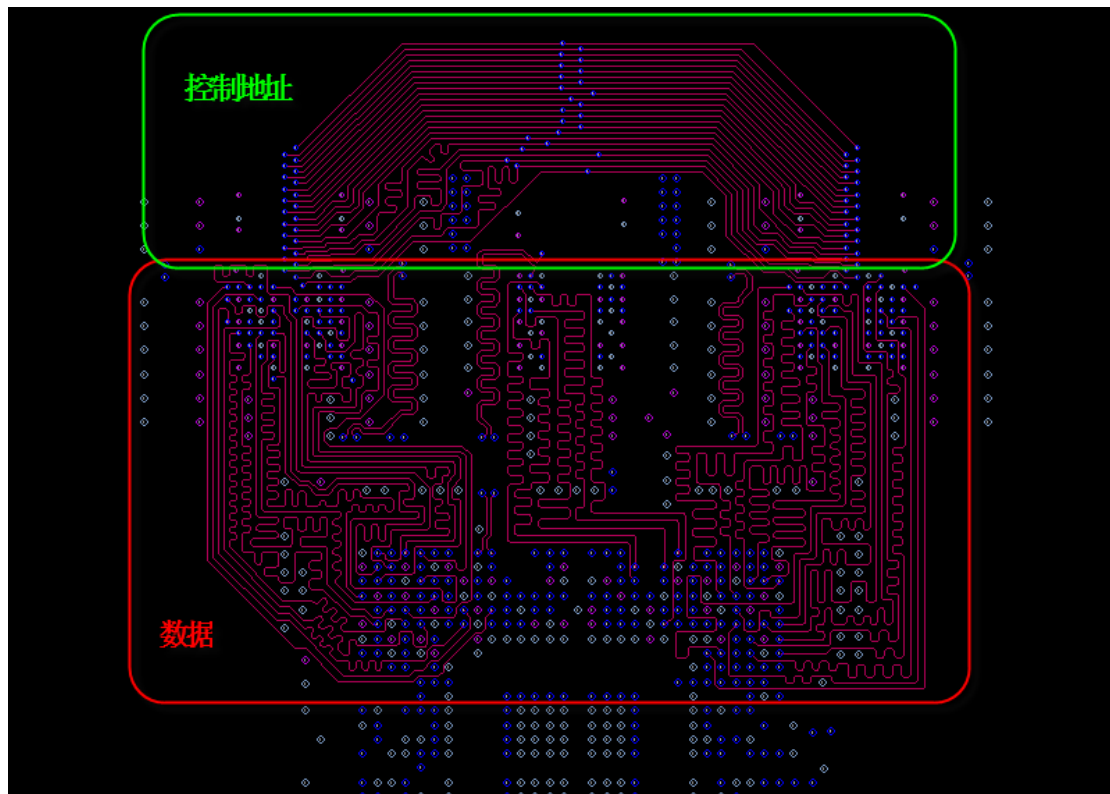


4. Ddr2 64 位要三个信号层才能走完线

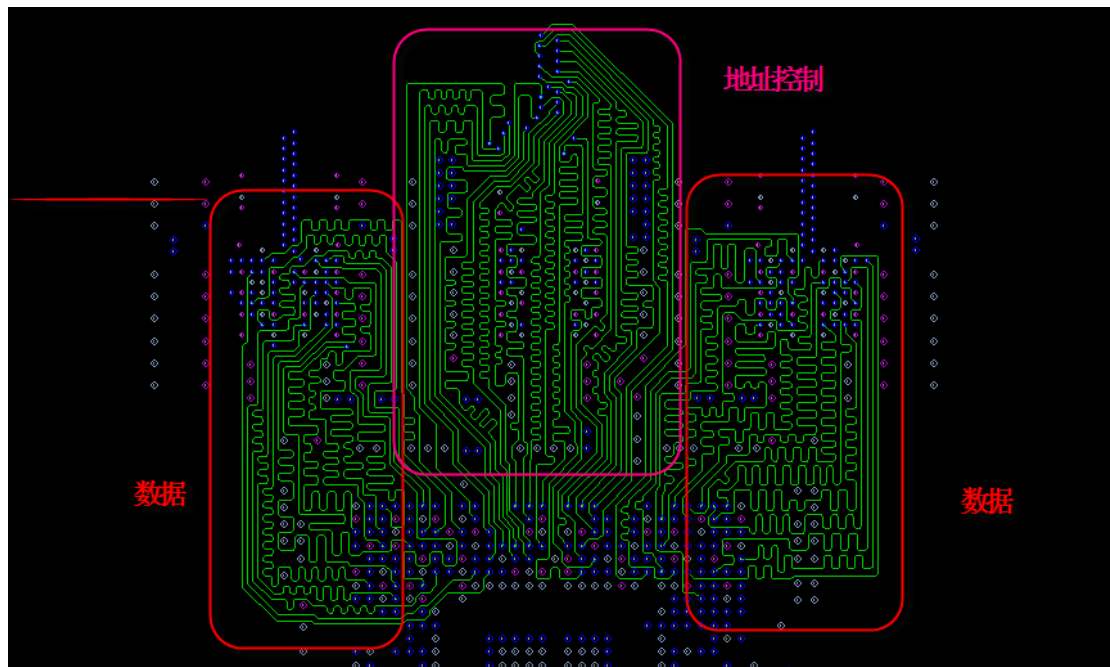
ART03 层



ART05 层



ART08 层



5. 像这样的模块很多，做一次以后遇到直接 COPY，效率很快就上去了！