Annie Li

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Education

Texas A&M University — College Station, TX

Bachelor of Science in Electrical Engineering; Minors in Mathematics & Graphic Design — *May 2027*

- Craig and Galen Brown Engineering Honors
- Relevant Coursework: Digital Systems Design, Computer Architecture, Electronics, Security of Embedded Systems, Machine Learning, Signals and Systems, Microprocessor Systems Design

Experience

Hewlett Packard Enterprise — Houston, TX

Electrical Hardware Engineering Intern — *May 2025 – Aug. 2025*

- Reworked critical component on hardware module by interpreting PCB schematics in **Cadence Allegro**
- Wrote **I2C** scripts to control power sequencing and validate chip functionality during hardware bring-up
- Implemented fault detection logic and scan chain-based bus communication on CPLD with **SystemVerilog**

Secure and Trustworthy Hardware Lab — College Station, TX

Undergraduate Research Assistant — *Mar. 2024 – Present*

- Located **2** system-level vulnerabilities on open-source RISC-V processor design using hardware fuzzing framework
- Designed and implemented **5** hardware vulnerabilities into **Verilog** SoC designs for CTF Hack@DAC 2024

Apple — College Station, TX

Next-Gen Innovators Mentee — *Sep. 2025 – Present*

- Learned and applied **flex PCB** design principles through weekly 1-on-1 mentorship with Apple hardware engineer

Texas A&M Health Science Center — Houston, TX

Research Assistant — *June 2024 – July 2024*

- Developed QRS complex detection algorithm to automate electrocardiographic data analysis with **MATLAB**

Projects

Tritone | *ESP32, C++, Python, WebSockets* — Nov. 2024

- Built IoT system leveraging **ESP32** and **WebSockets** to provide live speech transcription and directionality cues
- Implemented dynamic distance-based audio filtering at hardware level to maintain transcription performance

Traffic Light Controller | *Verilog, Vivado, FPGA* — Apr. 2024

- Designed traffic light FSM that evaluates sensor input and timing conditionals to drive state changes with **Verilog**
- Verified functionality by deploying design onto FPGA using **Xilinx Vivado** with LED output visualization

Activities

TAMUhack — College Station, TX

Creative Lead — *Nov. 2023 – Present*

- Led team of designers in developing strong visual identity and marketing materials for hackathons with **800+** attendees, resulting in a **17%** year-on-year increase in applications from diverse fields in technology
- Designed **3** user-friendly and visually appealing hackathon event websites, drawing **15,000+** visitors annually

Institute of Electrical and Electronics Engineers (IEEE) — College Station, TX

Public Relations Chair — *Aug. 2024 – Present*

- Directed creative strategy and external communication for career and social events throughout the year, including the first IEEE student-led Semiconductor Conference with **500+** attendees

Skills

Programming: C/C++, SystemVerilog, Python, JavaScript, HTML/CSS, ARM64, Linux
Software: Cadence Allegro, Vivado, MATLAB, SolidWorks