

Annie Li

liannie.app | (832) 537-9036 | liannie003@gmail.com

EDUCATION

Texas A&M University

May 2027

Bachelor of Science in Electrical Engineering, Minors in Mathematics & Graphic Design

- Craig and Galen Brown Engineering Honors
- Relevant Coursework: Digital Systems Design, Computer Architecture, Electronics, Security of Embedded Systems, Machine Learning, Signals and Systems

SKILLS

Programming: C/C++, SystemVerilog, Python, JavaScript, HTML/CSS, ARM64, Linux

Softwares: Cadence Allegro, Vivado, MATLAB

EXPERIENCE

Hewlett Packard Enterprise

Houston, TX

Electrical Hardware Engineering Intern

May 2025 – August 2025

- Reworked critical component on hardware module by interpreting PCB schematics in Cadence Allegro and performing precision soldering, followed by power sequencing and functionality scripting over I2C
- Implemented fault detection logic and scan chain-based bus communication on CPLD with **SystemVerilog**

Secure and Trustworthy Hardware Lab

College Station, TX

Undergraduate Research Assistant

March 2024 – Present

- Located system-level vulnerabilities on open-source RISC-V processor design using hardware fuzzing framework
- Designed and implemented hardware vulnerabilities into **Verilog** SoC designs for competition Hack@DAC 2024

Apple

College Station, TX

Next-Gen Innovators Mentee

September 2025 – Present

- Learned and applied **flex PCB** design principles through weekly 1-on-1 mentorship with Apple hardware engineer

Texas A&M Health Science Center

Houston, TX

Research Assistant

June 2024 – July 2024

- Developed QRS complex detection algorithm to automate electrocardiographic data analysis with **MATLAB**

PROJECTS

Tritone

November 2024

- Built IoT system leveraging **ESP32** and WebSockets to provide real-time speech transcription and speaker directionality indicators with **C++** and **Python**
- Implemented dynamic distance-based audio filtering at the hardware level to maintain transcription performance

Traffic Light Controller

April 2024

- Designed traffic light state machine to recognize sensor input and react to timing conditionals with **Verilog**
- Verified functionality by implementing design onto an FPGA board, utilizing Xilinx **Vivado** synthesis and LEDs to display output

ACTIVITIES

TAMUhack

November 2023 – Present

Creative Lead

- Led team of designers in establishing strong visual identity and marketing materials for hackathons with **800+** attendees, resulting in a **17%** year-on-year increase in applications from diverse fields in technology
- Designed **3** user-friendly and visually appealing hackathon event websites, drawing **15,000+** visitors annually

Institute of Electrical and Electronics Engineers

August 2024 – Present

Public Relations Chair

- Directed creative strategy and external communication for career and social events throughout the year, including the first IEEE student-led Semiconductor Conference with **500+** attendees