Quiz 06

Thursday, 20 October 2022 11:32

Which of the following is a benefit of virtual memory support?

Which of the following is a benefit of virtual memory support?

- a. Processes will have faster response time.
- b. Degree of multiprogramming is increased.
- c. More I/O is needed to load each user program into memory.

Which of the following is a benefit of virtual memory support?

- a. More I/O is needed to load each user program into memory.
- b. Logical address space of a process can be much larger than available memory.
- c. Degree of multiprogramming is decreased.

On a system with demand-paging, a process will experience a high page fault rate when

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- a. the process begins execution.
- b. the FIFO page replacement is used.
- c. the capacity of backing store is too small.

On a system with demand-paging, a process will experience a high page fault rate when

- a. the capacity of backing store is too small.
- b. the number of frames allocated to the process is not enough for its current working set.
- c. the FIFO page replacement is used.

Which of the following statement about Belady's anomaly is true?

Which of the following statement about Belady's anomaly is true?

- a. Because of Belady's anomaly, giving more memory to a process will improve its performance.
- b. Belady's anomaly states that for some page replacement algorithms, the page-fault rate may increase as the number of allocated frames increases.
- c. Belady's anomaly indicates that as the number of allocated frames increases, the page-fault rate may decrease for all page replacement algorithms.

Which of the following statement correctly states the Belady's anomaly?

- a. Giving more memory to a process will improve its performance.
- b. For some page replacement algorithms, the page-fault rate may increase as the number of allocated frames increases.
- c. As the number of allocated frames increases, the page-fault rate may decrease for all page replacement algorithms.

Which of the following statements about page replacement is not true?

Which of the following statements about page replacement is not true?

- a. When a page is first loaded into the memory, its reference bit is initialized to 0. When a page in the memory is referenced again, its reference bit is set to 1.
- b. If we have a reference to page p, then any references to page p that immediately follow will cause a page fault.
- c. When there is a page fault and memory is full, two page transfers (one for the page-out and one for page-in) might be required.

Which of the following statements about page replacement is not true?

- a. When a page is first loaded into the memory, its reference bit is set to 1. When a page in the memory is referenced again, its reference bit is cleared to 0.
- b. When there is a page fault and memory is full, two page transfers (one for the page-out and one for page-in) might be required.
- c. If we have a reference to page p, then any references to page p that immediately follow will never cause a page fault.

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Which of the following actions will alleviate thrashing situation?

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- a. Installing a fast CPU
- b. Enlarging the size of paging disk
- c. Enlarging memory

Which of the following actions will alleviate thrashing situation?

- a. Enlarging the size of paging disk
- b. Installing a fast CPU
- c. Swapping out some processes