The ABCs of GPU

- 1. Why is GPU so popular?
- 2. What is GPU
- 3. History of GPU
- 4. Linux Graphic
- 5. Cutting Edge Techs

 You may not hear GPU in 2018, but you definity heard of below:

Bitcoin



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AI/Big-Data/Deep-learning



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Al/Big-Data/Deep-learning

Player Unknown's Battlegrounds



One thing in common: A very good GPU cards!



Dell Price: \$19,508.16 Amazon Entry level price: \$8399 HP Price: \$18,782.75

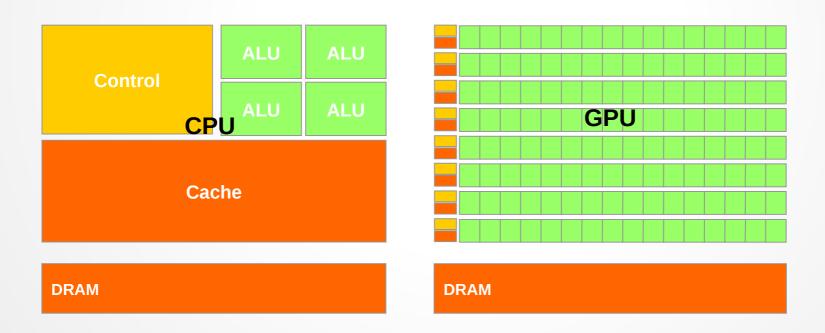
What is GPU

GPU: graphics processing unit a RISC specialized processor

What does it do?

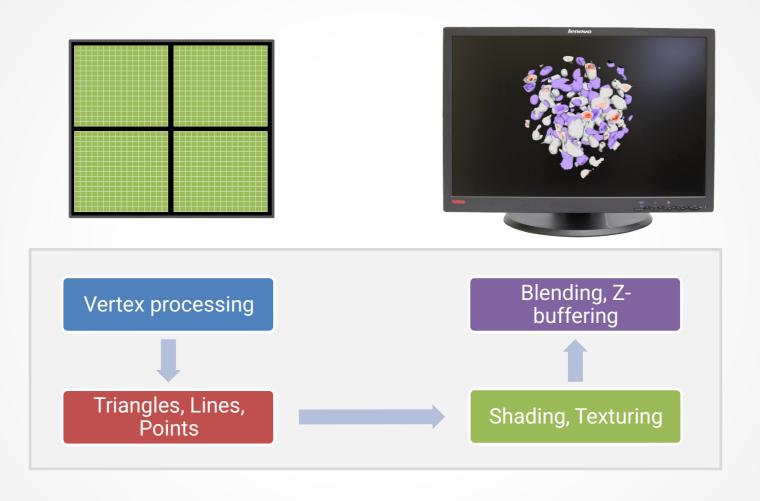
- 1. offloads 3D graphics rendering from microprocessor
- 2. General Purpose: no graphic outpu

Difference with CPU?



What GPU does

Traditional GPU workflow



SM **L0 Instruction Cache L0 Instruction Cache** Warp Scheduler (32 thread/clk Warp Scheduler (32 thread/clk) Dispatch Unit (32 thread/clk) Dispatch Unit (32 thread/clk) Register File (16,384 x 32-bit) Register File (16,384 x 32-bit) INT INT FP32 FP32 FP64 FP64 INT INT FP32 FP32 FP32 FP32 FP64 INT INT FP64 INT FP32 FP32 INT INT FP32 FP32 INT INT FP64 FP64 FP32 FP32 FP64 INT INT FP32 FP32 FP64 INT INT FP32 FP32 **TENSOR TENSOR** TENSOR **TENSOR** CORE CORE CORE CORE FP64 INT INT FP32 FP32 FP64 INT INT FP32 FP32 FP32 FP32 FP64 INT INT FP64 INT INT FP32 FP32 FP64 INT FP32 FP32 INT FP64 INT FP32 FP32 FP64 FP32 FP32 FP32 FP32 INT FP64 LD/ SFU ST **L0 Instruction Cache L0 Instruction Cache** Warp Scheduler (32 thread/clk) Warp Scheduler (32 thread/clk) Dispatch Unit (32 thread/clk) Dispatch Unit (32 thread/clk) Register File (16,384 x 32-bit) Register File (16,384 x 32-bit) INT INT INT INT FP64 FP64 FP32 FP32 FP64 INT INT FP32 FP32 FP64 FP32 FP32 FP64 INT INT FP32 FP32 FP64 FP32 FP32 FP64 INT INT FP32 FP32 FP64 INT INT FP32 FP32 TENSOR **TENSOR** TENSOR **TENSOR** CORE CORE CORE CORE FP32 FP32 FP64 INT INT FP64 INT INT FP32 FP32 INT INT INT INT FP32 FP32 FP64 FP64 LD/ SFU SFU ST 128KB L1 Data Cache / Shared Memory Tex Tex Tex Tex

GPU inside

SM 84

64 CUDA SM

8 Tensor

CUDA cores: 5376

Tensor cores: 672

GPU Evolution



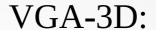
Console



VGA-2D Trident S3 Matrox

GPU Evolution





3dfx 3dlabs PowerVR Nvidia GForce ATI Radeon



GPU: Nvidia AMD Intel

GPU Evolution

- 1980's No GPU. PC used VGA controller
- 1990's Add more function into VGA controller
- 1997 3D acceleration functions:

 Hardware for triangle setup and rasterization
 Texture mapping
 Shading
- 2000 A single chip graphics processor (beginning of GPU term)
- 2005 Massively parallel programmable processors
- 2007 CUDA (Compute Unified Device Architecture)

Graphic in Operating System





Graphic in Operating System



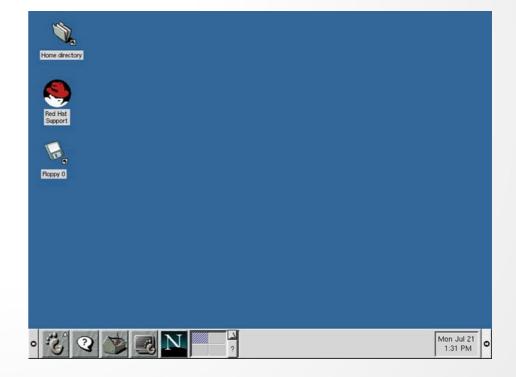
Graphic in Linux



GNOME 1.0 (released in 1999)

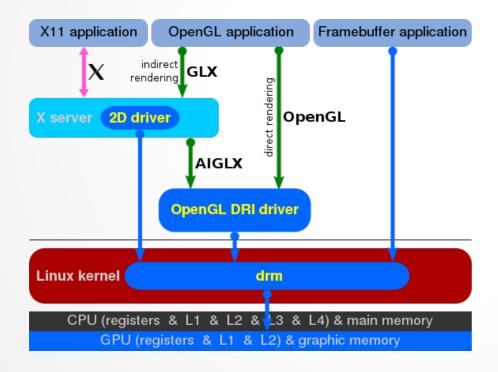
Linux 1.0 (released in 1994)

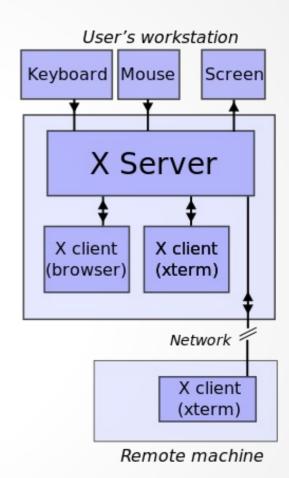
KDE 1.0 (released in 1998)



Xorg in Linux

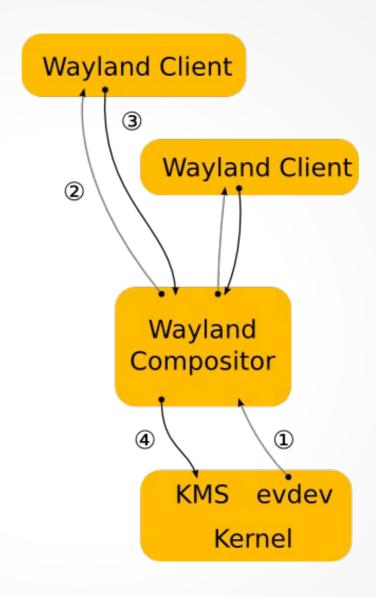






Wayland in Linux

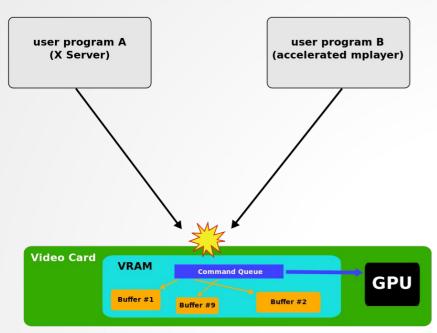




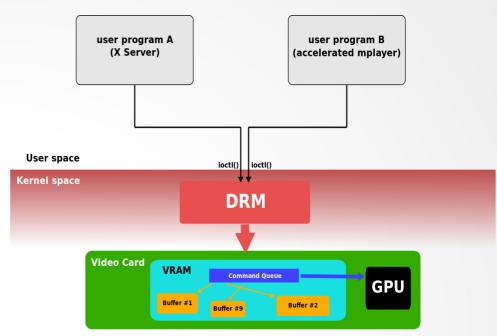
Development in Linux

- 1. Userspace application Xorg, performance is really bad, blank
- 2. DRM/ DRI, Direct Render Manager/Infrastructure It is like frontend of GPU driver, will provide GPU memory management, TTM/GEM KMS: kernel module setting (4.2)

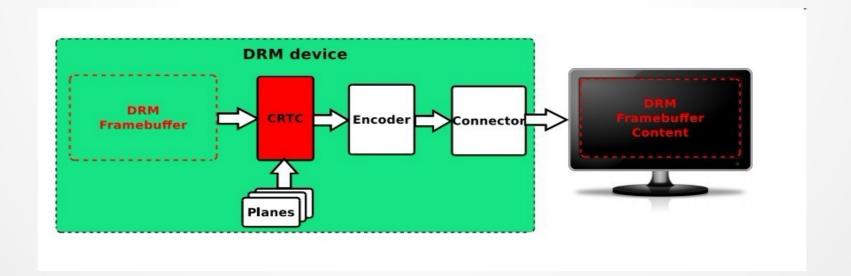
Development in Linux



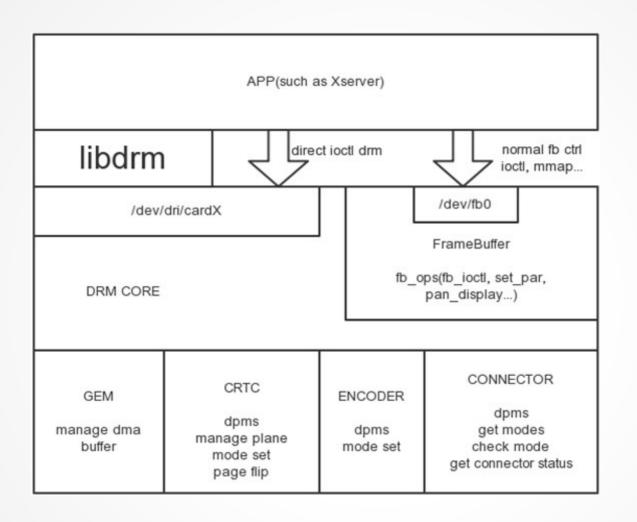
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DRM architecture



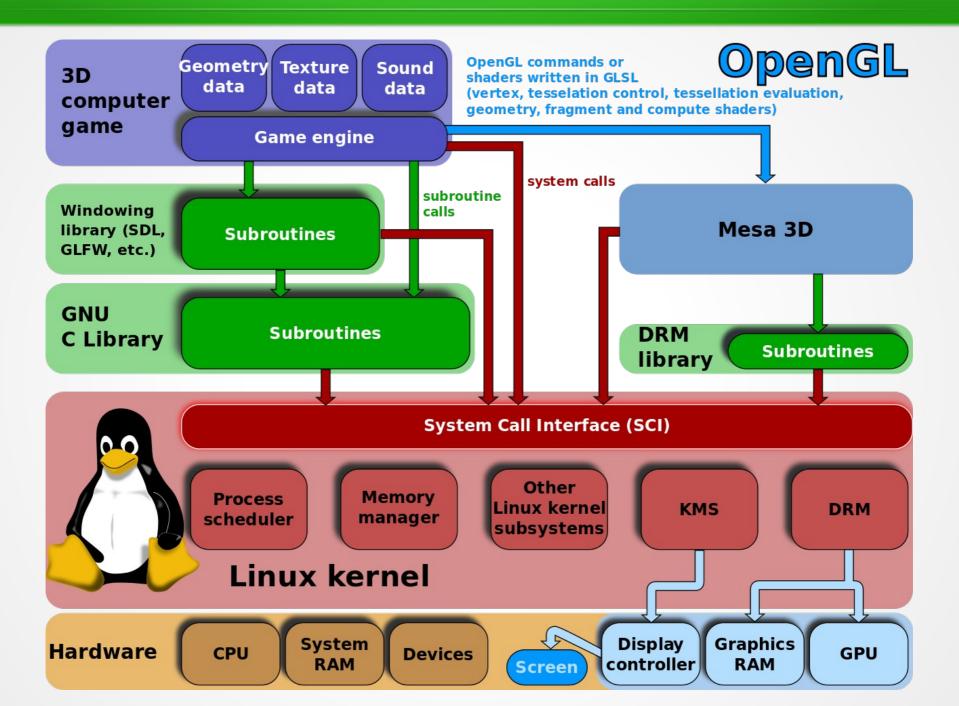
Development in Linux

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- 3. Hardware Acceleration

The idea is driving will provide some APIs for application and let applications access hardware rather than cpu directly

Mesa(openGL)

Development in Linux



Cutting Edge Techs

General compute:

heterogeneous architecture cpu+gpu cpu+fpga cpu+tpu

OpenCL CUDA

Cut-Edge Techs

GPU virtualization

GPU is so expensive, and there are so many cores on it, how could I use it efficiently?

GPU virtualization is Your Answer!

Intel: MDEV software implement

AMD: SRIOV hardware implement

Nvidia: MDEV software implement

GPU virtualization

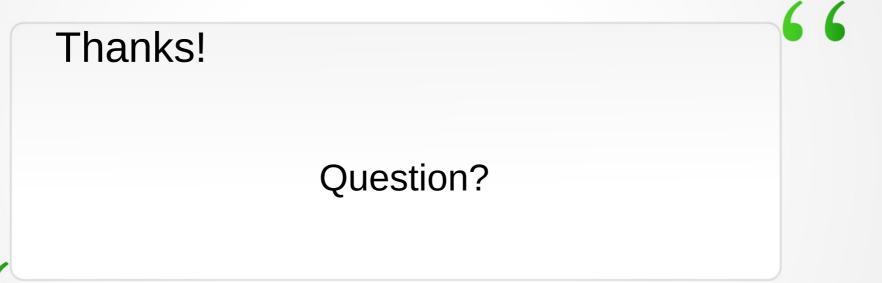
Physical GPU shared among multiple virtual machines

Great performance and suitable for different workload

Full API compatibility – GPU vendor driver inside the virtual machine

Full device visibility to the hypervisor/host -

allows for device-specific features such as dynamically monitoring and tuning performance, detailed error reporting, etc.



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