BENJAMIN **LI**

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Education

Boston University College of Engineering — **Boston, MA**
Bachelor of Science in Computer Engineering — May 2022
Bachelor of Science in Electrical Engineering — May 2022
GPA 3.84/4.0 | Dean's List *(all semesters)*

Experience

Hewlett Packard Enterprise — **Andover, MA**
DevOps/Software Engineering Intern — May 2020 – *Present*

- Completed development and implementation effort of CI/CD roadmap in support of Infosight's Big Data Service, including standing up Jenkins, Artifactory, and Kubernetes
- Spearheaded service delivery migration to container-based model through Kubernetes and automated deployment with Jenkins-Helm integration
- Engineered micro-services for ingestion of raw server statistics from HPE On-Premise collector using Java, Kafka Streams, and shell scripting
- Assisted Junior Dev Team in construction of Ansible suite to automate administration and deployment of CentOS server cluster through vSphere

Boston University Integrated Circuits & Systems Group — **Boston, MA**
Undergraduate Hardware Researcher — Jan 2020 – May 2020

- Implemented basic vector operation capabilities to Blackparrot, a linux-capable accelerator host multi-core CPU, using Verilog as part of a research team.
- Synthesized and quality tested vector components through generated waveforms using Vivado.

Rocket Software — **Waltham, MA**
Software Engineering Intern — Jun 2019 – Dec 2019

- Modernized an IBM Zowe (Mainframe OS) data recovery service to leverage the Java Spring Framework instead of raw servlets for integration with REST API.
- Developed an IBM Zowe infrastructure configuration service using JS and Java in collaboration with full-stack team.

Projects

Raspberry Pi Computing Cluster, Boston University High Performance Computing
10-node Raspberry Pi computing cluster, networked over Ethernet with a single controller/login, and powered with custom wired DC power supplies for voltage regulation. Special attention was given to the thermal design of the system. The cluster is currently being run for protein-folding workloads, and was previously deployed during HPCBoston, a super-computing hackathon. Coordinated club members to design and build cluster, as well as soldered custom DC power supplies for individual nodes.

WikiWhere, Personal — *https://wikiwhere.rciliberto.com/* *wikiwhere/wikiwhere*
Graph-based visualization of hyperlink connectivity among Wikipedia articles. Optimized shortest path algorithm by implementing a multi-threaded, bi-directional, Breadth-First Search of Wikipedia data dumps. Developed with OpenMP, C++ and SQL for application backend, and D3 for frontend graph visualization.

Whack-a-mole, Logic Design — *Whack-a-mole*
Final project for Logic Design class, created as a team. Developed Whack-a-mole game from scratch on the Nexys A7 FPGA using Vivado and Verilog. Delegated tasks and managed Github repository as team leader. Responsible for backend implementation of signal timing for hit registration.

Leadership

President, Boston University High Performance Computing — Apr 2020 – *Present*

Skills

Languages C/C++ | Java | Verilog HDL | Bash | Python
Technologies Electronic Devices | Linux | Git | FPGA | Jenkins | Vivado | OpenMP | Digital Circuits Design