

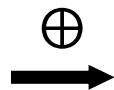
SPiR-V IR with  
instruction count



Tokenization



Token / Positional / Trace  
Embedding



Regression  
Head



Predicted  
Time

$V_{[BOS]}^{output}$

$V_1^{output}$

$V_2^{output}$

$V_3^{output}$

$V_4^{output}$

$V_5^{output}$

$V_6^{output}$

$N \times$  Transformer Encoder

$V_{[BOS]}^{input}$

$V_1^{input}$

$V_2^{input}$

$V_3^{input}$

$V_4^{input}$

$V_5^{input}$

$V_6^{input}$