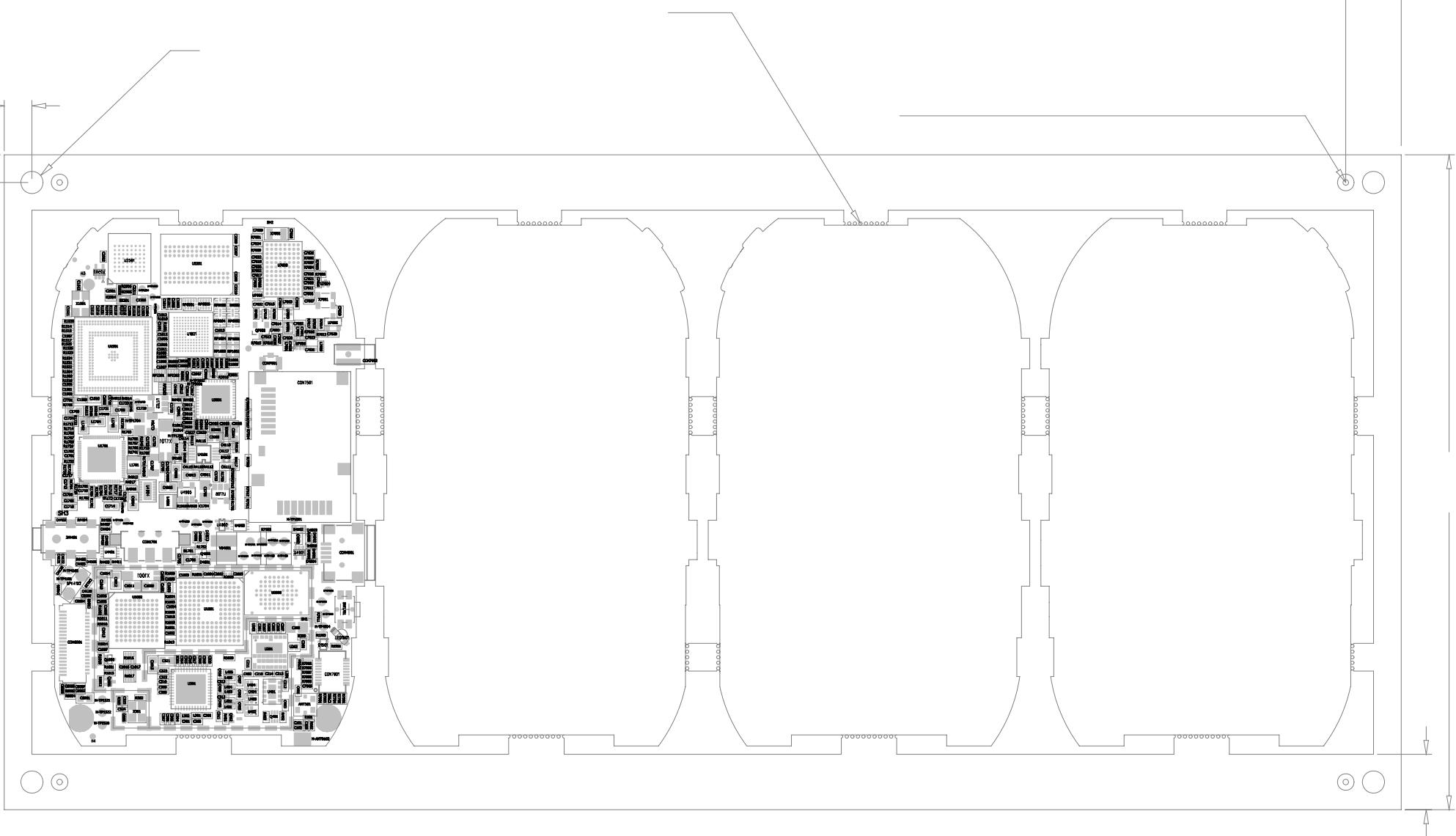
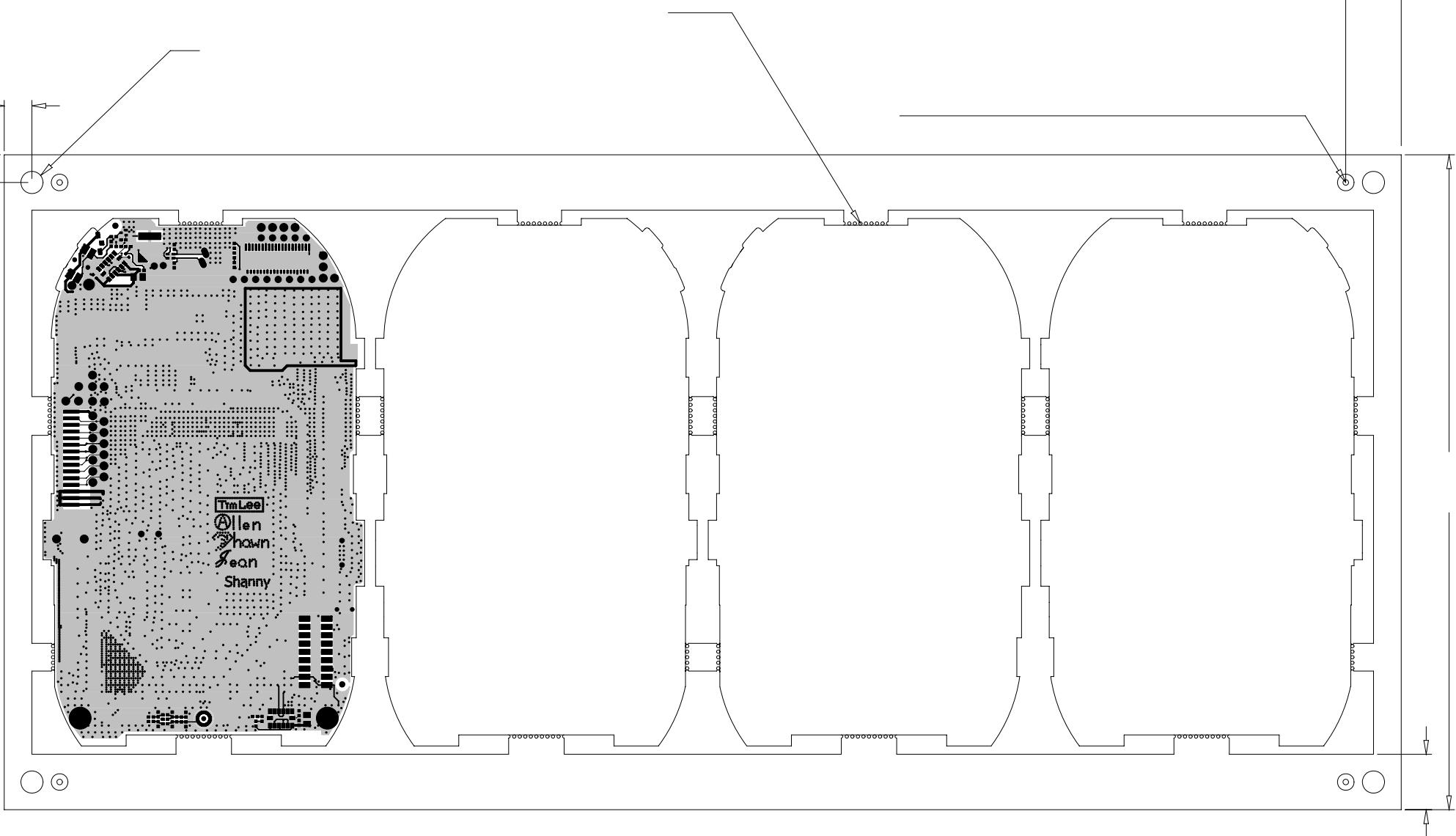


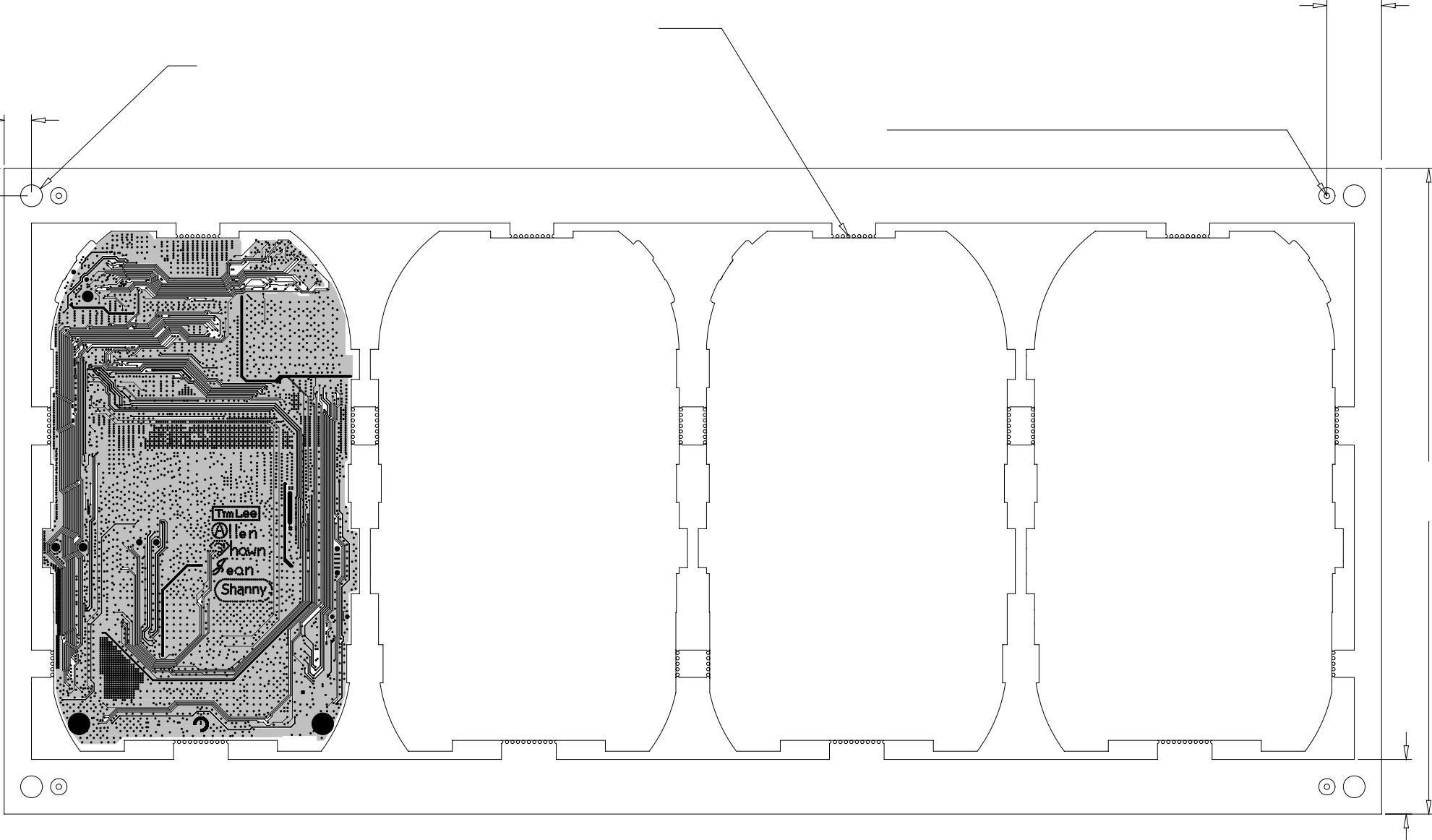
ST	GTA02	8 LAYER
	FIC011	
LAYER: ASSEMBLY TOP		
DATE: 2008-03-03		
Clearance error		8
Connectivity error		0



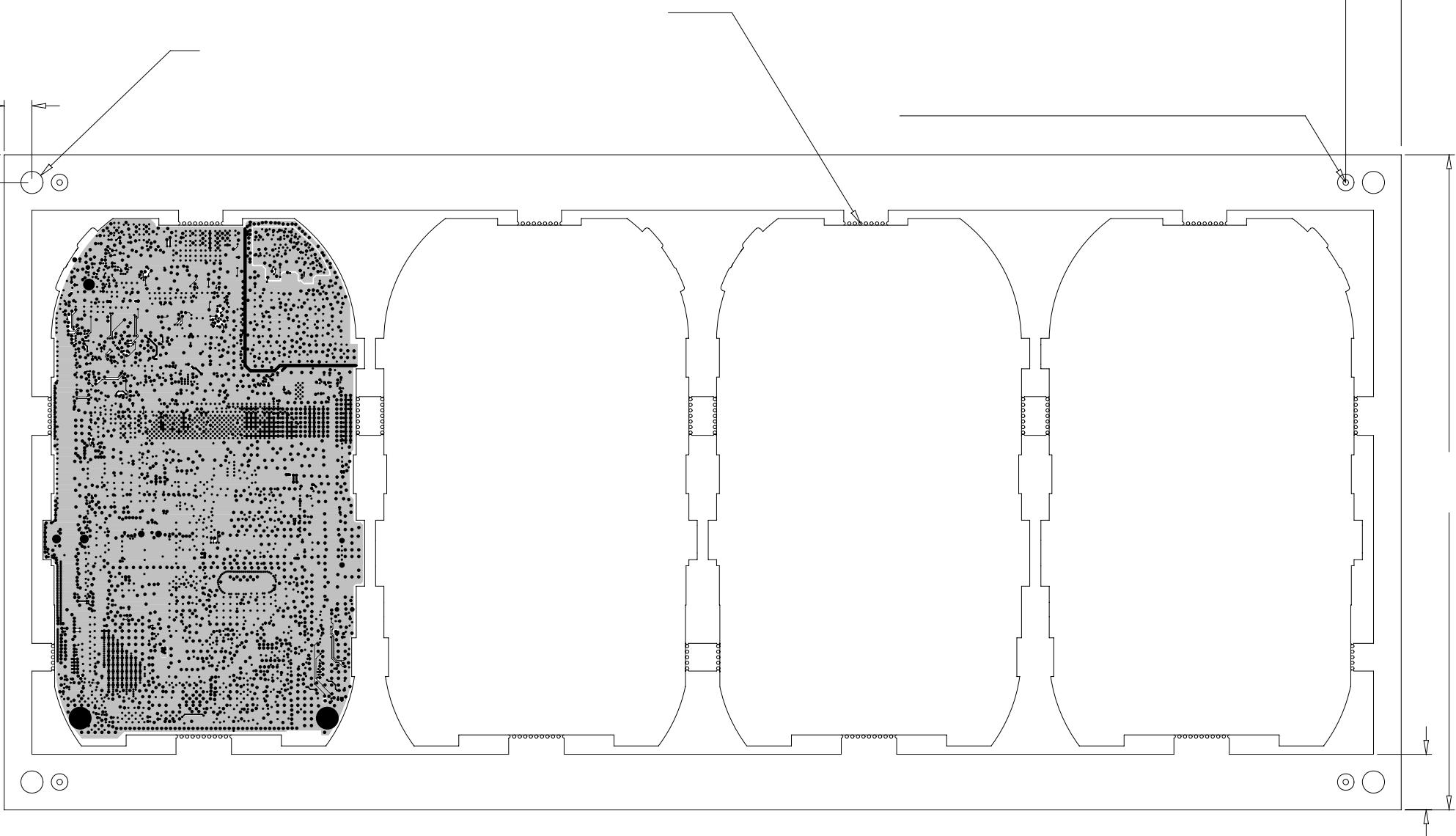
ST	GTA02	8 LAYER
	FIC011	
LAYER:	ASSEMBLY BOT	
DATE:	2008-03-03	
Clearance error		8
Connectivity error		0



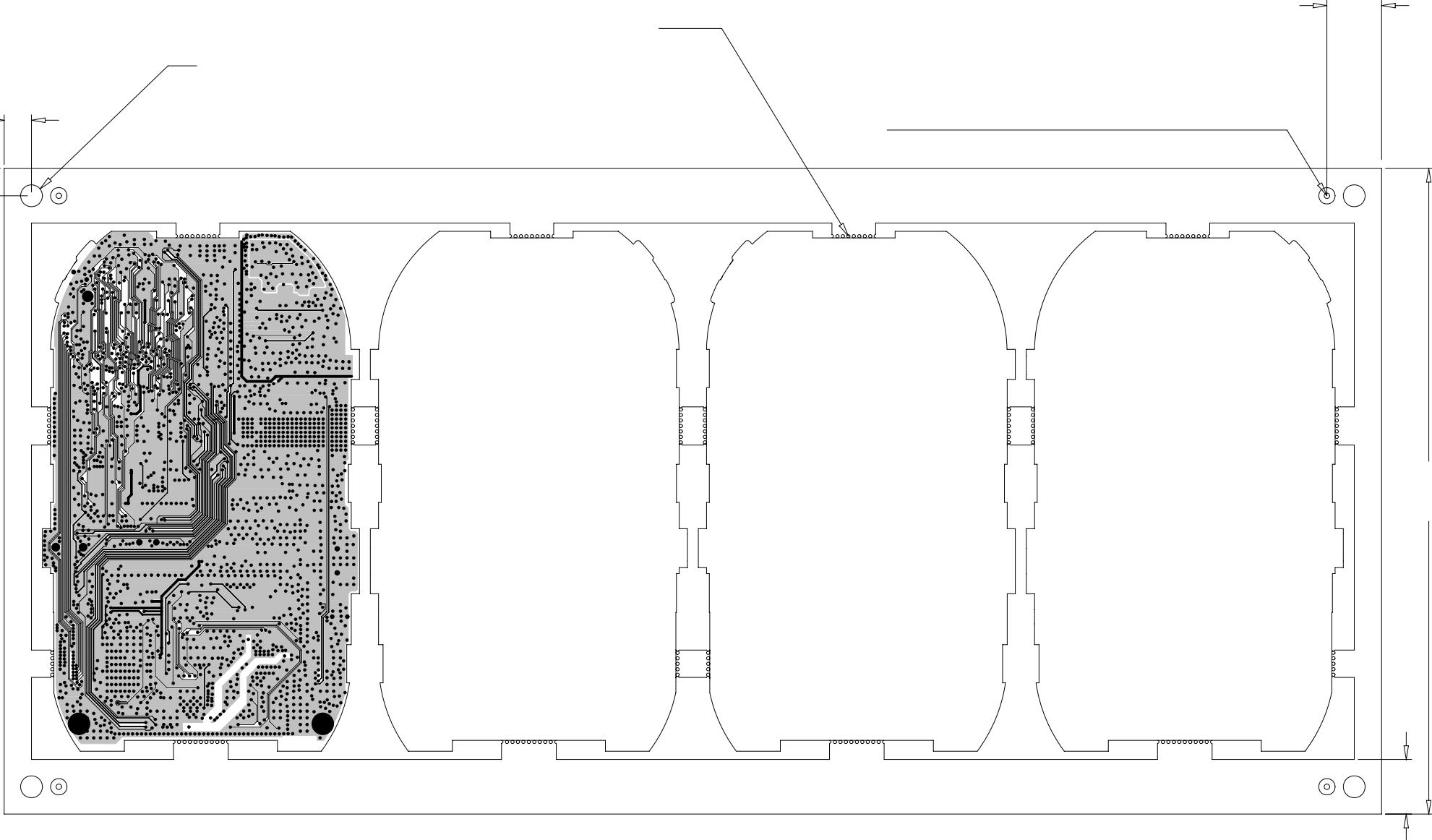
ST	GTA02	8 LAYER
	FIC011	
LAYER: L1 TOP SIDE		
DATE: 2008-03-03		
Clearance error		8
Connectivity error		0



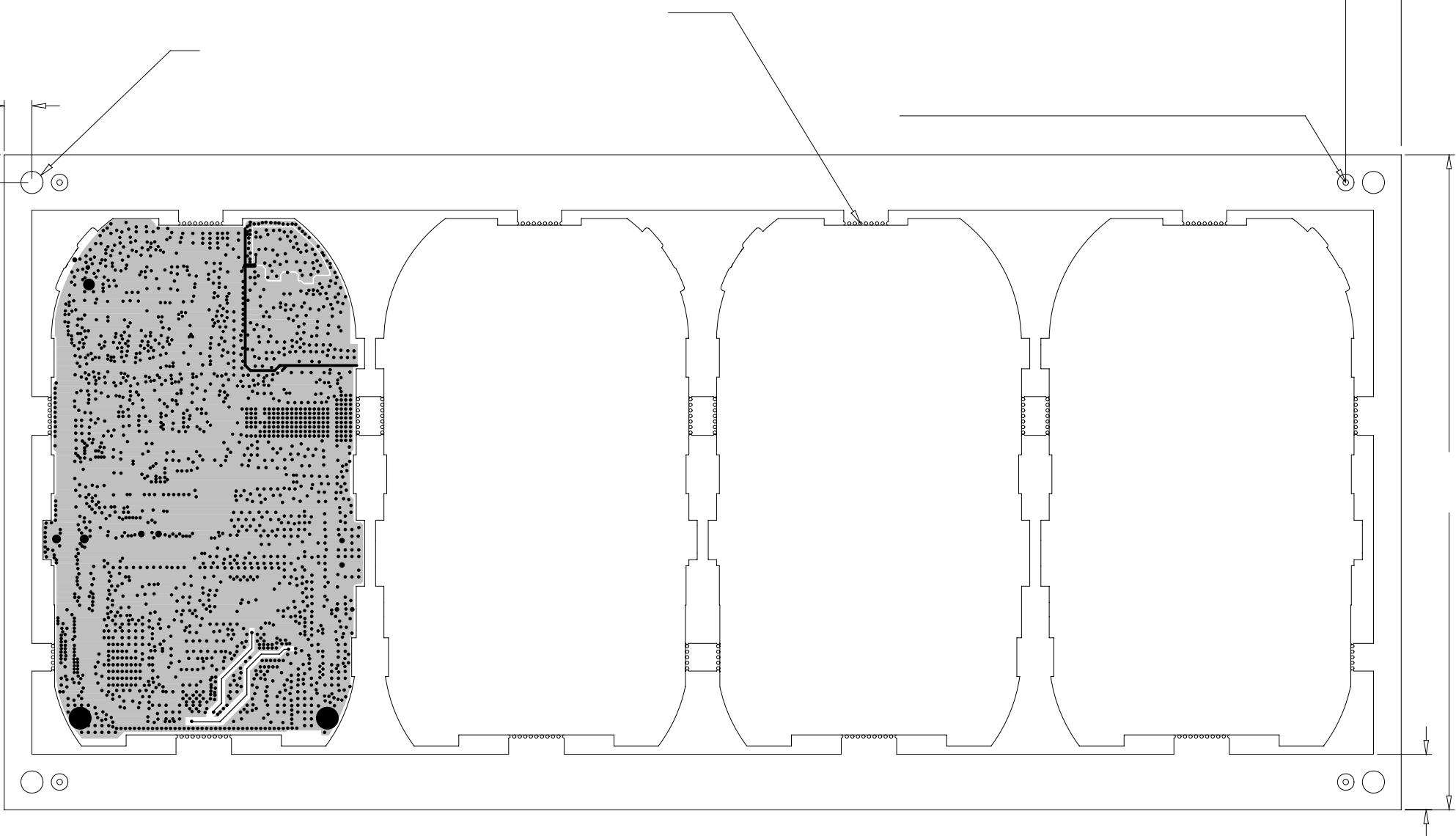
ST	GTA02	8 LAYER
	FIC011	
LAYER: L2 GND(TRK)		
DATE: 2008-03-03		
Clearance error		8
Connectivity error		0



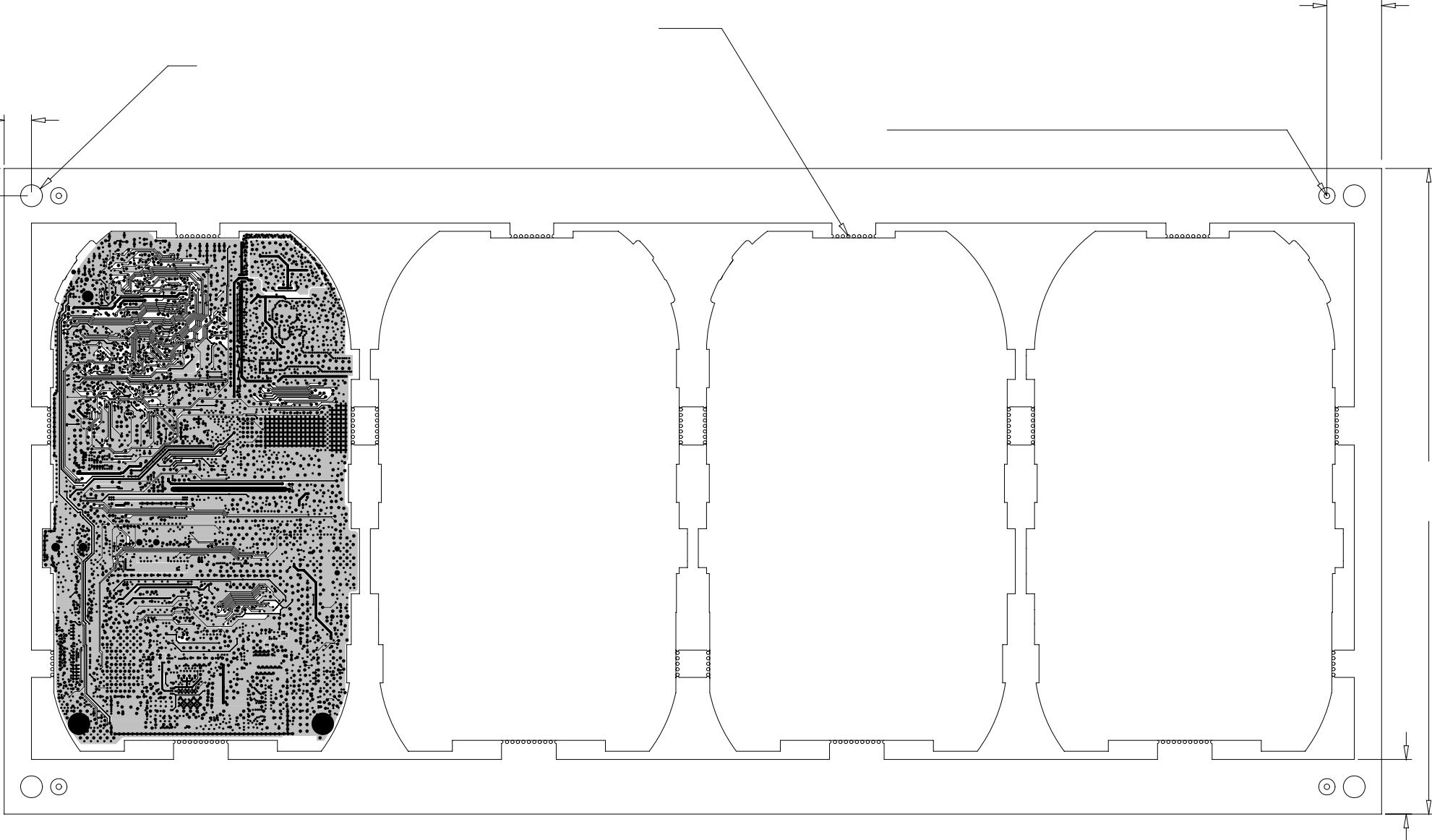
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L3 GND(TRK)	
DATE:	2008-03-03	
Clearance error		8
Connectivity error		0



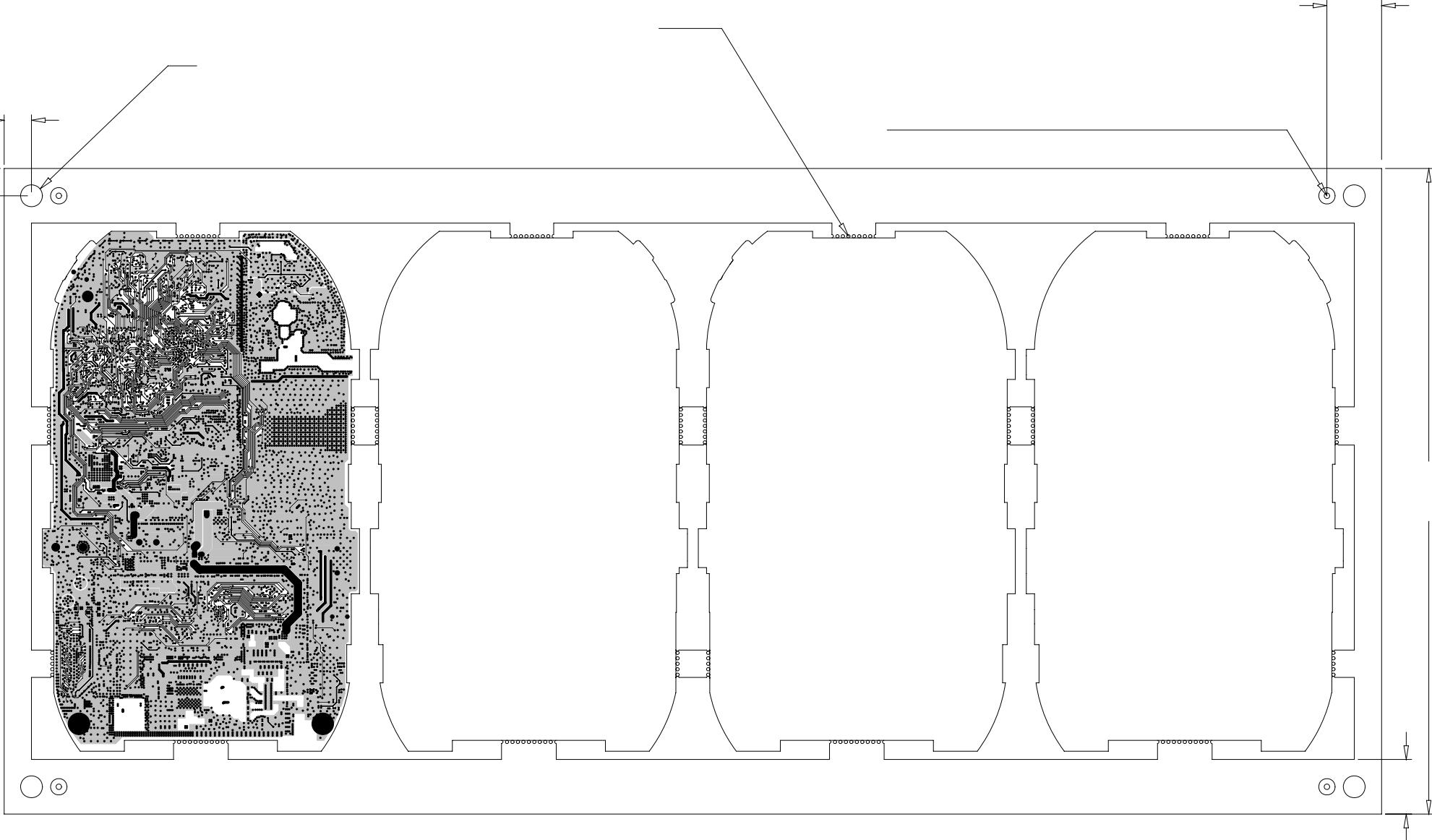
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L4 GND1	
DATE:	2008-03-03	
Clearance error	8	
Connectivity error	0	



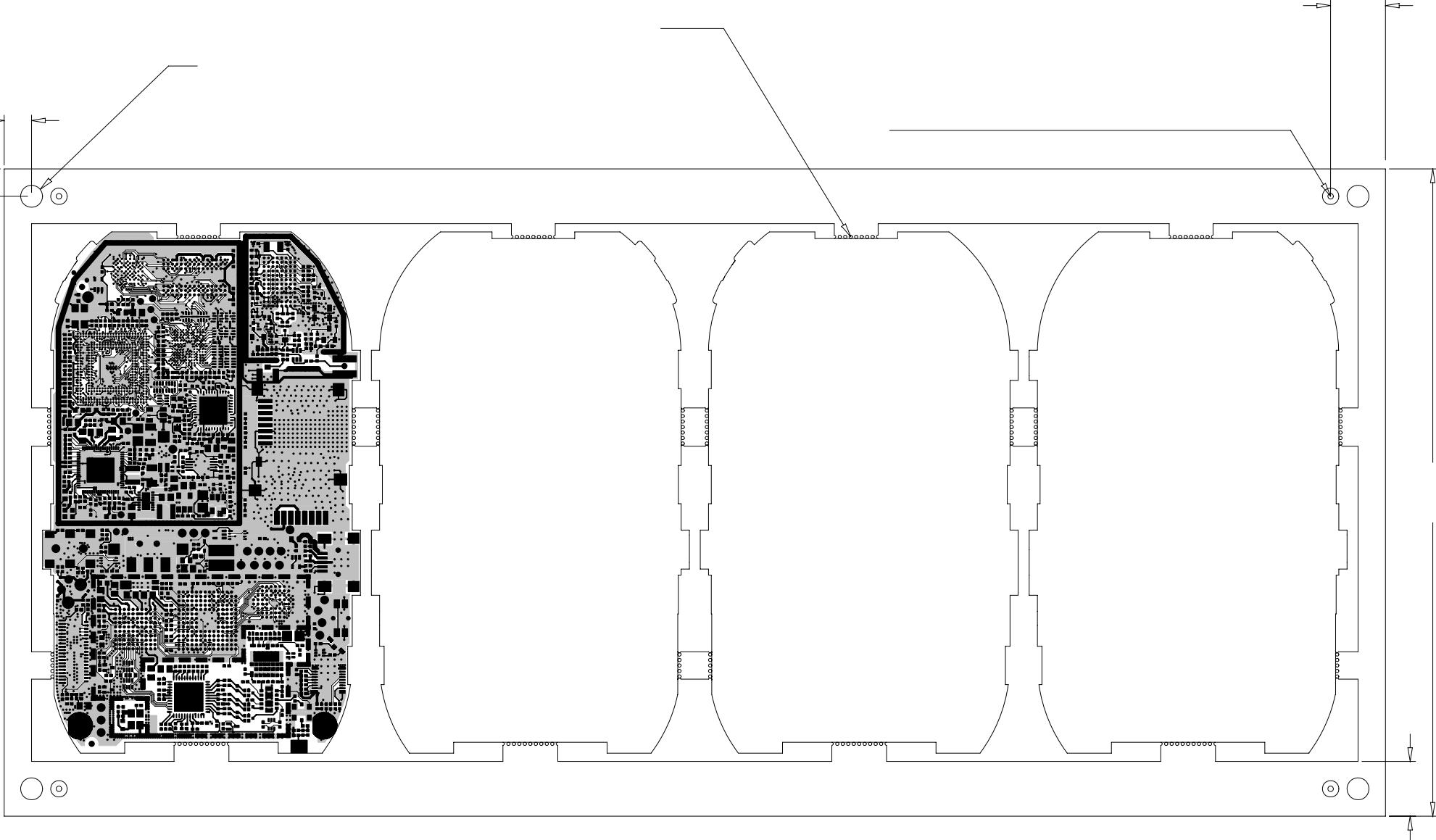
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L5 VCC	
DATE:	2008-03-03	
Clearance error		8
Connectivity error		0



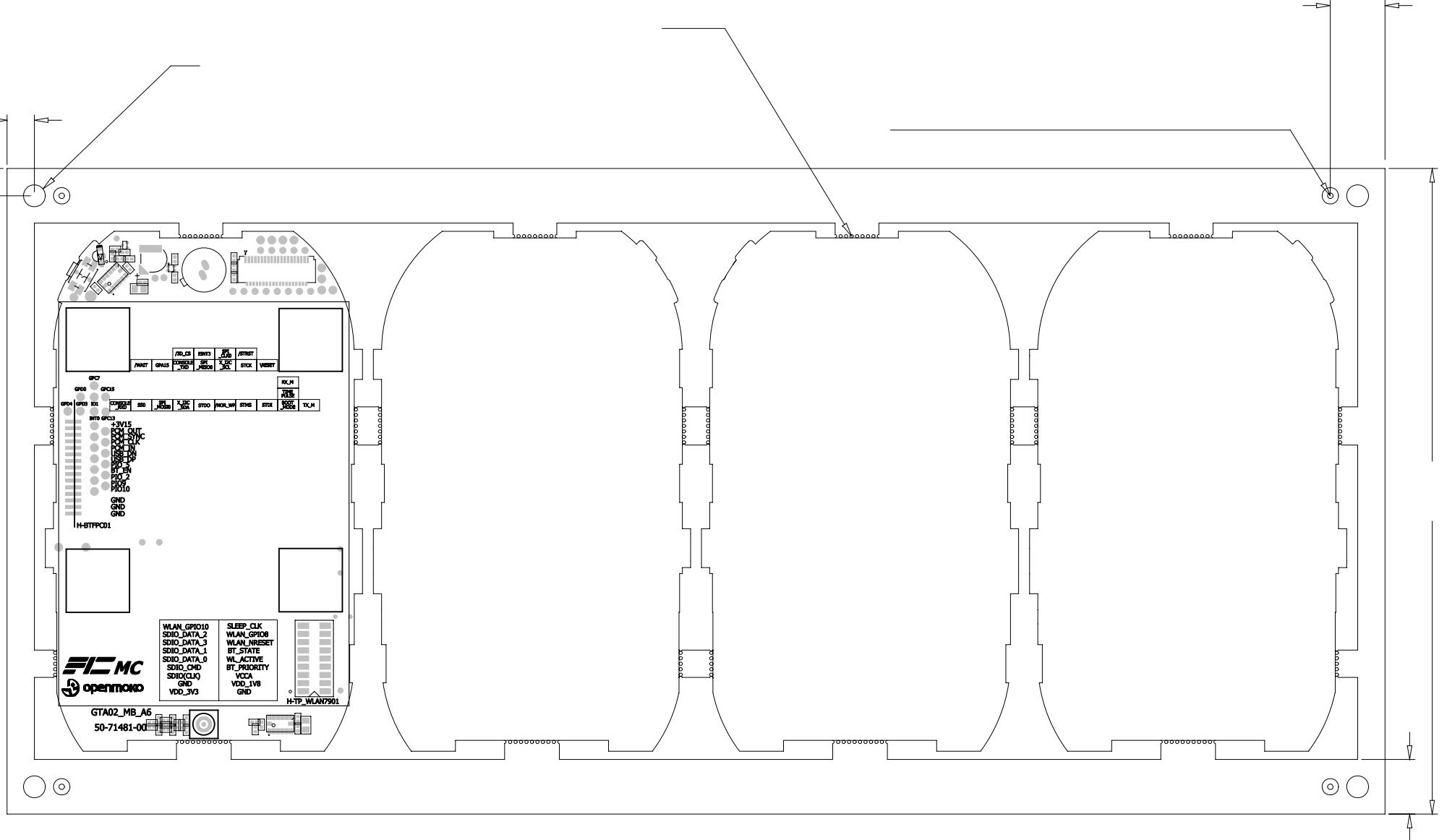
ST	GTA02	8 LAYER
	FIC011	
LAYER: L6 GND2		
DATE: 2008-03-03		
Clearance error		8
Connectivity error		0



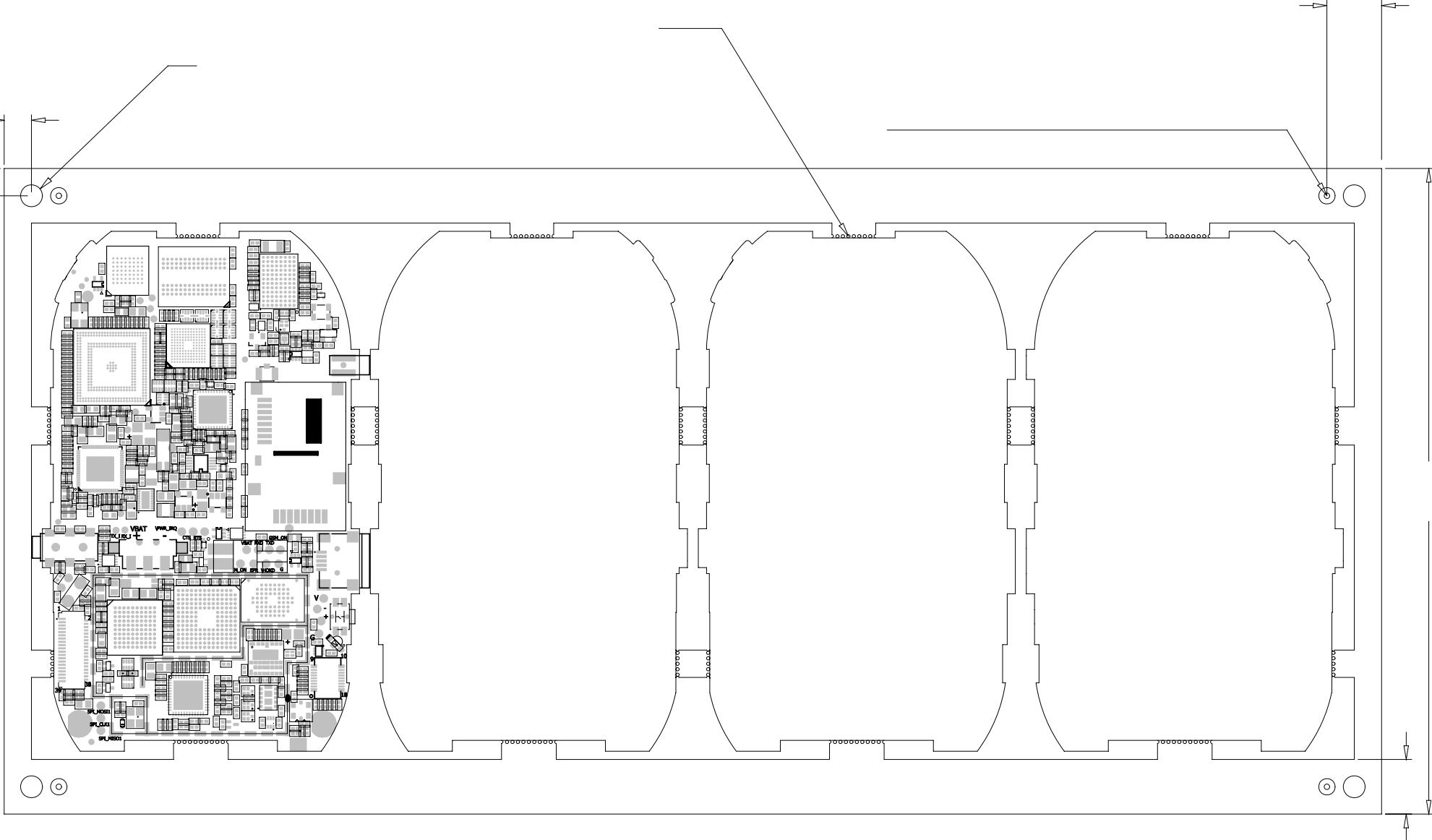
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L7 GND(TRK)	
DATE:	2008-03-03	
Clearance error	8	
Connectivity error	0	



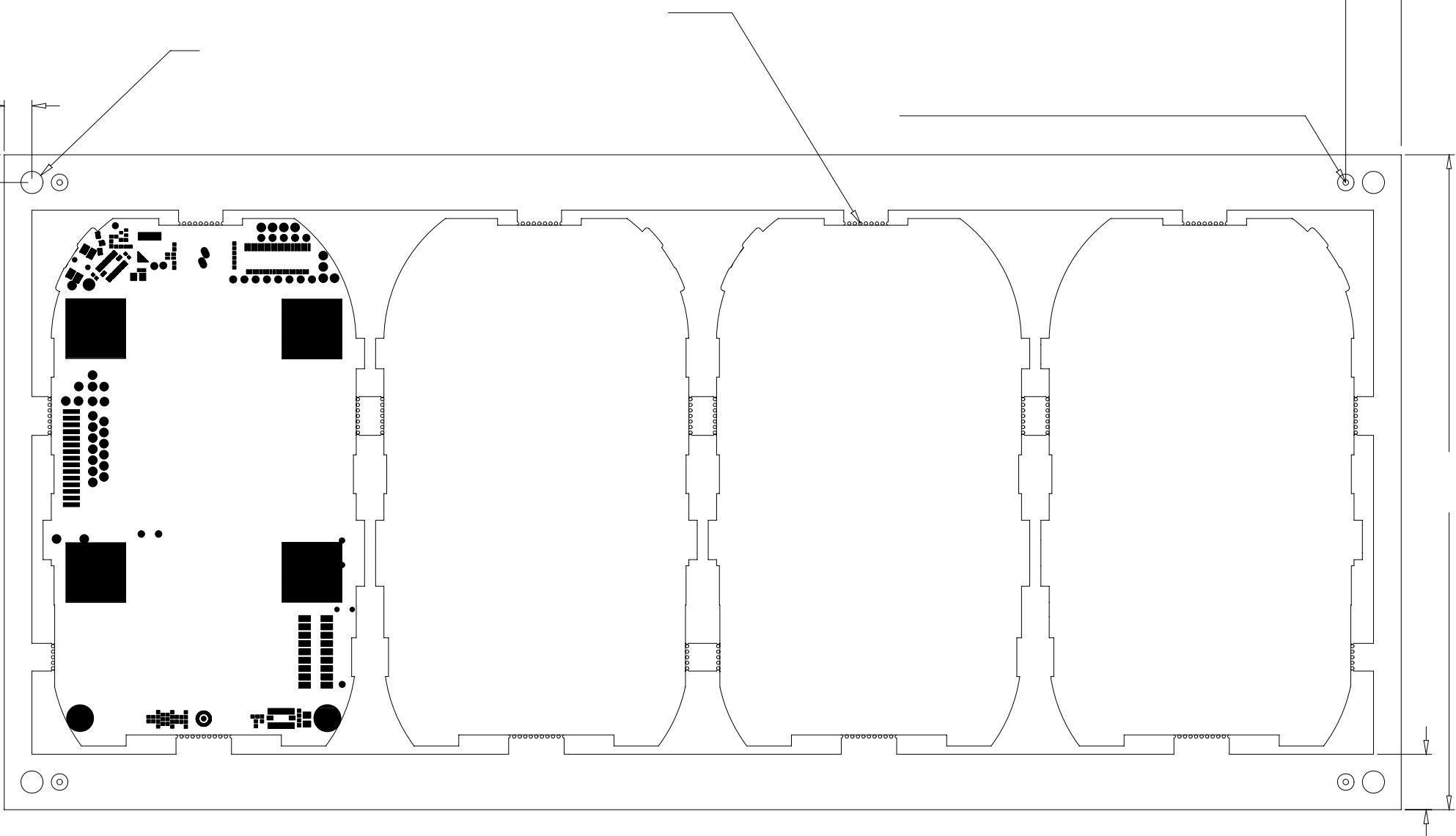
ST	GTA02	8 LAYER
	FIC011	
LAYER:	8 SOLDER SIDE	
DATE:	2008-03-03	
Clearance error		8
Connectivity error		0



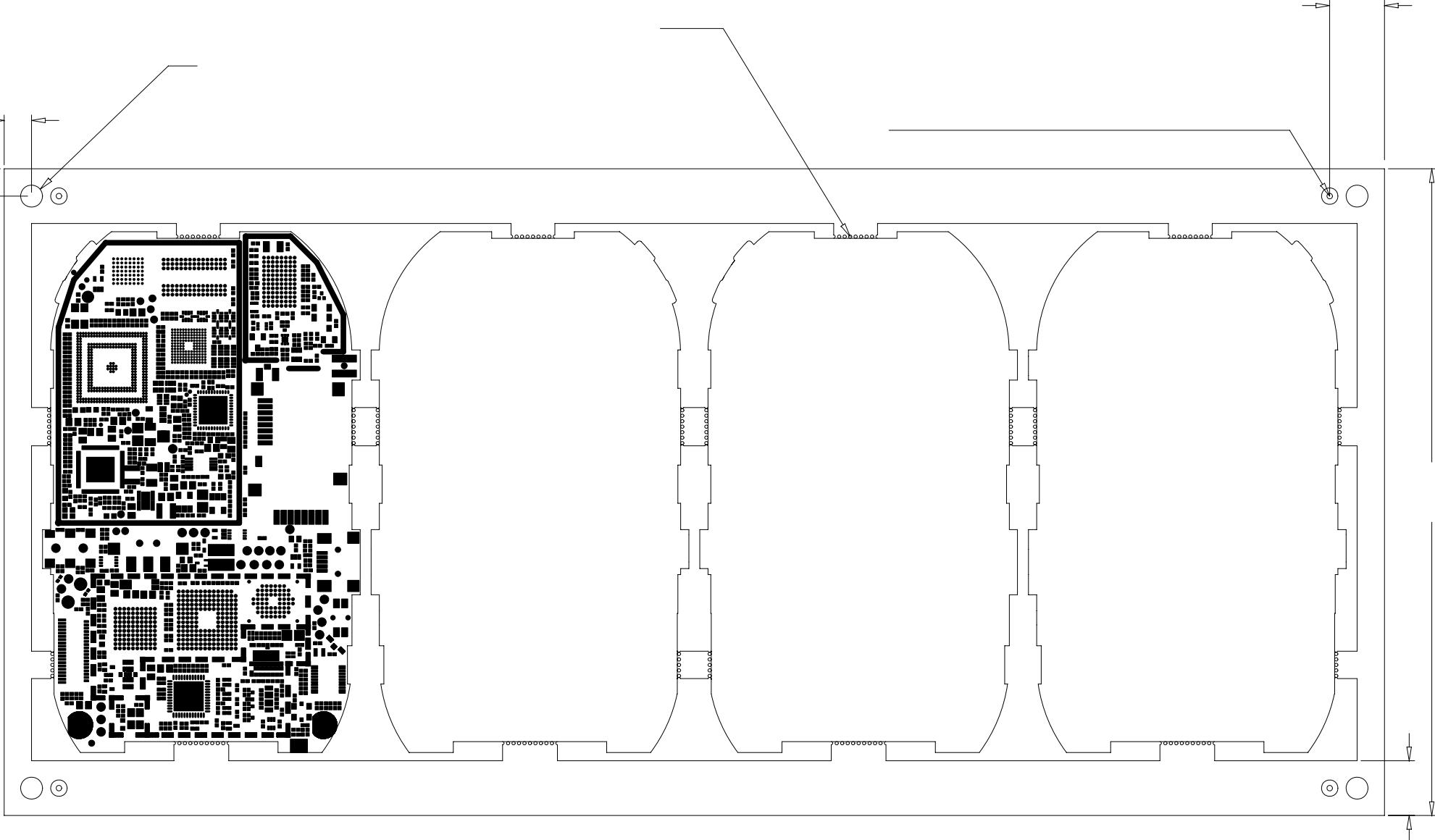
ST	GTA02 FIC011	8 LAYER
LAYER:	SILKSCREEN (TOP)	
DATE:	2008-03-03	



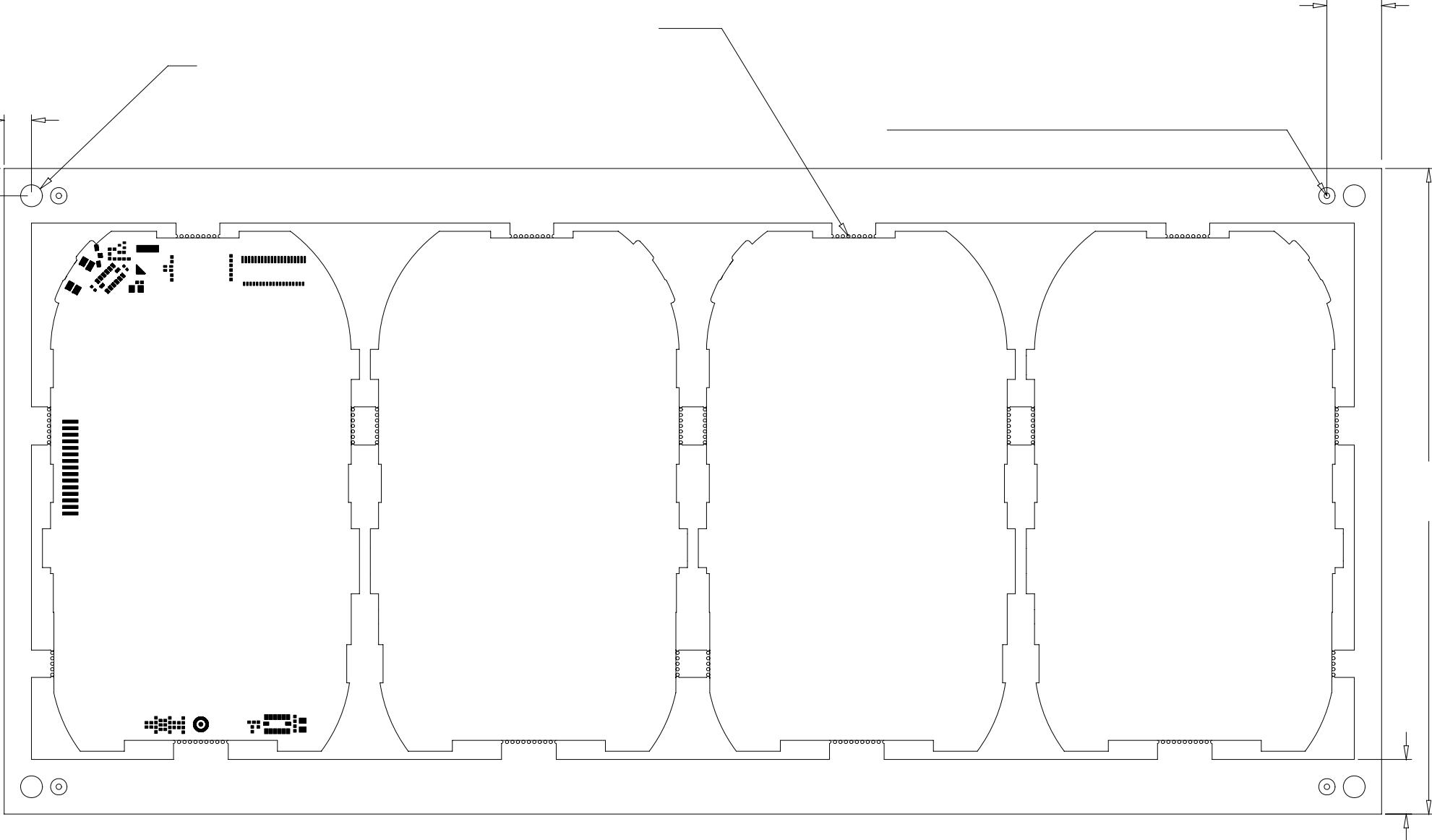
ST	GTA02	8 LAYER
	FIC011	
LAYER:	SILKSCREEN (BOT)	
DATE:	2008-03-03	
Clearance error	8	
Connectivity error	0	



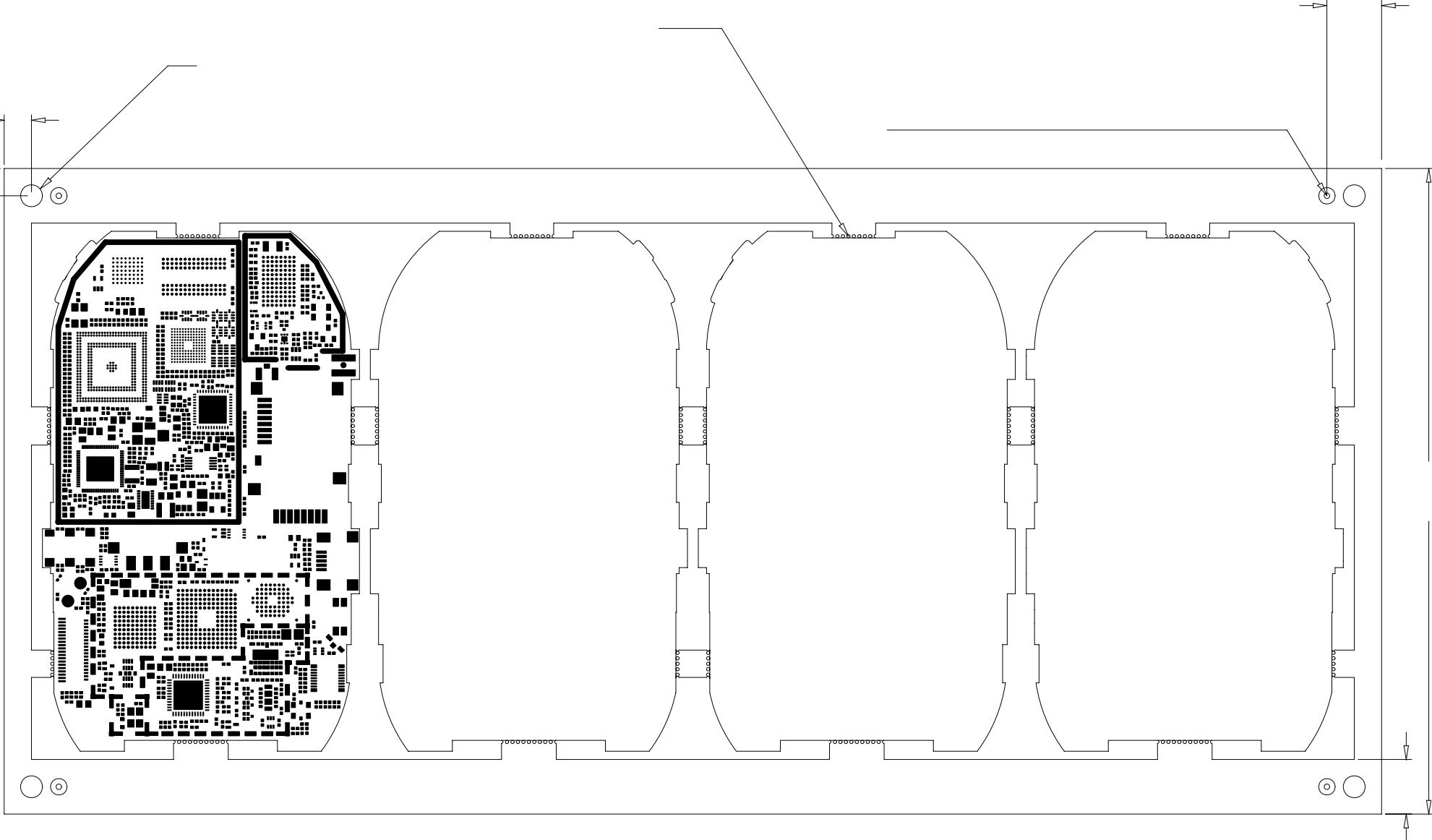
ST	GTA02	8 LAYER
	FIC011	
LAYER: TOP SOLDER MASK		
DATE: 2008-03-03		
Clearance error		8
Connectivity error		0



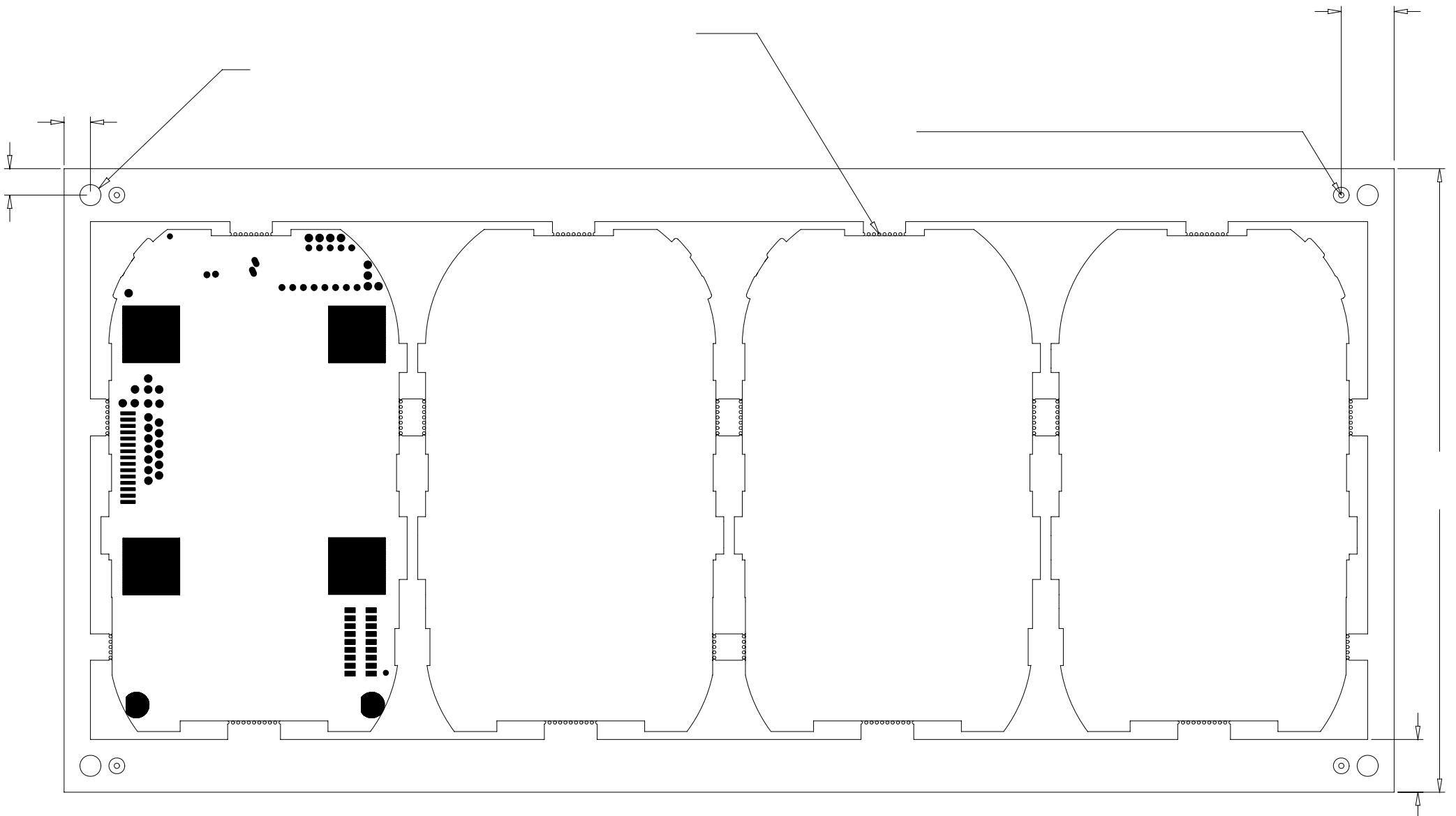
ST	GTA02	8 LAYER	
	FIC011		
LAYER:	BOT SOLDER MASK		
DATE:	2008-03-03		
Clearance error		8	
Connectivity error		0	



ST	GTA02	8 LAYER	
	FIC011		
LAYER: SMD PASTE MASK(TOP)			
DATE: 2008-03-03			
Clearance error		8	
Connectivity error		0	

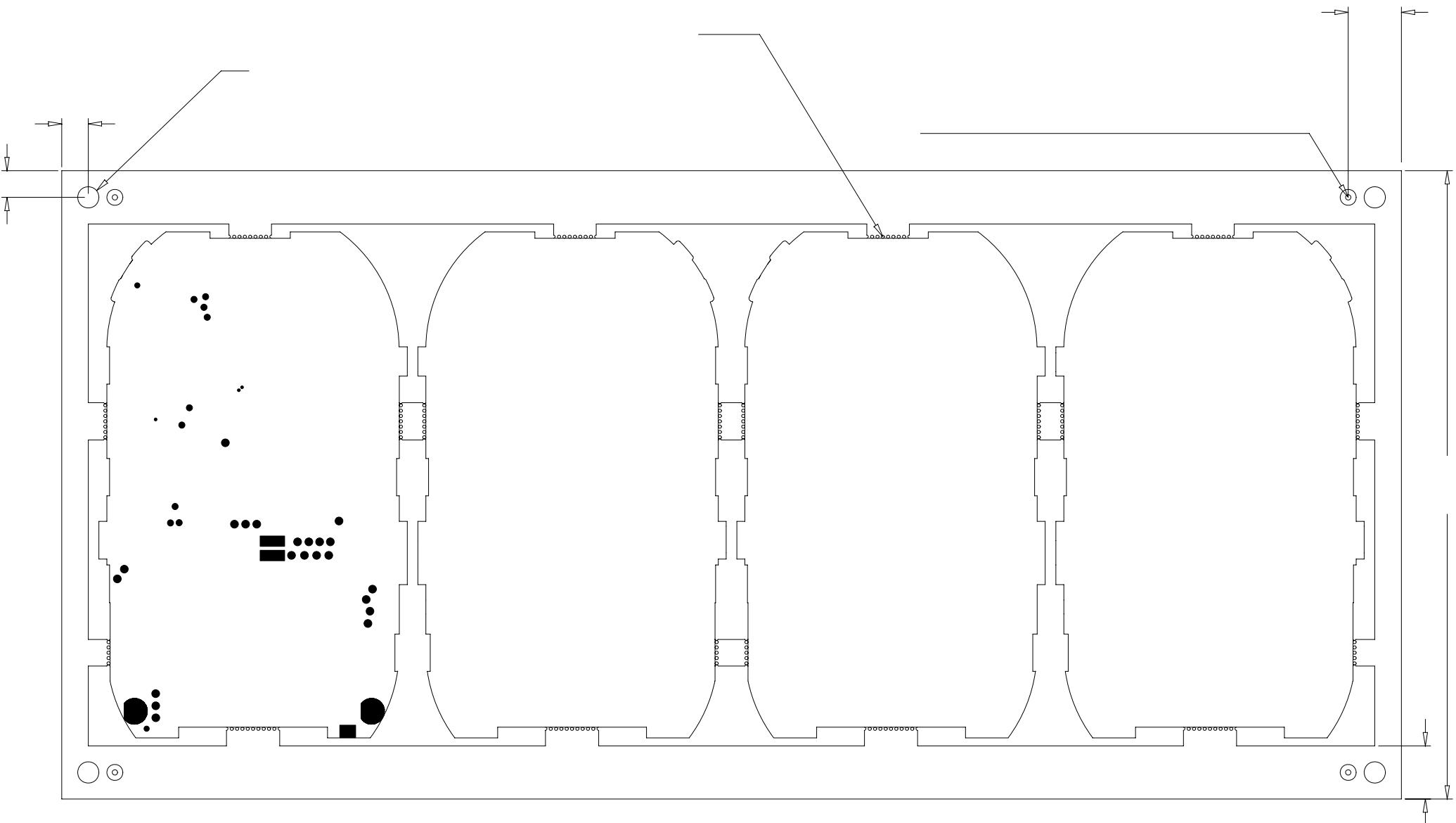


ST	GTA02	8 LAYER
	FIC011	
LAYER:	SMD PASTE MASK(BOT)	
DATE:	2008-03-03	
Clearance error	8	
Connectivity error	0	



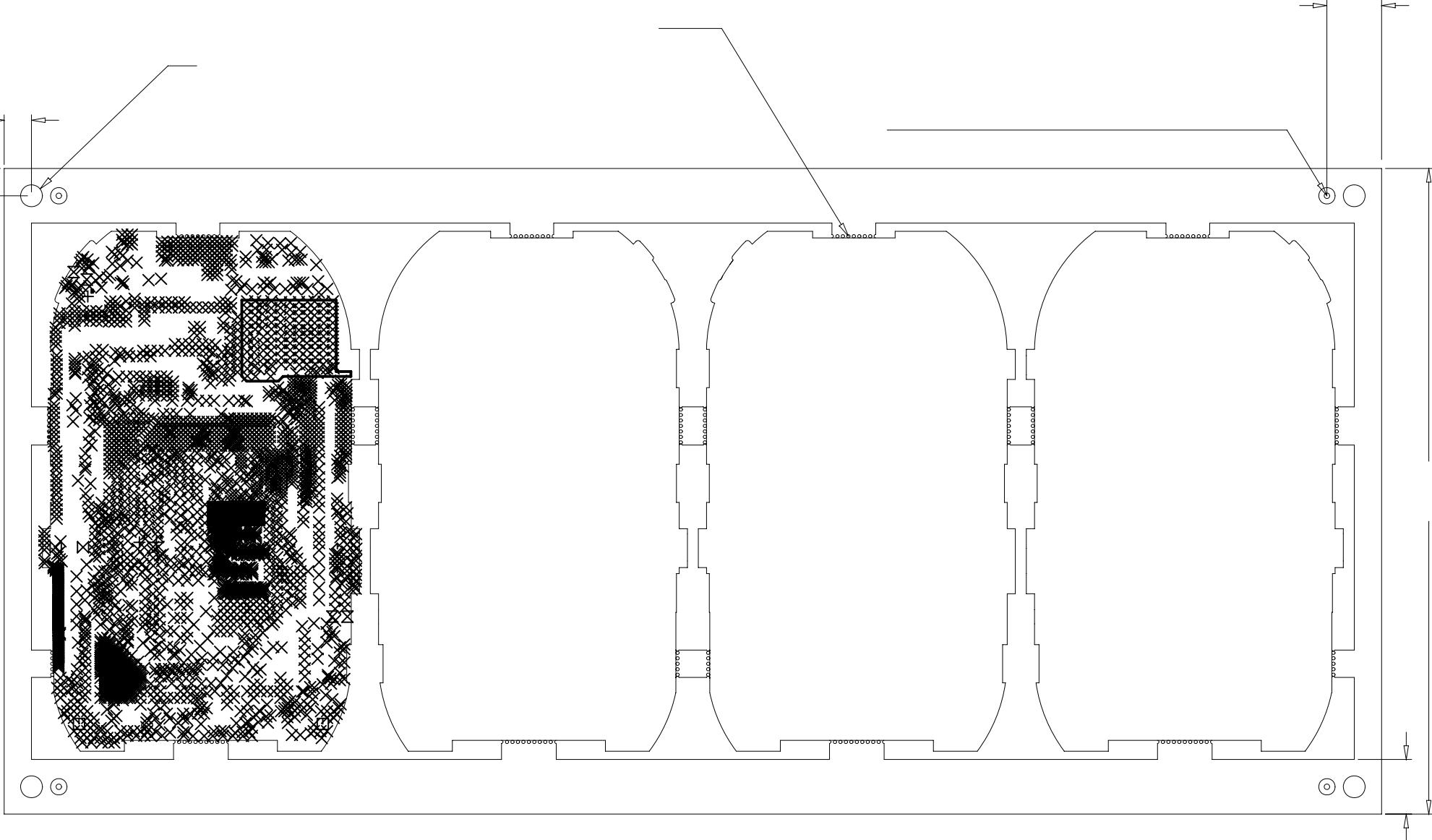
ST	GTA02	8 LAYER
	FIC011	
LAYER:		
DATE: 2008-03-03		
Clearance error		8
Connectivity error		0

GTA-MB-A6(50-71481-00)0 - Tue Mar 04 14:46:26 2008



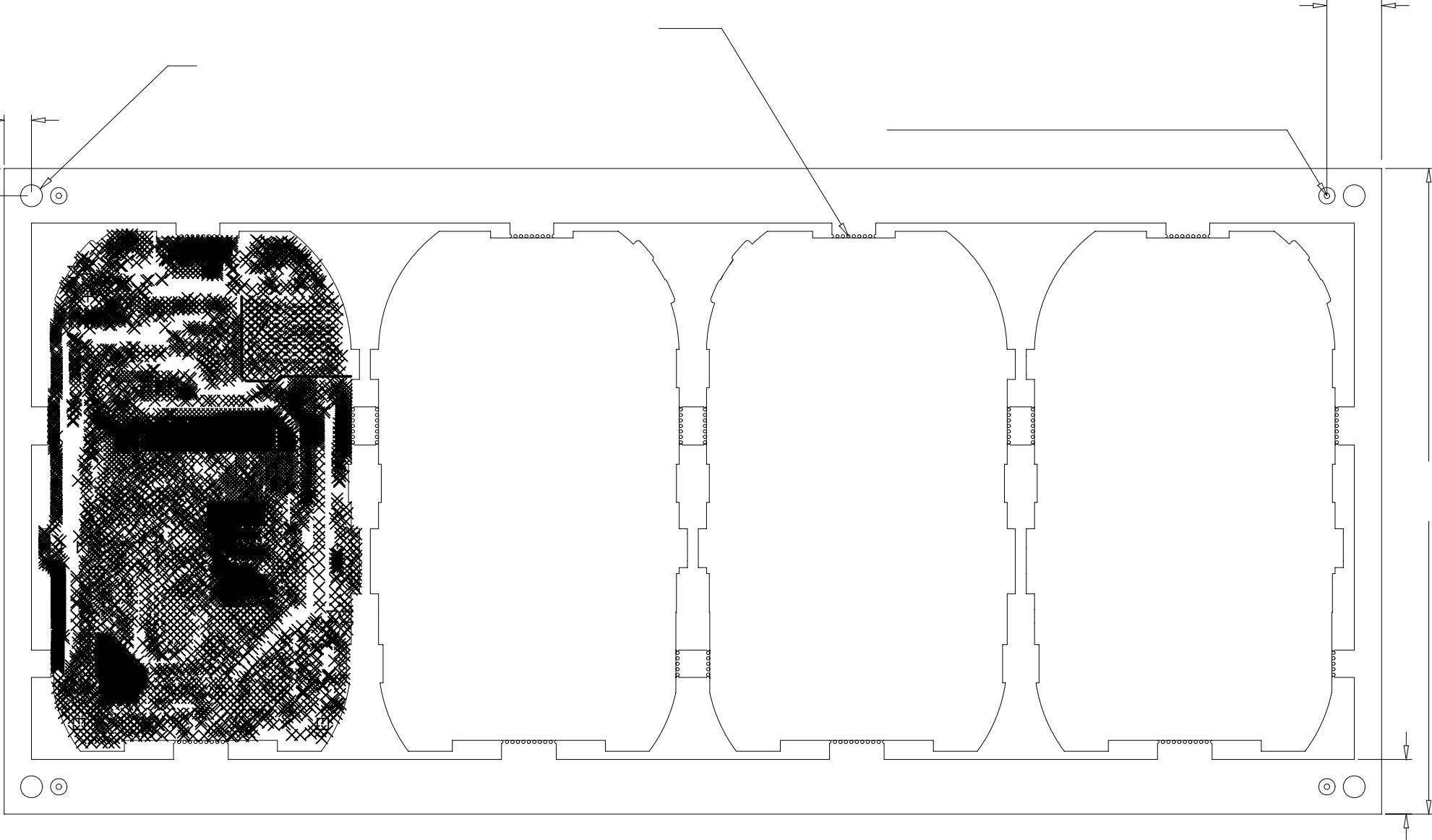
ST	GTA02	8 LAYER
	FIC011	
LAYER:		
DATE: 2008-03-03		
Clearance error		8
Connectivity error		0

GTA-MB-A6(50-71481-00)0 - Tue Mar 04 14:46:46 2008



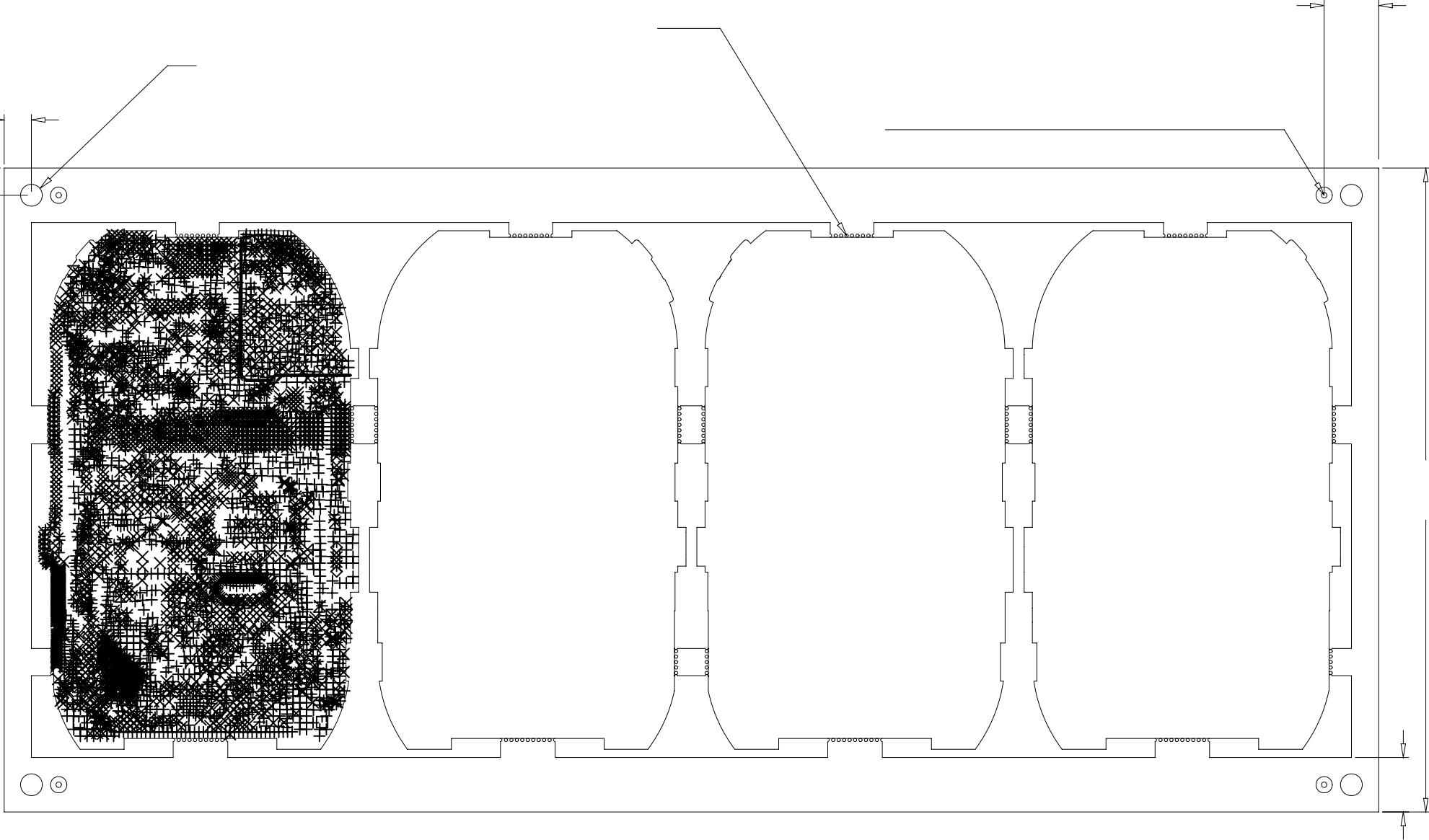
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L1 TOP SIDE	DRILL
DATE:	2008-03-03	
Clearance error		8
Connectivity error		0

SIZE	QTY	SYM	PLATED	TOL
4	2780	X	YES	+/-0.0
78.74	2	□	YES	+/-0.0
35.43	2	◇	NO	+/-0.0
29.53	4	☒	NO	+/-0.0
59.06	2	☒	NO	+/-0.0
43.31	2	+	NO	+/-0.0
80	1	+	NO	+/-0.0



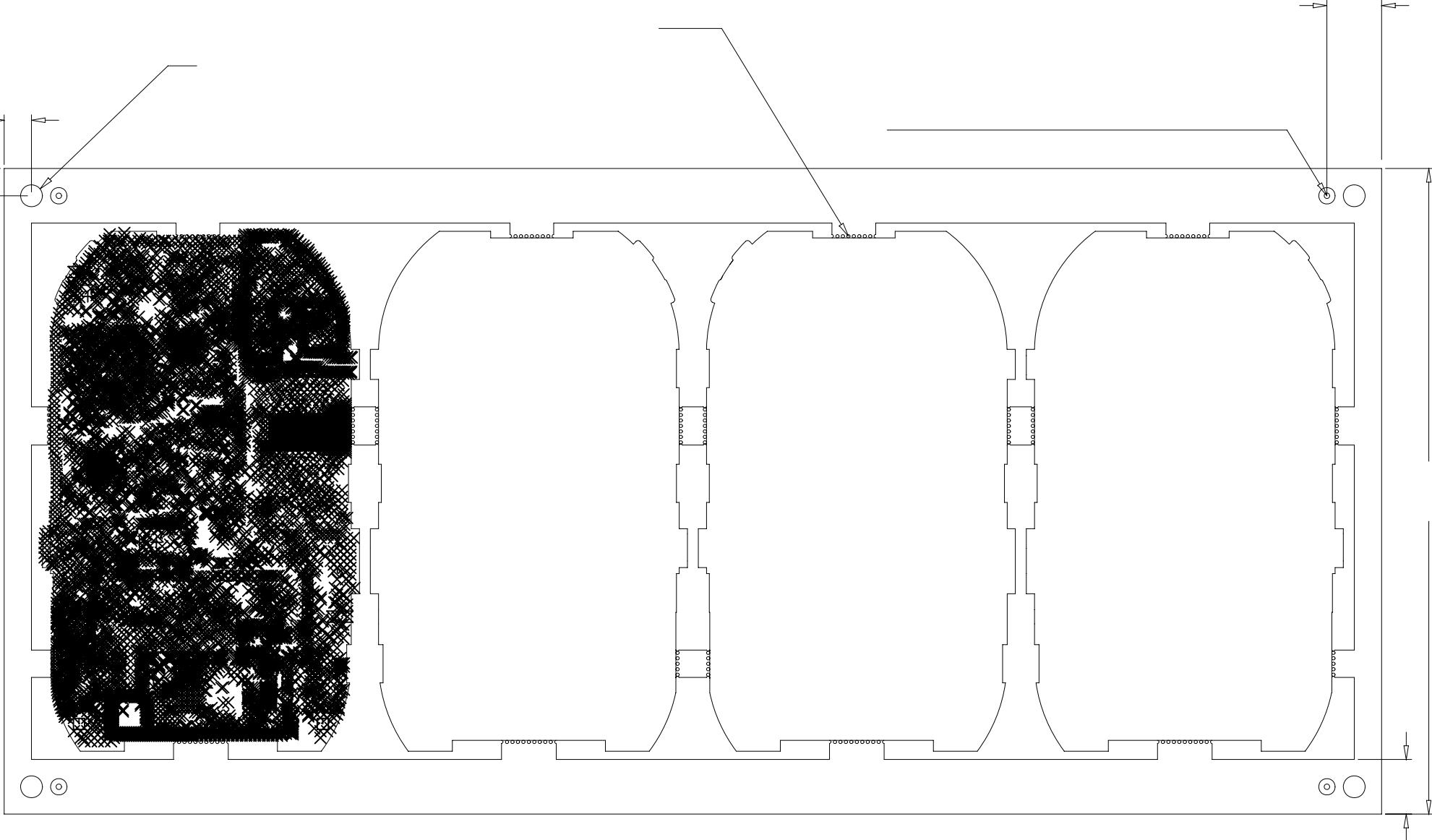
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L2 GND(TRK)	DRILL
DATE:	2008-03-03	
Clearance error	8	
Connectivity error	0	

SIZE	QTY	SYM	PLATED	TOL
4	5062	X	YES	+/-0.0
78.74	2	□	YES	+/-0.0
35.43	2	◇	NO	+/-0.0
29.53	4	☒	NO	+/-0.0
59.06	2	☒	NO	+/-0.0
43.31	2	+	NO	+/-0.0
80	1	+	NO	+/-0.0



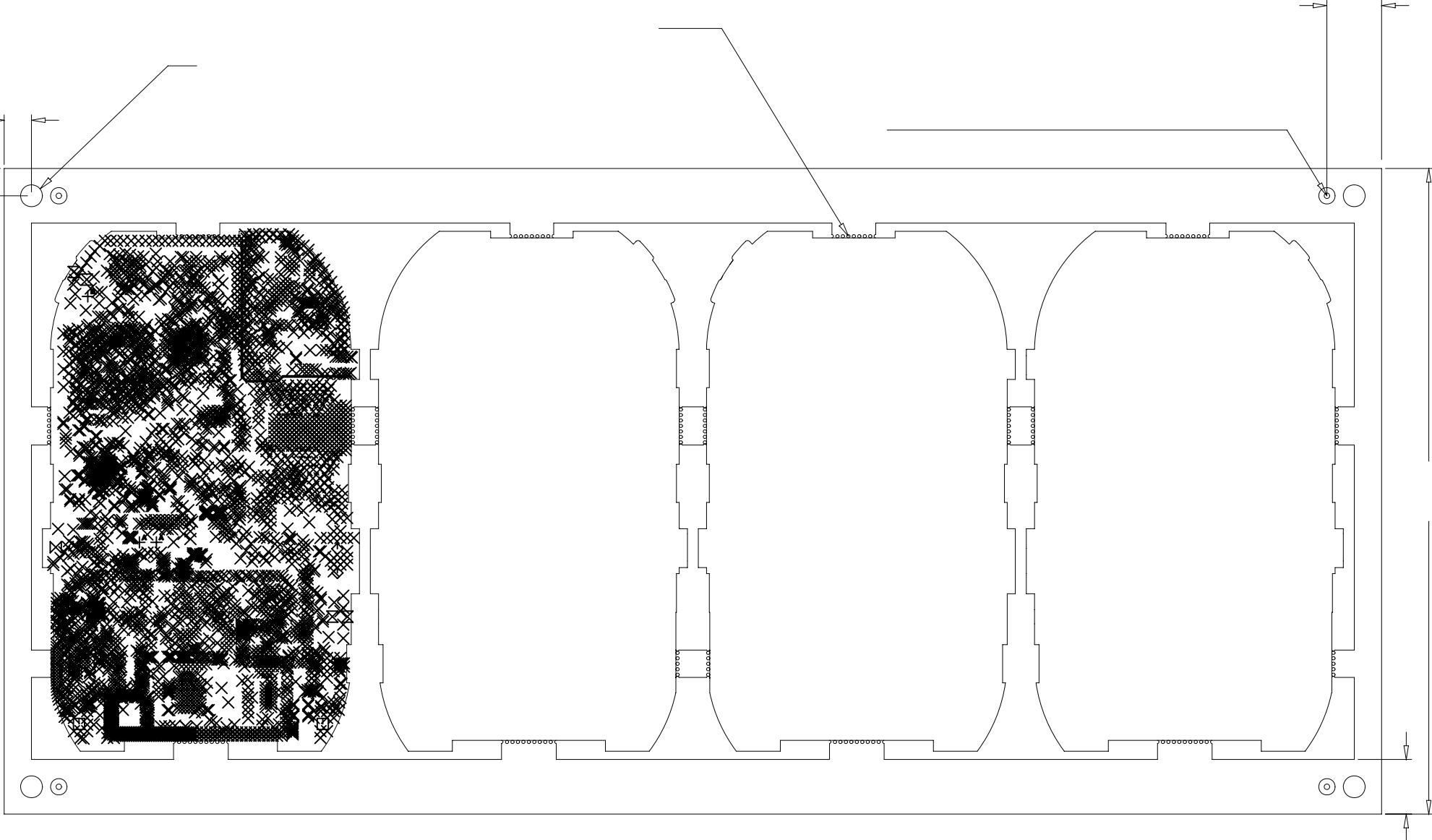
ST	GTA02	8 LAYER
	FIC011	
LAYER:	L3 GND(TRK)	DRILL
DATE:	2008-03-03	
Clearance error		8
Connectivity error		0

SIZE	QTY	SYM	PLATED	TOL
12	2231	+	YES	+/-0.0
4	2282	X	YES	+/-0.0
78.74	2	□	YES	+/-0.0
35.43	2	◇	NO	+/-0.0
29.53	4	☒	NO	+/-0.0
59.06	2	☒	NO	+/-0.0
43.31	2	+	NO	+/-0.0
80	1	+	NO	+/-0.0



ST	GTA02	8 LAYER
	FIC011	
LAYER:	L7 GND(TRK)	DRILL
DATE:	2008-03-03	
Clearance error		8
Connectivity error		0

SIZE	QTY	SYM	PLATED	TOL
4	7052	X	YES	+/-0.0
78.74	2	□	YES	+/-0.0
35.43	2	◇	NO	+/-0.0
29.53	4	☒	NO	+/-0.0
59.06	2	☒	NO	+/-0.0
43.31	2	+	NO	+/-0.0
80	1	+	NO	+/-0.0



ST	GTA02	8 LAYER
	FIC011	
LAYER:	DE	TLLR8 SOLDER SIDE
DATE:	2008-03-03	
Clearance error	8	
Connectivity error	0	

SIZE	QTY	SYM	PLATED	TOL
4	3507	X	YES	+/-0.0
78.74	2	□	YES	+/-0.0
35.43	2	◇	NO	+/-0.0
29.53	4	☒	NO	+/-0.0
59.06	2	☒	NO	+/-0.0
43.31	2	+	NO	+/-0.0
80	1	+	NO	+/-0.0