Pin	+5V	+3.3V	Universal	Description
A1	TRST			Test Logic Reset
A2	+12V			+12 VDC
А3	TMS			Test Mde Select
A4	TDI			Test Data Input
A5	+5V			+5 VDC
A6	INTA			Interrupt A
A7	INTC			Interrupt C
A8	+5V			+5 VDC
A9	RESV01			Reserved VDC
A10	+5V	+3.3V	Signal Rail	+V I/O (+5 V or +3.3 V)
A11	RESV03			Reserved VDC
A12	GND03	(OPEN)	(OPEN)	Ground or Open (Key)
A13	GND05	(OPEN)	(OPEN)	Ground or Open (Key)
A14	RESV05			Reserved VDC
A15	RESET			Reset
A16	+5V	+3.3V	Signal Rail	+V I/O (+5 V or +3.3 V)
A17	GNT			Grant PCI use
A18	GND08			Ground
A19	RESV06			Reserved VDC
A20	AD30			Address/Data 30
A21	+3.3V01			+3.3 VDC
A22	AD28			Address/Data 28
A23	AD26			Address/Data 26
A24	GND10			Ground
A25	AD24			Address/Data 24
A26	IDSEL			Initialization Device Select
A27	+3.3V03			+3.3 VDC
A28	AD22			Address/Data 22
A29	AD20			Address/Data 20
A30	GND12			Ground
A31	AD18			Address/Data 18
A32	AD16			Address/Data 16
A33	+3.3V05			+3.3 VDC
A34	FRAME			Address or Data phase
A35	GND14			Ground

A36	TRDY			Target Ready
	GND15			Ground
A38	STOP			Stop Transfer Cycle
A39	+3.3V07			+3.3 VDC
A40	SDONE			Snoop Done
A41	SBO			Snoop Backoff
A42	GND17			Ground
A43	PAR			Parity
A44	AD15			Address/Data 15
A45	+3.3V10			+3.3 VDC
A46	AD13			Address/Data 13
A47	AD11			Address/Data 11
A48	GND19			Ground
A49	AD9			Address/Data 9
A52	C/BE0			Command, Byte Enable 0
A53	+3.3V11			+3.3 VDC
A54	AD6			Address/Data 6
A55	AD4			Address/Data 4
A56	GND21			Ground
A57	AD2			Address/Data 2
A58	AD0			Address/Data 0
A59	+5V	+3.3V	Signal Rail	+V I/O (+5 V or +3.3 V)
A60	REQ64			Request 64 bit ???
A61	VCC11			+5 VDC
A62	VCC13			+5 VDC
A63	GND			Ground
A64	C/BE[7]#			Command, Byte Enable 7
A65	C/BE[5]#			Command, Byte Enable 5
A66	+5V	+3.3V	Signal Rail	+V I/O (+5 V or +3.3 V)
A67	PAR64			Parity 64 ???
A68	AD62			Address/Data 62
A69	GND			Ground
A70	AD60			Address/Data 60
A71	AD58			Address/Data 58
-				

A74 AD54 Address A75 +5V +3.3V Signal Rail +V I/O A76 AD52 Address A77 AD50 Address A78 GND Ground A79 AD48 Address A80 AD46 Address A81 GND Ground A82 AD44 Address A83 AD42 Address A84 +5V +3.3V Signal Rail +V I/O A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD B2 TCK	/Data 56 /Data 54 (+5 V or +3.3 V) /Data 52 /Data 50
A75 +5V +3.3V Signal Rail +V I/O A76 AD52 Address A77 AD50 Address A78 GND Ground A79 AD48 Address A80 AD46 Address A81 GND Ground A82 AD44 Address A83 AD42 Address A84 +5V +3.3V Signal Rail +V I/O A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD B2 TCK Test Ck B3 GND </td <td>(+5 V or +3.3 V) /Data 52 /Data 50</td>	(+5 V or +3.3 V) /Data 52 /Data 50
A76 AD52 Address A77 AD50 Address A78 GND Ground A79 AD48 Address A80 AD46 Address A81 GND Ground A82 AD44 Address A83 AD42 Address A84 +5V +3.3V Signal Rail +V I/O A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD B2 TCK Test Ck B3 GND Ground B4 TDO Test Da	/Data 52 /Data 50
A77 AD50	/Data 50
A78 GND Ground A79 AD48 Address A80 AD46 Address A81 GND Ground A82 AD44 Address A83 AD42 Address A84 +5V +3.3V Signal Rail +V I/O A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	
A79 AD48 Address A80 AD46 Address A81 GND Ground A82 AD44 Address A83 AD42 Address A84 +5V +3.3V Signal Rail +V I/O A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	/Data 48
A80 AD46 Address A81 GND Ground A82 AD44 Address A83 AD42 Address A84 +5V +3.3V Signal Rail +V I/O A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD B2 TCK Test Class B3 GND Ground B4 TDO Test Da	/Data 48
A81 GND Ground A82 AD44 Address A83 AD42 Address A84 +5V +3.3V Signal Rail +V I/O A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	
A82 AD44 Address A83 AD42 Address A84 +5V +3.3V Signal Rail +V I/O A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	/Data 46
A83 AD42 Address A84 +5V +3.3V Signal Rail +V I/O A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD B2 TCK Test Class B3 GND Ground B4 TDO Test Da	
A84 +5V +3.3V Signal Rail +V I/O A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	/Data 44
A85 AD40 Address A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	/Data 42
A86 AD38 Address A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD6 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	(+5 V or +3.3 V)
A87 GND Ground A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	/Data 40
A88 AD36 Address A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	/Data 38
A89 AD34 Address A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	
A90 GND Ground A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	/Data 36
A91 AD32 Address A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	/Data 34
A92 RES Reserve A93 GND Ground A94 RES Reserve B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	
A93 GND Ground A94 RES Reserve B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	/Data 32
A94 RES Reserve B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	d
B1 -12V -12 VD0 B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	
B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	d
B2 TCK Test Clo B3 GND Ground B4 TDO Test Da	
B3 GND Ground B4 TDO Test Da	0
B4 TDO Test Da	ock
B5 +5V +5 VDC	ta Output
	,
B6 +5V +5 VDC	,
B7 INTB Interrupt	В
B8 INTD Interrupt	
B9 PRSNT1 Reserve	D
B10 RES +V I/O	
B11 PRSNT2 ??	
B12 GND (OPEN) (OPEN) Ground	d
B13 GND (OPEN) (OPEN) Ground	d

B14 RES Reserved VDC B15 GND Reset B16 CLK Clock B17 GND Ground B18 REQ Request B19 +5V +3.3V Signal Rail +V I/O (+5 V or +3.3 V) B20 AD31 Address/Data 29 Address/Data 29 B22 AD29 Address/Data 27 Address/Data 27 B22 AD25 Address/Data 25 Address/Data 25 B25 +3.3V +3.3VDC Address/Data 23 B26 C/BE3 Ground Ground B29 AD21 Address/Data 21 Address/Data 19 B30 AD19 Address/Data 19 Address/Data 19 B31 +3.3V +3.3 VDC Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3V06 Parity Error B43 GND16 Ground	D44	DEC			Decembed VDC
B16 CLK B17 GND B18 REQ B19 +5V					Reserved VDC
B17 GND	B15	GND			Reset
B18 REQ Request B19 +5V +3.3V Signal Rail +V I/O (+5 V or +3.3 V) B20 AD31 Address/Data 29 B22 GND Ground B23 AD27 Address/Data 27 B24 AD25 Address/Data 25 B25 +3.3V +3.3VDC B26 C/BE3 Command, Byte Enable 3 B27 AD23 Address/Data 23 B28 GND Ground B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3V +3.3 VDC B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 RRDY Initiator Ready B36 +3.3V06 +3.3 VDC B37 DEVSEL Device Select B38 GND16 Ground B40 PERR Parity Error B41 <	B16	CLK			Clock
### B19	B17	GND			Ground
B20 AD31 Address/Data 31 B21 AD29 Address/Data 29 B22 GND Ground B23 AD27 Address/Data 27 B24 AD25 Address/Data 25 B25 +3.3V +3.3VDC B26 C/BE3 Command, Byte Enable 3 B27 AD23 Address/Data 23 B28 GND Ground B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3 VDC Address/Data 17 B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3 VDC Lock bus B37 DEVSEL Device Select B38 GND16 Ground B40 PERR Parity Error B41 +3.3 VDC +3.3 VDC B42 SERR System Error B43 +3.3 VDC Command, Byte Enable 1	B18	REQ			Request
B21 AD29 Address/Data 29 B22 GND Ground B23 AD27 Address/Data 27 B24 AD25 Address/Data 25 B25 +3.3V +3.3VDC B26 C/BE3 Command, Byte Enable 3 B27 AD23 Address/Data 23 B28 GND Ground B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3VDC Address/Data 17 B32 AD17 Address/Data 17 B33 GRD13 Ground B35 IRDY Initiator Ready B36 +3.3 VDC Parity Enable 2 B37 DEVSEL Device Select B38 GND16 Ground B40 PERR Parity Error B41 +3.3 VDC B42 SERR System Error B43 +3.3 VOO +3.3 VDC B44 C/BE1 Command, Byte Enable 1	B19	+5V	+3.3V	Signal Rail	+V I/O (+5 V or +3.3 V)
B22 GND Ground B23 AD27 Address/Data 27 B24 AD25 Address/Data 25 B25 +3.3V +3.3VDC B26 C/BE3 Command, Byte Enable 3 B27 AD23 Address/Data 23 B28 GND Ground B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3V +3.3 VDC B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3 VDC Device Select B37 DEVSEL Device Select B38 GND16 Ground B40 PERR Parity Error B41 +3.3 VDC B42 SERR System Error B43 +3.3 VDC Command, Byte Enable 1 B44 C/BE1 Command, Byte Enable 1	B20	AD31			Address/Data 31
B23 AD27 Address/Data 27 B24 AD25 Address/Data 25 B25 +3.3V +3.3VDC B26 C/BE3 Command, Byte Enable 3 B27 AD23 Address/Data 23 B28 GND Ground B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3V +3.3 VDC B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3VDC Device Select B37 DEVSEL Device Select B38 GND16 Ground B40 PERR Parity Error B41 +3.3VDC B42 SERR System Error B43 +3.3VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data	B21	AD29			Address/Data 29
B24 AD25 Address/Data 25 B25 +3.3V +3.3VDC B26 C/BE3 Command, Byte Enable 3 B27 AD23 Address/Data 23 B28 GND Ground B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3 VDC B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3 VDC Device Select B37 DEVSEL Device Select B38 GND16 Ground B39 LOCK Lock bus B40 PERR Parity Error B41 +3.3 VDC B42 SERR System Error B43 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 10 <td>B22</td> <td>GND</td> <td></td> <td></td> <td>Ground</td>	B22	GND			Ground
B25 +3.3V +3.3VDC B26 C/BE3 Command, Byte Enable 3 B27 AD23 Address/Data 23 B28 GND Ground B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3V +3.3 VDC B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3V06 +3.3 VDC B37 DEVSEL Device Select B38 GND16 Ground B39 LOCK Lock bus B40 PERR Parity Error B41 +3.3V08 +3.3 VDC B42 SERR System Error B43 +3.3V09 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground	B23	AD27			Address/Data 27
B26 C/BE3 Command, Byte Enable 3 B27 AD23 Address/Data 23 B28 GND Ground B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3V +3.3 VDC B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3V06 +3.3 VDC B37 DEVSEL Device Select B38 GND16 Ground B39 LOCK Lock bus B40 PERR Parity Error B41 +3.3 VDC B42 SERR System Error B43 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 10	B24	AD25			Address/Data 25
B27 AD23 Address/Data 23 B28 GND Ground B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3V +3.3 VDC B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3V06 +3.3 VDC B37 DEVSEL Device Select B38 GND16 Ground B40 PERR Parity Error B41 +3.3V08 +3.3 VDC B42 SERR System Error B43 +3.3V09 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 10	B25	+3.3V			+3.3VDC
B28 GND Ground B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3 VDC B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3 VDC Device Select B37 DEVSEL Device Select B38 GND16 Ground B40 PERR Parity Error B41 +3.3 VDC B42 SERR System Error B43 +3.3 VOC Command, Byte Enable 1 B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 10	B26	C/BE3			Command, Byte Enable 3
B29 AD21 Address/Data 21 B30 AD19 Address/Data 19 B31 +3.3V +3.3 VDC B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3V06 +3.3 VDC B37 DEVSEL Device Select B38 GND16 Ground B40 PERR Parity Error B41 +3.3V08 +3.3 VDC B42 SERR System Error B43 +3.3V09 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B27	AD23			Address/Data 23
B30 AD19 Address/Data 19 B31 +3.3V +3.3 VDC B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3V06 +3.3 VDC B37 DEVSEL Device Select B38 GND16 Ground B40 PERR Parity Error B41 +3.3V08 +3.3 VDC B42 SERR System Error B43 +3.3V09 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B28	GND			Ground
### ##################################	B29	AD21			Address/Data 21
B32 AD17 Address/Data 17 B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3V06 +3.3 VDC B37 DEVSEL Device Select B38 GND16 Ground B39 LOCK Lock bus B40 PERR Parity Error B41 +3.3V08 +3.3 VDC B42 SERR System Error B43 +3.3V09 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B30	AD19			Address/Data 19
B33 C/BE2 Command, Byte Enable 2 B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3V06 +3.3 VDC B37 DEVSEL Device Select B38 GND16 Ground B39 LOCK Lock bus B40 PERR Parity Error B41 +3.3V08 +3.3 VDC B42 SERR System Error B43 +3.3V09 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 10	B31	+3.3V			+3.3 VDC
B34 GND13 Ground B35 IRDY Initiator Ready B36 +3.3V06 +3.3 VDC B37 DEVSEL Device Select B38 GND16 Ground B39 LOCK Lock bus B40 PERR Parity Error B41 +3.3V08 +3.3 VDC B42 SERR System Error B43 +3.3V09 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B32	AD17			Address/Data 17
B35 IRDY Initiator Ready B36 +3.3 VDC B37 DEVSEL Device Select B38 GND16 Ground B39 LOCK Lock bus B40 PERR Parity Error B41 +3.3 VDC B42 SERR System Error B43 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B33	C/BE2			Command, Byte Enable 2
### B36 #### ###########################	B34	GND13			Ground
B37 DEVSEL Device Select B38 GND16 Ground B39 LOCK Lock bus B40 PERR Parity Error B41 +3.3 VDC System Error B42 SERR System Error B43 +3.3 VDC Command, Byte Enable 1 B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B35	IRDY			Initiator Ready
B38 GND16 Ground B39 LOCK Lock bus B40 PERR Parity Error B41 +3.3 VDC +3.3 VDC B42 SERR System Error B43 +3.3 VDC Command, Byte Enable 1 B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B36	+3.3V06			+3.3 VDC
B39 LOCK Lock bus B40 PERR Parity Error B41 +3.3 VDC +3.3 VDC B42 SERR System Error B43 +3.3 VDC Command, Byte Enable 1 B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B37	DEVSEL			Device Select
B40 PERR Parity Error B41 +3.3 VDC B42 SERR System Error B43 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B38	GND16			Ground
B41 +3.3 VDC B42 SERR System Error B43 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B39	LOCK			Lock bus
B42 SERR System Error B43 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B40	PERR			Parity Error
B43 +3.3 VDC B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B41	+3.3V08			+3.3 VDC
B44 C/BE1 Command, Byte Enable 1 B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B42	SERR			System Error
B45 AD14 Address/Data 14 B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B43	+3.3V09			+3.3 VDC
B46 GND18 Ground B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B44	C/BE1			Command, Byte Enable 1
B47 AD12 Address/Data 12 B48 AD10 Address/Data 10	B45	AD14			Address/Data 14
B48 AD10 Address/Data 10	B46	GND18			Ground
	B47	AD12			Address/Data 12
B49 GND20 Ground	B48	AD10			Address/Data 10
	B49	GND20			Ground

B50	(OPEN)	GND	(OPEN)	Ground or Open (Key)
B51	(OPEN)	GND	(OPEN)	Ground or Open (Key)
B52	AD8			Address/Data 8
B53	AD7			Address/Data 7
B54	+3.3V12			+3.3 VDC
B55	AD5			Address/Data 5
B56	AD3			Address/Data 3
B57	GND22			Ground
B58	AD1			Address/Data 1
B59	VCC08			+5 VDC
B60	ACK64			Acknowledge 64 bit ???
B61	VCC10			+5 VDC
B62	VCC12			+5 VDC
B63	RES			Reserved
B64	GND			Ground
B65	C/BE[6]#			Command, Byte Enable 6
B66	C/BE[4]#			Command, Byte Enable 4
B67	GND			Ground
B68	AD63			Address/Data 63
B69	AD61			Address/Data 61
B70	+5V	+3.3V	Signal Rail	+V I/O (+5 V or +3.3 V)
B71	AD59			Address/Data 59
B72	AD57			Address/Data 57
B73	GND			Ground
B74	AD55			Address/Data 55
B75	AD53			Address/Data 53
B76	GND			Ground
B77	AD51			Address/Data 51
B78	AD49			Address/Data 49
B79	+5V	+3.3V	Signal Rail	+V I/O (+5 V or +3.3 V)
B80	AD47			Address/Data 47
B81	AD45			Address/Data 45
B82	GND			Ground
B83	AD43			Address/Data 43
B84	AD41			Address/Data 41

B85	GND			Ground
B86	AD39			Address/Data 39
B87	AD37			Address/Data 37
B88	+5V	+3.3V	Signal Rail	+V I/O (+5 V or +3.3 V)
B89	AD35			Address/Data 35
B90	AD33			Address/Data 33
B91	GND			Ground
B92	RES			Reserved
B93	RES			Reserved
B94	GND			Ground