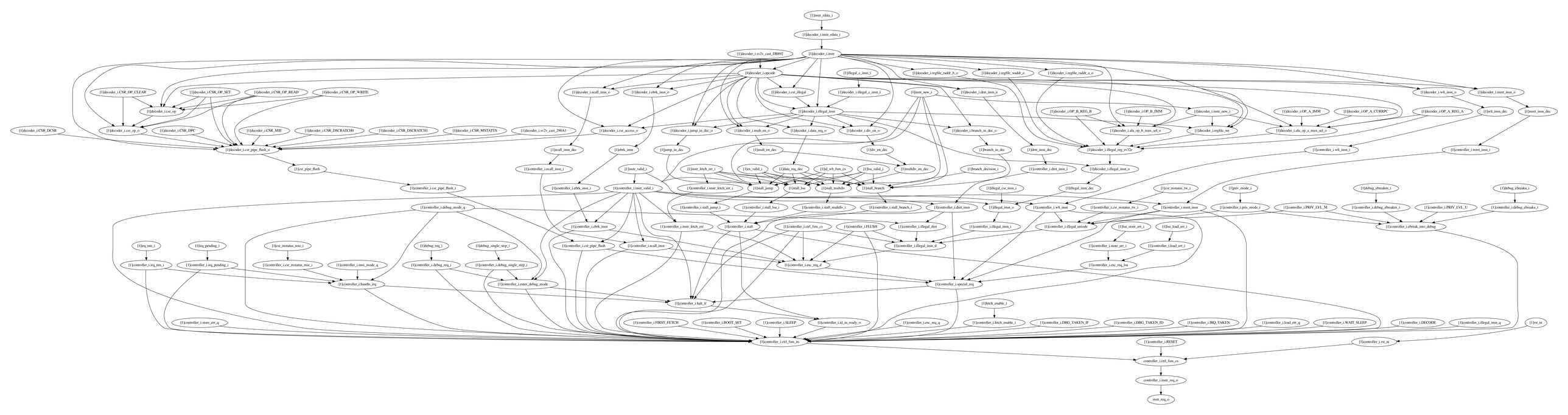
[1]instr_rdata_i
[1]decoder_i.instr_rdata_i
[1]decoder_i.instr
[1]decoder_i.opcode
[1]decoder_i.illegal_insn
[1]decoder_i.illegal_c_insn_o
[1]decoder_i.illegal_c_insn_i
[1]decoder_i.regfile_raddr_b_o
[1]decoder_i.regfile_waddr_o
[1]decoder_i.regfile_raddr_a_o
[1]decoder_i.csr_op
[1]decoder_i.csr_op_o
[1]decoder_i.CSR_OP_CLEAR
[1]decoder_i.CSR_OP_SET
[1]decoder_i.CSR_OP_READ
[1]decoder_i.CSR_OP_WRITE
[1]decoder_i.CSR_DCSR
[1]decoder_i.CSR_DPC
[1]decoder_i.CSR_MIE
[1]decoder_i.CSR_DSCRATCH0
[1]decoder_i.CSR_DSCRATCH1
[1]decoder_i.CSR_MSTATUS
[1]decoder_i.csr_pipe_flush_o
[1]csr_pipe_flush
[1]controller_i.csr_pipe_flush_i
[1]controller_i.debug_mode_q
[1]irq_nm_i
[1]controller_i.irq_nm_i
[1]irq_pending_i
[1]controller_i.irq_pending_i
[1]csr_mstatus_mie_i
[1]controller_i.csr_mstatus_mie_i
[1]controller_i.nmi_mode_q
[1]controller_i.handle_irq
[1]controller_i.store_err_q
[1]controller_i.halt_if
[1]controller_i.ecall_insn_o
[1]controller_i.ebrk_insn_o
[1]controller_i.csr_illegal
[1]controller_i.ecall_insn_dec
[1]controller_i.ecall_insn_i
[1]controller_i.ebrk_insn_i
[1]controller_i.ebrk_insn
[1]controller_i.csr_pipe_flush
[1]controller_i.ecall_insn
[1]controller_i.enter_debug_mode
[1]controller_i.debug_req_i
[1]controller_i.debug_single_step_i
[1]controller_i.jump_in_dec_o
[1]controller_i.mult_en_o
[1]controller_i.data_req_o
[1]controller_i.div_en_o
[1]controller_i.branch_in_dec_o
[1]controller_i.dret_insn_o
[1]controller_i.mret_insn_o
[1]controller_i.wfi_insn_o
[1]controller_i.csr_access_o
[1]jump_in_dec
[1]mult_en_dec
[1]div_en_dec
[1]branch_in_dec
[1]dret_insn_dec
[1]ebrk_insn
[1]instr_valid_i
[1]instr_fetch_err_i
[1]controller_i.instr_fetch_err_i
[1]controller_i.instr_valid_i
[1]controller_i.jump_in_dec
[1]controller_i.stall_jump_i
[1]controller_i.stall_bus_i
[1]controller_i.stall_multdiv_i
[1]controller_i.stall_branch_i
[1]controller_i.stall
[1]controller_i.ctrl_fsm_cs
[1]controller_i.FLUSH
[1]controller_i.illegal_dret
[1]controller_i.illegal_insn_d
[1]controller_i.exc_req_d
[1]controller_i.special_req
[1]controller_i.ctrl_fsm_ns
[1]controller_i.HIRST_FETCH
[1]controller_i.BOOT_SET
[1]controller_i.SLEEP
[1]controller_i.id_in_ready_o
[1]controller_i.exc_req_q
[1]controller_i.fetch_enable_i
[1]controller_i.DBG_TAKEN_IF
[1]controller_i.DBG_TAKEN_ID
[1]controller_i.IRQ_TAKEN
[1]controller_i.load_err_q
[1]controller_i.WAIT_SLEEP
[1]controller_i.DECODE
[1]controller_i.illegal_insn_q
[1]rst_ni
[1]controller_i.RESET
[1]controller_i.rst_ni
controller_i.ctrl_fsm_cs
controller_i.instr_req_o
instr_req_o
[1]illegal_csr_insn_i
[1]illegal_insn_dec
[1]illegal_insn_o
[1]illegal_insn_i
[1]controller_i.illegal_insn
[1]controller_i.illegal_insn_q
[1]controller_i.illegal_insn_d
[1]controller_i.illegal_dret
[1]controller_i.illegal_mmode
[1]controller_i.dret_insn
[1]controller_i.dret_insn_i
[1]controller_i.wfi_insn
[1]controller_i.wfi_insn_i
[1]controller_i.mret_insn
[1]controller_i.mret_insn_i
[1]controller_i.csr_mstatus_tw_i
[1]controller_i.csr_mstatus_tw_i
[1]priv_mode_i
[1]controller_i.priv_mode_i
[1]controller_i.PRIV_LVL_M
[1]controller_i.PRIV_LVL_U
[1]debug_ebreakm_i
[1]controller_i.debug_ebreakm_i
[1]debug_ebreaku_i
[1]controller_i.debug_ebreaku_i
[1]controller_i.ebreak_into_debug
[1]bus_store_err_i
[1]controller_i.store_err_i
[1]bus_load_err_i
[1]controller_i.load_err_i
[1]controller_i.exc_req_lsu
[1]fetch_enable_i
[1]controller_i.fetch_enable_i
[1]branch_decision_i