[1]decoder_i.regfile_waddr_o

[1]decoder_i.illegal_reg_rv32e

[1]decoder_i.illegal_insn_o

[1]controller_i.store_err_d

controller_i.store_err_q

controller_i.pc_set_o

[1]controller_i.debug_mode_q

[1]controller_i.debug_mode_d [1]controller_i.enter_debug_mode

[1]controller_i.ctrl_fsm_ns

[1]controller_i.handle_irq

[1]controller_i.illegal_dret

controller_i.debug_mode_q

[1]controller_i.halt_if

controller_i.ctrl_fsm_cs

[1]controller_i.nmi_mode_d

[1]controller_i.illegal_insn_d decoder_i.OP_A_CURRPC

decoder_i.alu_op_a_mux_sel_o

decoder_i.illegal_reg_rv32e

[1]decoder_i.ecall_insn_o

[1]ecall_insn_dec

[1]controller_i.ecall_insn_i

[1]debug_ebreaku_i

[1]controller_i.debug_ebreaku_i

[1]controller_i.ebreak_into_debug

[1]controller_i.store_err_i

[1]controller_i.exc_req_lsu

[1]controller_i.special_req

decoder_i.csr_op

decoder_i.csr_op_o

decoder_i.csr_pipe_flush_o

controller_i.nmi_mode_q

controller_i.handle_irq

[1]controller_i.store_err_q

[1]controller_i.ebrk_insn_i

[1]controller_i.ebrk_insn

[1]controller_i.exc_req_d

[1]instr_valid_i

[1]controller_i.instr_valid_i

[1]controller_i.instr_fetch_err [1]controller_i.mret_insn [1]controller_i.csr_pipe_flush [1]controller_i.dret_insn [1]controller_i.ecall_insn [1]illegal_insn_o [1]controller_i.wfi_insn

[1]controller_i.illegal_insn_i

controller_i.illegal_dret

controller_i.debug_req_i

pc_set_o

decoder_i.CSR_DSCRATCH1

csr_pipe_flush

controller_i.store_err_i

controller_i.exc_req_lsu

controller_i.special_req

[1]controller_i.id_in_ready_o

[1]controller_i.illegal_umode

[1]decoder_i.illegal_insn

[1]decoder_i.data_req_o [1]decoder_i.jump_in_dec_o [1]decoder_i.div_en_o [1]decoder_i.mult_en_o [1]decoder_i.branch_in_dec_o [1]decoder_i.csr_access_o [1]decoder_i.regfile_we

[1]branch_in_dec [1]decoder_i.csr_pipe_flush_o

[1]illegal_insn_dec

[1]jump_in_dec [1]mult_en_dec

[1]data_req_dec

[1]div_en_dec

branch_set_q

controller_i.branch_set_i

mret_insn_dec

controller_i.mret_insn_i

controller_i.mret_insn

[1]controller_i.debug_single_step_i

controller_i.instr_valid_i

controller_i.instr_fetch_err

controller_i.wfi_insn controller_i.csr_pipe_flush

controller_i.ecall_insn

controller_i.dret_insn

controller_i.ebrk_insn

controller_i.exc_req_d

controller_i.illegal_umode

controller_i.ebrk_insn_i

controller_i.csr_mstatus_mie_i [1]controller_i.RESET

controller_i.illegal_insn_d

decoder_i.csr_illegal

decoder_i.illegal_insn

decoder_i.illegal_insn_o

decoder_i.regfile_we decoder_i.jump_set_o decoder_i.csr_access_o

[1]instr_rdata_i

[1]decoder_i.instr_rdata_i

[1]decoder_i.instr

[1]instr_new_i

[1]stall_multdiv

[1]decoder_i.instr_new_i

[1]stall_jump [1]stall_lsu [1]stall_branch [1]branch_set_n

[1]controller_i.stall_multdiv_i

[1]decoder_i.alu_op_b_mux_sel_o [1]decoder_i.alu_op_a_mux_sel_o

[1]controller_i.stall_jump_i [1]controller_i.stall_lsu_i [1]controller_i.stall_branch_i

controller_i.exc_req_q

[1]decoder_i.csr_op

[1]decoder_i.csr_op_o

decoder_i.CSR_OP_CLEAR

debug_req_i

[1]controller_i.stall [1]controller_i.ctrl_fsm_cs

controller_i.illegal_insn_q

controller_i.ebreak_into_debug

[1]controller_i.debug_req_i

[1]ex_valid_i

controller_i.wfi_insn_i

[1]decoder_i.ebrk_insn_o

[1]ebrk_insn

[1]decoder_i.illegal_c_insn_i [1]decoder_i.CSR_OP_CLEAR

[1]illegal_c_insn_i

[1]controller_i.dret_insn_i

[1]csr_mstatus_tw_i

[1]controller_i.csr_mstatus_tw_i

decoder_i.OP_A_IMM

[1]controller_i.illegal_insn_q

[1]decoder_i.OP_A_IMM

[1]decoder_i.CSR_OP_WRITE

[1]csr_pipe_flush

[1]controller_i.instr_fetch_err_i

decoder_i.regfile_waddr_o

[1]decoder_i.regfile_raddr_a_o

[1]controller_i.IRQ_TAKEN [1]controller_i.FIRST_FETCH illegal_csr_insn_i illegal_insn_dec

illegal_insn_o

controller_i.illegal_insn_i

[1]controller_i.BOOT_SET [1]controller_i.SLEEP

[1]controller_i.csr_mstatus_mie_i

[1]controller_i.priv_mode_i

jump_set

controller_i.jump_set_i

decoder_i.OP_B_IMM

decoder_i.alu_op_b_mux_sel_o

[1]decoder_i.CSR_MIE

decoder_i.instr_rdata_i

decoder_i.instr

decoder_i.ebrk_insn_o decoder_i.ecall_insn_o

decoder_i.regfile_raddr_a_o

decoder_i.mret_insn_o decoder_i.dret_insn_o

decoder_i.regfile_raddr_b_o

decoder_i.wfi_insn_o

decoder_i.opcode

[1]decoder_i.dret_insn_o

[1]dret_insn_dec

[1]decoder_i.wfi_insn_o

[1]wfi_insn_dec

[1]controller_i.wfi_insn_i

[1]id_wb_fsm_cs

instr_fetch_err_i

controller_i.instr_fetch_err_i

controller_i.debug_ebreakm_i

decoder_i.OP_A_REG_A

[1]debug_ebreakm_i

[1]controller_i.debug_ebreakm_i [1]controller_i.PRIV_LVL_U

lsu_load_err_i

controller_i.load_err_i

debug_ebreaku_i

controller_i.debug_ebreaku_i

[1]decoder_i.CSR_DSCRATCH0

controller_i.FLUSH

[1]controller_i.PRIV_LVL_M

controller_i.dret_insn_i

decoder_i.CSR_DSCRATCH0

[1]decoder_i.CSR_OP_READ

[1]lsu_valid_i

[1]illegal_csr_insn_i

[1]decoder_i.CSR_DSCRATCH1

[1]fetch_enable_i

[1]controller_i.fetch_enable_i

instr_valid_i

decoder_i.CSR_OP_WRITE

csr_mstatus_tw_i

controller_i.csr_mstatus_tw_i

illegal_c_insn_i

[1]irq_nm_i decoder_i.illegal_c_insn_i

[1]controller_i.irq_nm_i

decoder_i.CSR_OP_SET

[1]controller_i.DBG_TAKEN_IF [1]controller_i.DBG_TAKEN_ID

[1]controller_i.csr_pipe_flush_i

[1]decoder_i.regfile_raddr_b_o

[1]csr_mstatus_mie_i instr_new_i

decoder_i.instr_new_i

[1]controller_i.exc_req_q

[1]controller_i.nmi_mode_q

[1]decoder_i.opcode

[1]decoder_i.mret_insn_o [1]decoder_i.csr_illegal

controller_i.PRIV_LVL_M

[1]decoder_i.OP_A_CURRPC

ebrk_insn

[1]debug_req_i

controller_i.PRIV_LVL_U

[1]debug_single_step_i

debug_single_step_i

controller_i.debug_single_step_i

[1]decoder_i.CSR_MSTATUS

[1]irq_pending_i

[1]controller_i.irq_pending_i

decoder_i.CSR_MSTATUS

[1]instr_fetch_err_i

csr_mstatus_mie_i

[1]controller_i.load_err_q

[1]controller_i.load_err_d

controller_i.load_err_q

ecall_insn_dec

controller_i.ecall_insn_i

[1]mret_insn_dec [1]decoder_i.OP_B_IMM

[1]controller_i.load_err_i

[1]rst_ni

[1]controller_i.rst_ni

[1]controller_i.mret_insn_i

dret_insn_dec

[1]lsu_load_err_i

[1]decoder_i.sv2v_cast_290A1

decoder_i.OP_B_REG_B

decoder_i.CSR_DPC

[1]decoder_i.CSR_DPC

[1]decoder_i.OP_A_REG_A

irq_pending_i

controller_i.irq_pending_i

controller_i.csr_pipe_flush_i

decoder_i.CSR_DCSR

[1]multdiv_en_dec

[1]controller_i.WAIT_SLEEP

wfi_insn_dec

decoder_i.sv2v_cast_290A1

[1]controller_i.FLUSH

controller_i.irq_nm_i

[1]branch_decision_i

[1]decoder_i.CSR_DCSR

decoder_i.CSR_OP_READ

lsu_store_err_i

decoder_i.CSR_MIE

[1]controller_i.DECODE debug_ebreakm_i

[1]decoder_i.OP_B_REG_B

decoder_i.sv2v_cast_DB892

[1]decoder_i.sv2v_cast_DB892

[1]priv_mode_i

irq_nm_i

instr_rdata_i

[1]decoder_i.CSR_OP_SET

priv_mode_i

controller_i.priv_mode_i

[1]lsu_store_err_i