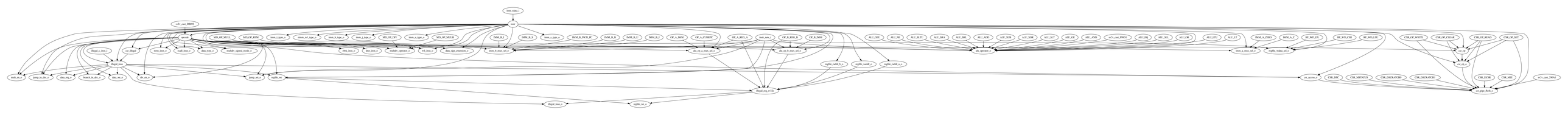

inst_sbus_i
inst
n<2>_cust_DBINT
opcode
MD_OP_MULL
MD_OP_MUM
inst_i_type_o
inst_rcl_type_o
inst_b_type_o
inst_j_type_o
MD_OP_DIV
inst_a_type_o
MD_OP_MULH
INM_B_I
INM_B_S
inst_s_type_o
INM_B_INCR_PC
INM_B_B
INM_B_U
INM_B_J
OP_A_INM
OP_A_CURRPC
OP_A_REG_A
inst_sev_i
OP_B_REG_B
OP_B_INM
ALU_GEU
ALU_NE
ALU_SLTU
ALU_SRA
ALU_SRL
ALU_ADD
ALU_SUB
ALU_XOR
ALU_SLT
ALU_GE
ALU_AND
n<2>_cust_FWDI
ALU_EQ
ALU_SLL
ALU_OR
ALU_LTU
ALU_LT
INM_A_ZERO
INM_A_Z
RF_WD_EX
RF_WD_CSR
RF_WD_LSU
CSR_OP_WRITE
CSR_OP_CLEAR
CSR_OP_READ
CSR_OP_SET
illegal_c_inst_i
cst_illegal
inst_inst_o
null_inst_o
data_type_o
multiplr_signal_mode_o
rfwb_inst_o
dist_inst_o
multiplr_operator_o
wb_inst_o
data_sign_extension_o
inst_b_inst_sel_o
ds_op_a_inst_sel_o
ds_op_b_inst_sel_o
ds_operator_o
inst_a_inst_sel_o
mgfile_wdata_sel_o
cst_op_o
cst_op_e
illegal_inst
mgfile_rsdst_b_o
mgfile_wsdst_o
mgfile_rsdst_a_o
mult_ds_o
jump_in_dec_o
data_req_o
branch_in_dec_o
data_rec_o
ds_ch_o
jump_sel_o
mgfile_wr
cst_access_o
CSR_DPC
CSR_MSTATUS
CSR_DSCRATCH0
CSR_DSCRATCH1
CSR_DCSR
CSR_MIE
n<2>_cust_INA1
illegal_reg_rs1b
cst_pipe_flush_o
illegal_inst_o
mgfile_wr_o