

# 1. Description

## 1.1. Project

Project Name	Haptic_Hearing_Aid
Board Name	custom
Generated with:	STM32CubeMX 6.12.1
Date	04/29/2025

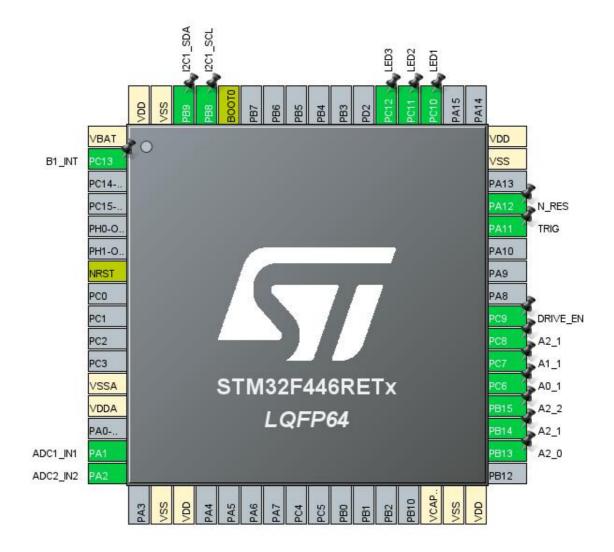
### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F446
MCU name	STM32F446RETx
MCU Package	LQFP64
MCU Pin number	64

## 1.3. Core(s) information

Core(s)	Arm Cortex-M4

# 2. Pinout Configuration

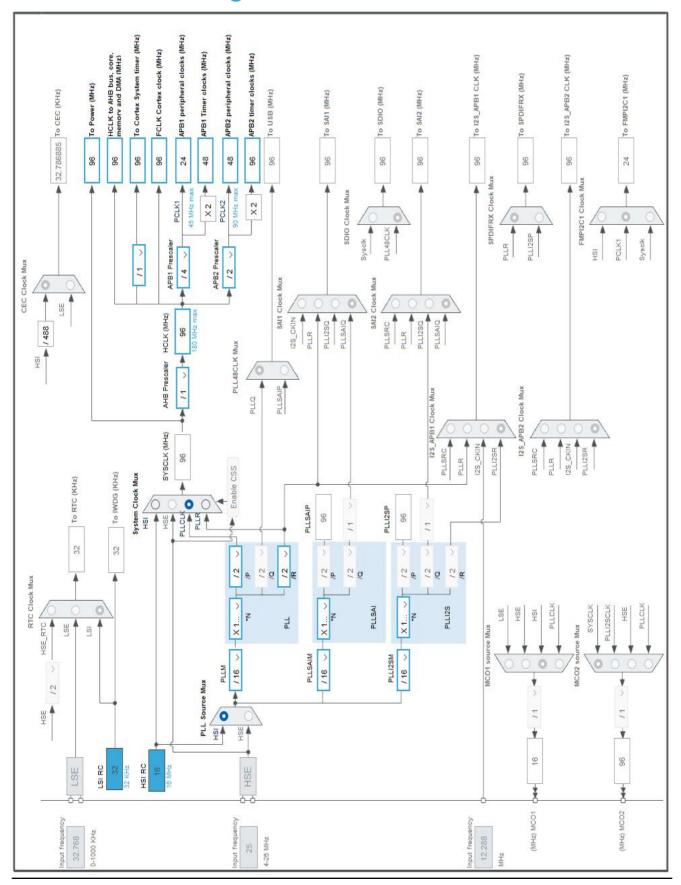


# 3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13	I/O	GPIO_EXTI13	B1_INT
7	NRST	Reset	GI IO_EXTIIS	D1_IIV1
12	VSSA	Power		
13	VDDA	Power		
15	PA1	I/O	ADC1_IN1	
16	PA2	I/O	ADC2_IN2	
18	VSS	Power		
19	VDD	Power		
30	VCAP_1	Power		
31	VSS	Power		
32	VDD	Power		
34	PB13 *	I/O	GPIO_Output	A2_0
35	PB14 *	I/O	GPIO_Output	A2_1
36	PB15 *	I/O	GPIO_Output	A2_2
37	PC6 *	I/O	GPIO_Output	A0_1
38	PC7 *	I/O	GPIO_Output	A1_1
39	PC8 *	I/O	GPIO_Output	A2_1
40	PC9 *	I/O	GPIO_Output	DRIVE_EN
44	PA11 *	I/O	GPIO_Output	TRIG
45	PA12 *	I/O	GPIO_Output	N_RES
47	VSS	Power		
48	VDD	Power		
51	PC10 *	I/O	GPIO_Output	LED1
52	PC11 *	I/O	GPIO_Output	LED2
53	PC12 *	I/O	GPIO_Output	LED3
60	воото	Boot		
61	PB8	I/O	I2C1_SCL	
62	PB9	I/O	I2C1_SDA	
63	VSS	Power		
64	VDD	Power		

<sup>\*</sup> The pin is affected with an I/O function

# 4. Clock Tree Configuration



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# 1. Power Consumption Calculator report

#### 1.1. Microcontroller Selection

Series	STM32F4
Line	STM32F446
MCU	STM32F446RETx
Datasheet	DS10693_Rev6

#### 1.2. Parameter Selection

Temperature	25
Vdd	3.3

### 1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

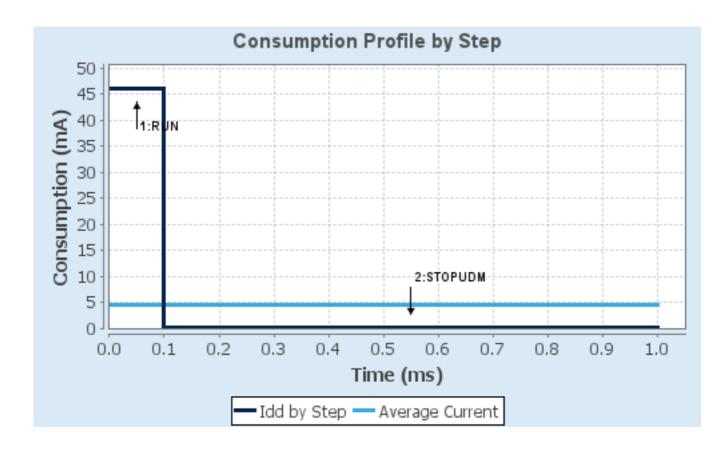
## 1.4. Sequence

	1	
Step	Step1	Step2
Mode	RUN	STOP UDM (Under Drive)
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	Scale1-High	No Scale
Fetch Type	RAM/FLASH/REGON/ART/P REFETCH	n/a
CPU Frequency	180 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator LP Flash-PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	46 mA	55 μA
Duration	0.1 ms	0.9 ms
DMIPS	225.0	0.0
Ta Max	98.02	104.99
Category	In DS Table	In DS Table

## 1.5. Results

Sequence Time	1 ms	Average Current	4.65 mA
Battery Life	1 month	Average DMIPS	225.0 DMIPS

#### 1.6. Chart



# 2. Software Project

### 2.1. Project Settings

Name	Value
Project Name	Haptic_Hearing_Aid
Project Folder	C:\Users\franc\STMCubeIDE_workspace\Master_Thesis\Haptic_Hearing_Aid
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F4 V1.28.1
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

## 2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

### 2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_DMA_Init	DMA
4	MX_I2C1_Init	I2C1
5	MX_TIM7_Init	TIM7
6	MX_ADC1_Init	ADC1
7	MX_ADC2_Init	ADC2

# 3. Peripherals and Middlewares Configuration

# 3.1. ADC1 mode: IN1

#### 3.1.1. Parameter Settings:

ADCs\_Common\_Settings:

Mode Independent mode

ADC\_Settings:

Clock Prescaler PCLK2 divided by 2

Resolution 12 bits (15 ADC Clock cycles)

Data Alignment Right alignment

Scan Conversion Mode Disabled
Continuous Conversion Mode Disabled
Discontinuous Conversion Mode Disabled
DMA Continuous Requests Disabled

End Of Conversion Selection EOC flag at the end of single channel conversion

ADC\_Regular\_ConversionMode:

Number Of Conversion 1

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None
Rank 1

Channel Channel 1
Sampling Time 3 Cycles

ADC\_Injected\_ConversionMode:

Number Of Conversions 0

WatchDog:

Enable Analog WatchDog Mode false

3.2. ADC2 mode: IN2

3.2.1. Parameter Settings:

ADCs\_Common\_Settings:

Mode Independent mode

ADC\_Settings:

Clock Prescaler PCLK2 divided by 2

Resolution 12 bits (15 ADC Clock cycles)

Data Alignment Right alignment

Scan Conversion Mode Disabled
Continuous Conversion Mode Disabled
Discontinuous Conversion Mode Disabled
DMA Continuous Requests Disabled

End Of Conversion Selection EOC flag at the end of single channel conversion

ADC\_Regular\_ConversionMode:

Number Of Conversion 1

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None
Rank 1

Channel Channel 2
Sampling Time 3 Cycles

ADC\_Injected\_ConversionMode:

Number Of Conversions 0

WatchDog:

Enable Analog WatchDog Mode false

3.3. I2C1 I2C: I2C

#### 3.3.1. Parameter Settings:

#### **Master Features:**

I2C Speed Mode Fast Mode \*

I2C Clock Speed (Hz) 400000

Fast Mode Duty Cycle Duty cycle Tlow/Thigh = 16/9 \*

**Slave Features:** 

Clock No Stretch Mode Disabled

Primary Address Length selection 7-bit

Dual Address Acknowledged Disabled

Primary slave address 0

General Call address detection Disabled

#### 3.4. RCC

#### 3.4.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3

Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 3 WS (4 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16

TIM Prescaler Selection Disabled

HSE Startup Timout Value (ms) 100

LSE Startup Timout Value (ms) 5000

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 3

Power Over Drive Disabled

3.5. SYS

Timebase Source: SysTick

3.6. TIM7

mode: Activated

3.6.1. Parameter Settings:

#### **Counter Settings:**

Prescaler (PSC - 16 bits value) 1599 \*

Counter Mode Up

Counter Period (AutoReload Register - 16 bits value) 299 \*

auto-reload preload Enable \*

#### **Trigger Output (TRGO) Parameters:**

<sup>\*</sup> User modified value

# 4. System Configuration

# 4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC1	PA1	ADC1_IN1	Analog mode	No pull-up and no pull-down	n/a	
ADC2	PA2	ADC2_IN2	Analog mode	No pull-up and no pull-down	n/a	
I2C1	PB8	I2C1_SCL	Alternate Function Open Drain	Pull-up *	Very High *	
	PB9	I2C1_SDA	Alternate Function Open Drain	Pull-up *	Very High	
GPIO	PC13	GPIO_EXTI13	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	n/a	B1_INT
	PB13	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	A2_0
	PB14	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	A2_1
	PB15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	A2_2
	PC6	PC6 GPIO_Output Output Push Pull No pull-up and no pull-down		Low	A0_1	
	PC7 GPIO_Output Output Push Pull No pull-up and no pull-down		Low	A1_1		
	PC8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	A2_1
	PC9	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DRIVE_EN
	PA11	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	TRIG
	PA12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	N_RES
	PC10	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED1
	PC11	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED2
	PC12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED3

#### 4.2. DMA configuration

DMA request	Stream	Direction	Priority
ADC1	DMA2_Stream0	Peripheral To Memory	Low
ADC2	DMA2_Stream2	Peripheral To Memory	Low

#### ADC1: DMA2\_Stream0 DMA request Settings:

Mode: Circular \*
Use fifo: Disable
Peripheral Increment: Disable
Memory Increment: Enable \*
Peripheral Data Width: Half Word
Memory Data Width: Half Word

#### ADC2: DMA2\_Stream2 DMA request Settings:

Mode: Circular \*
Use fifo: Disable
Peripheral Increment: Disable
Memory Increment: Enable \*
Peripheral Data Width: Half Word
Memory Data Width: Half Word

## 4.3. NVIC configuration

# 4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Pre-fetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	15	0	
EXTI line[15:10] interrupts	true	0	0	
TIM7 global interrupt	true	0	0	
DMA2 stream0 global interrupt	true	0	0	
DMA2 stream2 global interrupt	true	0	0	
PVD interrupt through EXTI line 16		unused		
Flash global interrupt	unused			
RCC global interrupt	unused			
ADC1, ADC2 and ADC3 interrupts	unused			
I2C1 event interrupt	unused			
I2C1 error interrupt	unused			
FPU global interrupt	unused			

## 4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
EXTI line[15:10] interrupts	false	true	true
TIM7 global interrupt	false	true	true
DMA2 stream0 global interrupt	false	true	true
DMA2 stream2 global interrupt	false	true	true

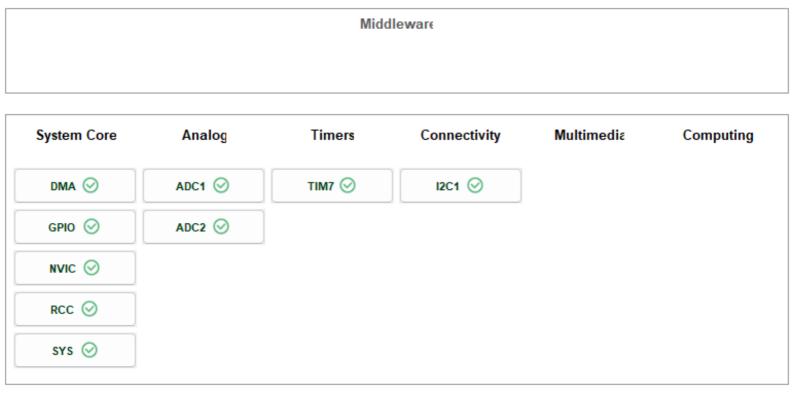
Haptic_Hearing_	Aid	Projec	ct
Configura	tion	Repo	rt

* User modified value			

# 5. System Views

5.1. Category view

5.1.1. Current



## 6. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32f446\_bsdl.zip https://www.st.com/resource/en/ibis\_model/stm32f446\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4-svd.zip

Description

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32\_eval-

tools\_portfolio.pdf

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stm32-family-overview.pdf

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and-smart-i-os.pdf

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guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

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microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

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driver-enable-signal-for-rs485-and-iolink-communications-with-the-

- stm32s-usart-stmicroelectronics.pdf
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