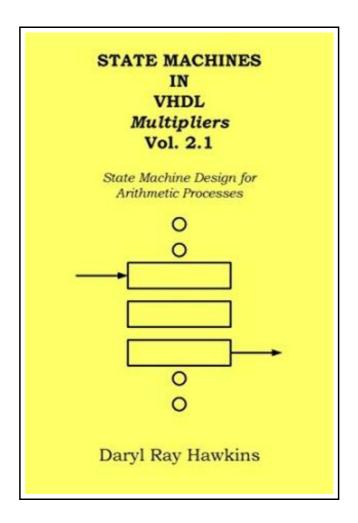
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Createspace, United States, 2013. Paperback. Book Condition: New. 229 x 152 mm. Language: English . Brand New Book ***** Print on Demand *****. This booklet contains all the information required to implement multiplier circuits in FPGA and ASIC devices as state machines. Four designs are provided ranging from simple to high performance with suggestions for even higher performance. Also included is a comprehensive chapter on normalizing and rounding, fixed-point as well as floating-point. The use of carry-save adders for partial product multiplication. Combining embedded array multipliers available in most FPGA devices for fashioning high perform large operand multipliers. Each design covered has a fully coded and fully functional design in VHDL as a state machine, with normalizing, rounding, and overflow included. Each design is also scalable in terms of operand size and the size of integer and fractional portions.

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