



1. Description

1.1. Project

| | |
|-----------------|-------------------|
| Project Name | Project |
| Board Name | custom |
| Generated with: | STM32CubeMX 6.8.0 |
| Date | 04/18/2023 |

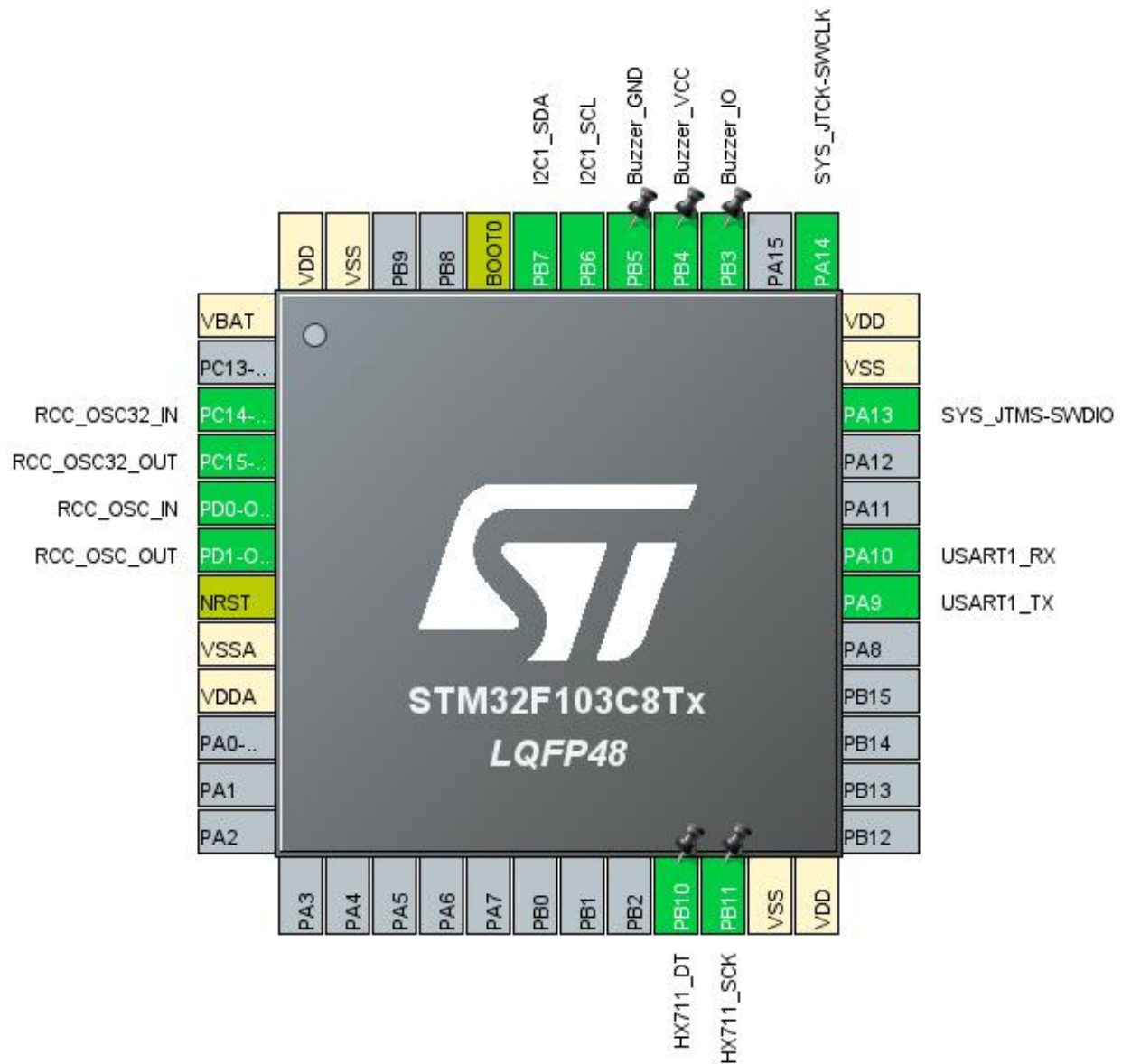
1.2. MCU

| | |
|----------------|---------------|
| MCU Series | STM32F1 |
| MCU Line | STM32F103 |
| MCU name | STM32F103C8Tx |
| MCU Package | LQFP48 |
| MCU Pin number | 48 |

1.3. Core(s) information

| | |
|---------|---------------|
| Core(s) | Arm Cortex-M3 |
|---------|---------------|

2. Pinout Configuration

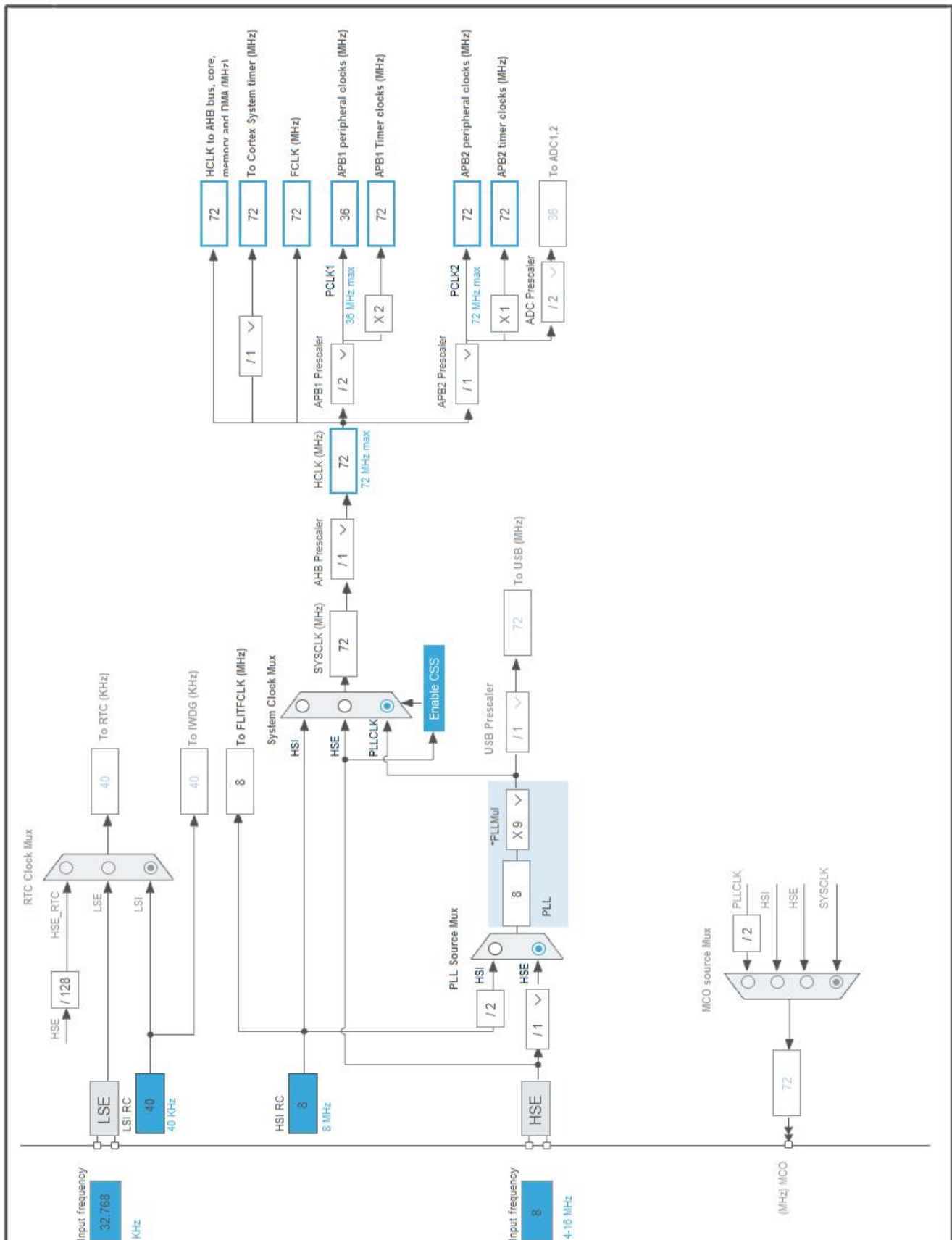


3. Pins Configuration

| Pin Number LQFP48 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|----------------------|---------------------------------------|----------|--------------------------|------------|
| 1 | VBAT | Power | | |
| 3 | PC14-OSC32_IN | I/O | RCC_OSC32_IN | |
| 4 | PC15-OSC32_OUT | I/O | RCC_OSC32_OUT | |
| 5 | PD0-OSC_IN | I/O | RCC_OSC_IN | |
| 6 | PD1-OSC_OUT | I/O | RCC_OSC_OUT | |
| 7 | NRST | Reset | | |
| 8 | VSSA | Power | | |
| 9 | VDDA | Power | | |
| 21 | PB10 * | I/O | GPIO_Input | HX711_DT |
| 22 | PB11 * | I/O | GPIO_Output | HX711_SCK |
| 23 | VSS | Power | | |
| 24 | VDD | Power | | |
| 30 | PA9 | I/O | USART1_TX | |
| 31 | PA10 | I/O | USART1_RX | |
| 34 | PA13 | I/O | SYS_JTMS-SWDIO | |
| 35 | VSS | Power | | |
| 36 | VDD | Power | | |
| 37 | PA14 | I/O | SYS_JTCK-SWCLK | |
| 39 | PB3 * | I/O | GPIO_Output | Buzzer_IO |
| 40 | PB4 * | I/O | GPIO_Output | Buzzer_VCC |
| 41 | PB5 * | I/O | GPIO_Output | Buzzer_GND |
| 42 | PB6 | I/O | I2C1_SCL | |
| 43 | PB7 | I/O | I2C1_SDA | |
| 44 | BOOT0 | Boot | | |
| 47 | VSS | Power | | |
| 48 | VDD | Power | | |

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

| Name | Value |
|-----------------------------------|--|
| Project Name | Project |
| Project Folder | D:\Documents\GithubProjects\STM32_IntelligentElectronicScaleSystem\Project |
| Toolchain / IDE | MDK-ARM V5.32 |
| Firmware Package Name and Version | STM32Cube FW_F1 V1.8.4 |
| Application Structure | Advanced |
| Generate Under Root | No |
| Do not generate the main() | No |
| Minimum Heap Size | 0x200 |
| Minimum Stack Size | 0x400 |

5.2. Code Generation Settings

| Name | Value |
|---|---|
| STM32Cube MCU packages and embedded software | Copy all used libraries into the project folder |
| Generate peripheral initialization as a pair of '.c/.h' files | Yes |
| Backup previously generated files when re-generating | No |
| Keep User Code when re-generating | Yes |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power consumption) | No |
| Enable Full Assert | No |

5.3. Advanced Settings - Generated Function Calls

| Rank | Function Name | Peripheral Instance Name |
|------|---------------------|--------------------------|
| 1 | SystemClock_Config | RCC |
| 2 | MX_GPIO_Init | GPIO |
| 3 | MX_I2C1_Init | I2C1 |
| 4 | MX_USART1_UART_Init | USART1 |

6. Power Consumption Calculator report

6.1. Microcontroller Selection

| | |
|-----------|---------------|
| Series | STM32F1 |
| Line | STM32F103 |
| MCU | STM32F103C8Tx |
| Datasheet | DS5319_Rev17 |

6.2. Parameter Selection

| | |
|-------------|-----|
| Temperature | 25 |
| Vdd | 3.3 |

6.3. Battery Selection

| | |
|-------------------|-----------------|
| Battery | Li-SOCL2(A3400) |
| Capacity | 3400.0 mAh |
| Self Discharge | 0.08 %/month |
| Nominal Voltage | 3.6 V |
| Max Cont Current | 100.0 mA |
| Max Pulse Current | 200.0 mA |
| Cells in series | 1 |
| Cells in parallel | 1 |

6.4. Sequence

| | | |
|-------------------------------|-------------|--------------|
| Step | Step1 | Step2 |
| Mode | RUN | STOP |
| Vdd | 3.3 | 3.3 |
| Voltage Source | Battery | Battery |
| Range | No Scale | No Scale |
| Fetch Type | FLASH | n/a |
| CPU Frequency | 72 MHz | 0 Hz |
| Clock Configuration | HSE PLL | Regulator LP |
| Clock Source Frequency | 8 MHz | 0 Hz |
| Peripherals | | |
| Additional Cons. | 0 mA | 0 mA |
| Average Current | 27 mA | 14 μ A |
| Duration | 0.1 ms | 0.9 ms |
| DMIPS | 90.0 | 0.0 |
| Ta Max | 100.1 | 105 |
| Category | In DS Table | In DS Table |

6.5. Results

| | | | |
|---------------|-------------------------------|-----------------|------------|
| Sequence Time | 1 ms | Average Current | 2.71 mA |
| Battery Life | 1 month, 21 days, 17 hours | Average DMIPS | 61.0 DMIPS |

6.6. Chart



7. Peripherals and Middlewares Configuration

7.1. I2C1

I2C: I2C

7.1.1. Parameter Settings:

Master Features:

| | |
|----------------------|---------------|
| I2C Speed Mode | Standard Mode |
| I2C Clock Speed (Hz) | 100000 |

Slave Features:

| | |
|----------------------------------|----------|
| Clock No Stretch Mode | Disabled |
| Primary Address Length selection | 7-bit |
| Dual Address Acknowledged | Disabled |
| Primary slave address | 0 |
| General Call address detection | Disabled |

7.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

Low Speed Clock (LSE) : Crystal/Ceramic Resonator

7.2.1. Parameter Settings:

System Parameters:

| | |
|-------------------|--------------------|
| VDD voltage (V) | 3.3 |
| Prefetch Buffer | Enabled |
| Flash Latency(WS) | 2 WS (3 CPU cycle) |

RCC Parameters:

| | |
|--------------------------------|------|
| HSI Calibration Value | 16 |
| HSE Startup Timeout Value (ms) | 100 |
| LSE Startup Timeout Value (ms) | 5000 |

7.3. SYS

Debug: Serial Wire

Timebase Source: SysTick

7.4. USART1

Mode: Asynchronous

7.4.1. Parameter Settings:

Basic Parameters:

| | |
|-------------|---------------------------|
| Baud Rate | 115200 |
| Word Length | 8 Bits (including Parity) |
| Parity | None |
| Stop Bits | 1 |

Advanced Parameters:

| | |
|----------------|----------------------|
| Data Direction | Receive and Transmit |
| Over Sampling | 16 Samples |

* User modified value

8. System Configuration

8.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|--------|----------------|----------------|-------------------------------|-----------------------------|---------------|------------|
| I2C1 | PB6 | I2C1_SCL | Alternate Function Open Drain | n/a | High * | |
| | PB7 | I2C1_SDA | Alternate Function Open Drain | n/a | High * | |
| RCC | PC14-OSC32_IN | RCC_OSC32_IN | n/a | n/a | n/a | |
| | PC15-OSC32_OUT | RCC_OSC32_OUT | n/a | n/a | n/a | |
| | PD0-OSC_IN | RCC_OSC_IN | n/a | n/a | n/a | |
| | PD1-OSC_OUT | RCC_OSC_OUT | n/a | n/a | n/a | |
| SYS | PA13 | SYS_JTMS-SWDIO | n/a | n/a | n/a | |
| | PA14 | SYS_JTCK-SWCLK | n/a | n/a | n/a | |
| USART1 | PA9 | USART1_TX | Alternate Function Push Pull | n/a | High * | |
| | PA10 | USART1_RX | Input mode | No pull-up and no pull-down | n/a | |
| GPIO | PB10 | GPIO_Input | Input mode | Pull-up * | n/a | HX711_DT |
| | PB11 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | High * | HX711_SCK |
| | PB3 | GPIO_Output | Output Push Pull | Pull-down * | High * | Buzzer_IO |
| | PB4 | GPIO_Output | Output Push Pull | Pull-up * | High * | Buzzer_VCC |
| | PB5 | GPIO_Output | Output Push Pull | Pull-down * | High * | Buzzer_GND |

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

| Interrupt Table | Enable | Preenmption Priority | SubPriority |
|---|--------|----------------------|-------------|
| Non maskable interrupt | true | 0 | 0 |
| Hard fault interrupt | true | 0 | 0 |
| Memory management fault | true | 0 | 0 |
| Prefetch fault, memory access fault | true | 0 | 0 |
| Undefined instruction or illegal state | true | 0 | 0 |
| System service call via SWI instruction | true | 0 | 0 |
| Debug monitor | true | 0 | 0 |
| Pendable request for system service | true | 0 | 0 |
| System tick timer | true | 15 | 0 |
| PVD interrupt through EXTI line 16 | unused | | |
| Flash global interrupt | unused | | |
| RCC global interrupt | unused | | |
| I2C1 event interrupt | unused | | |
| I2C1 error interrupt | unused | | |
| USART1 global interrupt | unused | | |

8.3.2. NVIC Code generation

| Enabled interrupt Table | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|---|-----------------------------------|----------------------|------------------|
| Non maskable interrupt | false | true | false |
| Hard fault interrupt | false | true | false |
| Memory management fault | false | true | false |
| Prefetch fault, memory access fault | false | true | false |
| Undefined instruction or illegal state | false | true | false |
| System service call via SWI instruction | false | true | false |
| Debug monitor | false | true | false |
| Pendable request for system service | false | true | false |
| System tick timer | false | true | true |

* User modified value

9. System Views

9.1. Category view

9.1.1. Current

Middleware

System Core

Analog

Timers

Connectivity

Computing

DMA

GPIO 

NVIC 

RCC 

SYS 

I2C1 

USART1 

10. Docs & Resources

| Type | Link |
|-------------------------|---|
| BSDL files | https://www.st.com/resource/en/bsdl_model/stm32f1_bsd1.zip |
| IBIS models | https://www.st.com/resource/en/ibis_model/stm32ibis.zip |
| System View Description | https://www.st.com/resource/en/svd/stm32f1_svd.zip |
| BSDL files | https://www.st.com/resource/en/bsdl_model/stm32f1_bsd1.zip |
| IBIS models | https://www.st.com/resource/en/ibis_model/stm32ibis.zip |
| System View Description | https://www.st.com/resource/en/svd/stm32f1_svd.zip |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf |
| Training Material | https://www.st.com/resource/en/sales_guide/sg_sc2155.pdf |
| Brochures | https://www.st.com/resource/en/brochure/breveco0518.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32nucleo.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstmcsuite.pdf |
| Flyers | https://www.st.com/resource/en/flyer/fldpstpf11120.pdf |
| Product Certifications | https://www.st.com/resource/en/certification_document/1239988349.pdf |
| Product Certifications | https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an1709-emc-design- |

guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2586-getting-started-with-stm32f10xxx-hardware-development-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2604-stm32f101xx-and-stm32f103xx-rtc-calibration-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3095-stevalisv002v1-stevalisv002v2-3-kw-gridconnected-pv-system-based-on-the-stm32f103xx-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3108-stlm75-firmware-library-for-the-stm32f10x-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3156-usb-dfu-

protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3422-migration-of-microcontroller-applications-from-stm32f1-to-stm32l1-series-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3427-migrating-a-microcontroller-application-from-stm32f1-to-stm32f2-series-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3429-stm32-proprietary-code-protection-overview-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3961-stevalime003v1-demonstration-board-based-on-the-sthv748-ultrasound-pulser-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4013-stm32-crossseries-timer-overview-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4070-250-w-grid-connected-microinverter-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an4228-migrating-from-stm32f1-series-to-stm32f3-series-microcontrollers-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an4277-using-stm32-device-pwm-shutdown-features-for-motor-control-and-digital-power-conversion-stmicroelectronics.pdf

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- Application Notes https://www.st.com/resource/en/application_note/an4649-migrating-from-stm32f1-series-to-stm32l4-series--stm32l4-series-microntrrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf
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& Software

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& Software

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Application Notes https://www.st.com/resource/en/application_note/an2557-stm32f10x-in-application-programming-using-the-usart-stmicroelectronics.pdf
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for related Tools
& Software

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for related Tools
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Application Notes https://www.st.com/resource/en/application_note/an2598-smartcard-interface-with-stm32f10x-and-stm32l1xx-microcontrollers-stmicroelectronics.pdf
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Application Notes https://www.st.com/resource/en/application_note/an2629-stm32f101xx-stm32f102xx-and-stm32f103xx-lowpower-modes-stmicroelectronics.pdf
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for related Tools
& Software

Application Notes https://www.st.com/resource/en/application_note/an2784-using-the-highdensity-stm32f10xxx-fsmc-peripheral-to-drive-external-memories-stmicroelectronics.pdf
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Application Notes https://www.st.com/resource/en/application_note/an2790-tft-lcd-interfacing-with-the-highdensity-stm32f10xxx-fsmc-stmicroelectronics.pdf
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for related Tools
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Application Notes https://www.st.com/resource/en/application_note/an2824-stm32f10xxx-ic-

for related Tools [optimized-examples-stmicroelectronics.pdf](#)
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for related Tools [implemented-on-stm32-microcontroller-stmicroelectronics.pdf](#)
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for related Tools [internal-rc-oscillator-hsi-calibration-stmicroelectronics.pdf](#)
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