

1. Description

1.1. Project

Project Name	Project
Board Name	custom
Generated with:	STM32CubeMX 6.8.0
Date	04/25/2023

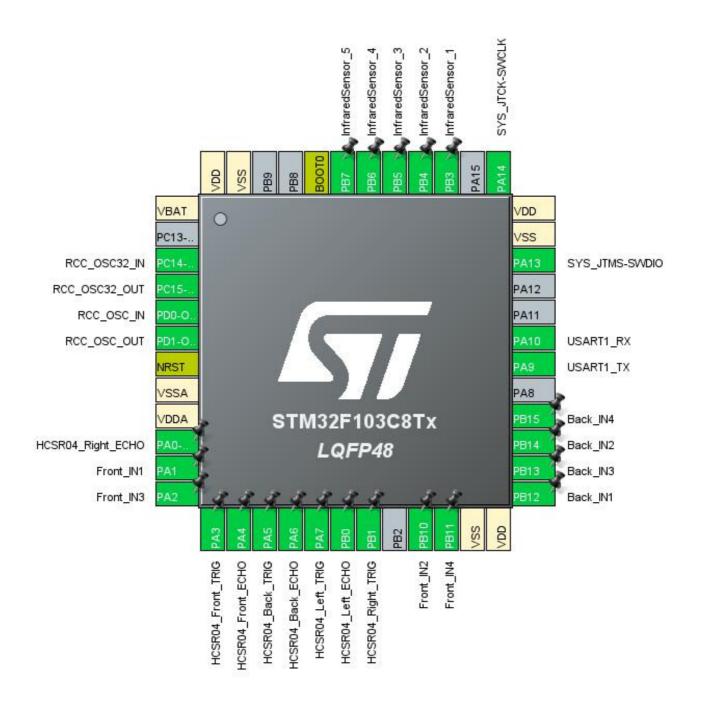
1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

1.3. Core(s) information

Core(s)	Arm Cortex-M3

2. Pinout Configuration



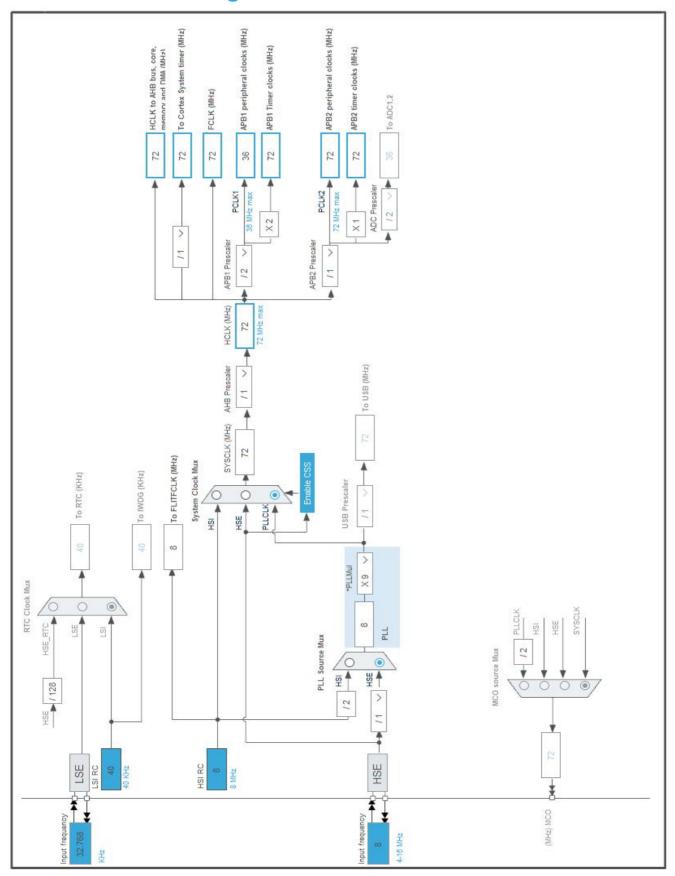
3. Pins Configuration

Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP48	(function after		Function(s)	
	reset)			
1	VBAT	Power		
3	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0-WKUP *	I/O	GPIO_Input	HCSR04_Right_ECHO
11	PA1 *	I/O	GPIO_Output	Front_IN1
12	PA2 *	I/O	GPIO_Output	Front_IN3
13	PA3 *	I/O	GPIO_Output	HCSR04_Front_TRIG
14	PA4 *	I/O	GPIO_Input	HCSR04_Front_ECHO
15	PA5 *	I/O	GPIO_Output	HCSR04_Back_TRIG
16	PA6 *	I/O	GPIO_Input	HCSR04_Back_ECHO
17	PA7 *	I/O	GPIO_Output	HCSR04_Left_TRIG
18	PB0 *	I/O	GPIO_Input	HCSR04_Left_ECHO
19	PB1 *	I/O	GPIO_Output	HCSR04_Right_TRIG
21	PB10 *	I/O	GPIO_Output	Front_IN2
22	PB11 *	I/O	GPIO_Output	Front_IN4
23	VSS	Power		
24	VDD	Power		
25	PB12 *	I/O	GPIO_Output	Back_IN1
26	PB13 *	I/O	GPIO_Output	Back_IN3
27	PB14 *	I/O	GPIO_Output	Back_IN2
28	PB15 *	I/O	GPIO_Output	Back_IN4
30	PA9	I/O	USART1_TX	
31	PA10	I/O	USART1_RX	
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
39	PB3 *	I/O	GPIO_Input	InfraredSensor_1
40	PB4 *	I/O	GPIO_Input	InfraredSensor_2
41	PB5 *	I/O	GPIO_Input	InfraredSensor_3
42	PB6 *	I/O	GPIO_Input	InfraredSensor_4

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
43	PB7 *	I/O	GPIO_Input	InfraredSensor_5
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	Project
Project Folder	D:\Documents\GithubProjects\STM32_IntelligentTrackingCar\Project
Toolchain / IDE	MDK-ARM V5.32
Firmware Package Name and Version	STM32Cube FW_F1 V1.8.4
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_USART1_UART_Init	USART1
4	MX_TIM1_Init	TIM1

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103C8Tx
Datasheet	DS5319_Rev17

6.2. Parameter Selection

Temperature	25
Vdd	3.3

6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

6.4. Sequence

	la	
Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	72 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	27 mA	14 µA
Duration	0.1 ms	0.9 ms
DMIPS	90.0	0.0
Ta Max	100.1	105
Category	In DS Table	In DS Table

6.5. Results

Sequence Time	1 ms	Average Current	2.71 mA
Battery Life	1 month, 21 days,	Average DMIPS	61.0 DMIPS
	17 hours		

6.6. Chart



7. Peripherals and Middlewares Configuration

7.1. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator Low Speed Clock (LSE): Crystal/Ceramic Resonator

7.1.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

7.2. SYS

Debug: Serial Wire

Timebase Source: SysTick

7.3. TIM1

Clock Source: Internal Clock

7.3.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)

Counter Mode

Counter Period (AutoReload Register - 16 bits value)

Internal Clock Division (CKD)

Repetition Counter (RCR - 8 bits value)

O

71 *

Up

65535

No Division

Repetition Counter (RCR - 8 bits value) 0
auto-reload preload Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit) Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx_EGR)

7.4. **USART1**

Mode: Asynchronous

7.4.1. Parameter Settings:

Basic Parameters:

Baud Rate 9600 *

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

^{*} User modified value

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
	PD0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
USART1	PA9	USART1_TX	Alternate Function Push Pull	n/a	High *	
	PA10	USART1_RX	Input mode	No pull-up and no pull-down	n/a	
GPIO	PA0-WKUP	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	HCSR04_Right_ECHO
	PA1	GPIO_Output	Output Push Pull	Pull-down *	High *	Front_IN1
	PA2	GPIO_Output	Output Push Pull	Pull-down *	High *	Front_IN3
	PA3	GPIO_Output	Output Push Pull	Pull-down *	High *	HCSR04_Front_TRIG
	PA4	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	HCSR04_Front_ECHO
	PA5	GPIO_Output	Output Push Pull	Pull-down *	High *	HCSR04_Back_TRIG
	PA6	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	HCSR04_Back_ECHO
	PA7	GPIO_Output	Output Push Pull	Pull-down *	High *	HCSR04_Left_TRIG
	PB0	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	HCSR04_Left_ECHO
	PB1	GPIO_Output	Output Push Pull	Pull-down *	High *	HCSR04_Right_TRIG
	PB10	GPIO_Output	Output Push Pull	Pull-down *	High *	Front_IN2
	PB11	GPIO_Output	Output Push Pull	Pull-down *	High *	Front_IN4
	PB12	GPIO_Output	Output Push Pull	Pull-down *	High *	Back_IN1
	PB13	GPIO_Output	Output Push Pull	Pull-down *	High *	Back_IN3
	PB14	GPIO_Output	Output Push Pull	Pull-down *	High *	Back_IN2
	PB15	GPIO_Output	Output Push Pull	Pull-down *	High *	Back_IN4
	PB3	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	InfraredSensor_1
	PB4	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	InfraredSensor_2
	PB5	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	InfraredSensor_3

IP	Pin	Signal	GPIO mode	GPIO pull/up pull	Max	User Label
				down	Speed	
	PB6	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	InfraredSensor_4
	PB7	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	InfraredSensor_5

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority		
Non maskable interrupt	true	0	0		
Hard fault interrupt	true	0	0		
Memory management fault	true	0	0		
Prefetch fault, memory access fault	true	0	0		
Undefined instruction or illegal state	true	0	0		
System service call via SWI instruction	true	0	0		
Debug monitor	true	0	0		
Pendable request for system service	true	0	0		
System tick timer	true	15	0		
PVD interrupt through EXTI line 16	unused				
Flash global interrupt		unused	unused		
RCC global interrupt		unused			
TIM1 break interrupt	unused				
TIM1 update interrupt		unused			
TIM1 trigger and commutation interrupts	unused				
TIM1 capture compare interrupt	unused				
USART1 global interrupt		unused			

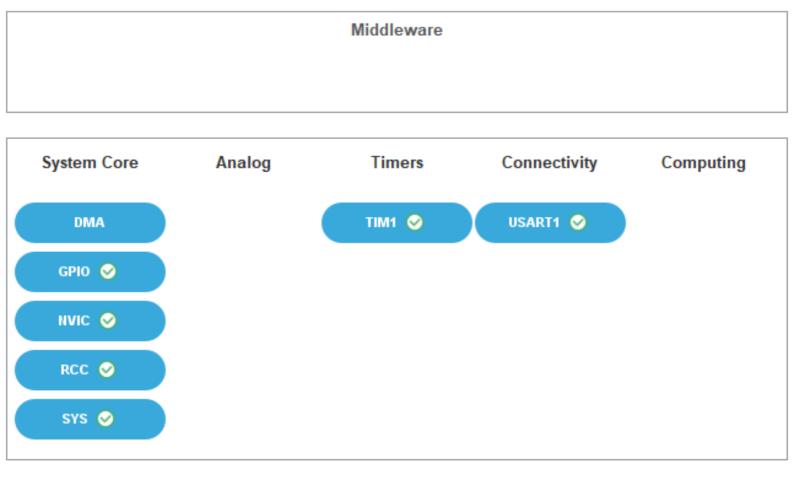
8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init	Generate IRQ	Call HAL handler
	sequence ordering	handler	
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

* User modified value

9. System Views

- 9.1. Category view
- 9.1.1. Current



10. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl_model/stm32f1_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis_model/stm32ibis.zip

System View https://www.st.com/resource/en/svd/stm32f1_svd.zip

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Description

Presentations https://www.st.com/resource/en/product_presentation/stm32-

stm8_embedded_software_solutions.pdf

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- Application Notes https://www.st.com/resource/en/application_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3095-stevalisv002v1-stevalisv002v2-3-kw-gridconnected-pv-system-based-on-the-stm32f103xx-stmicroelectronics.pdf
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- protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
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