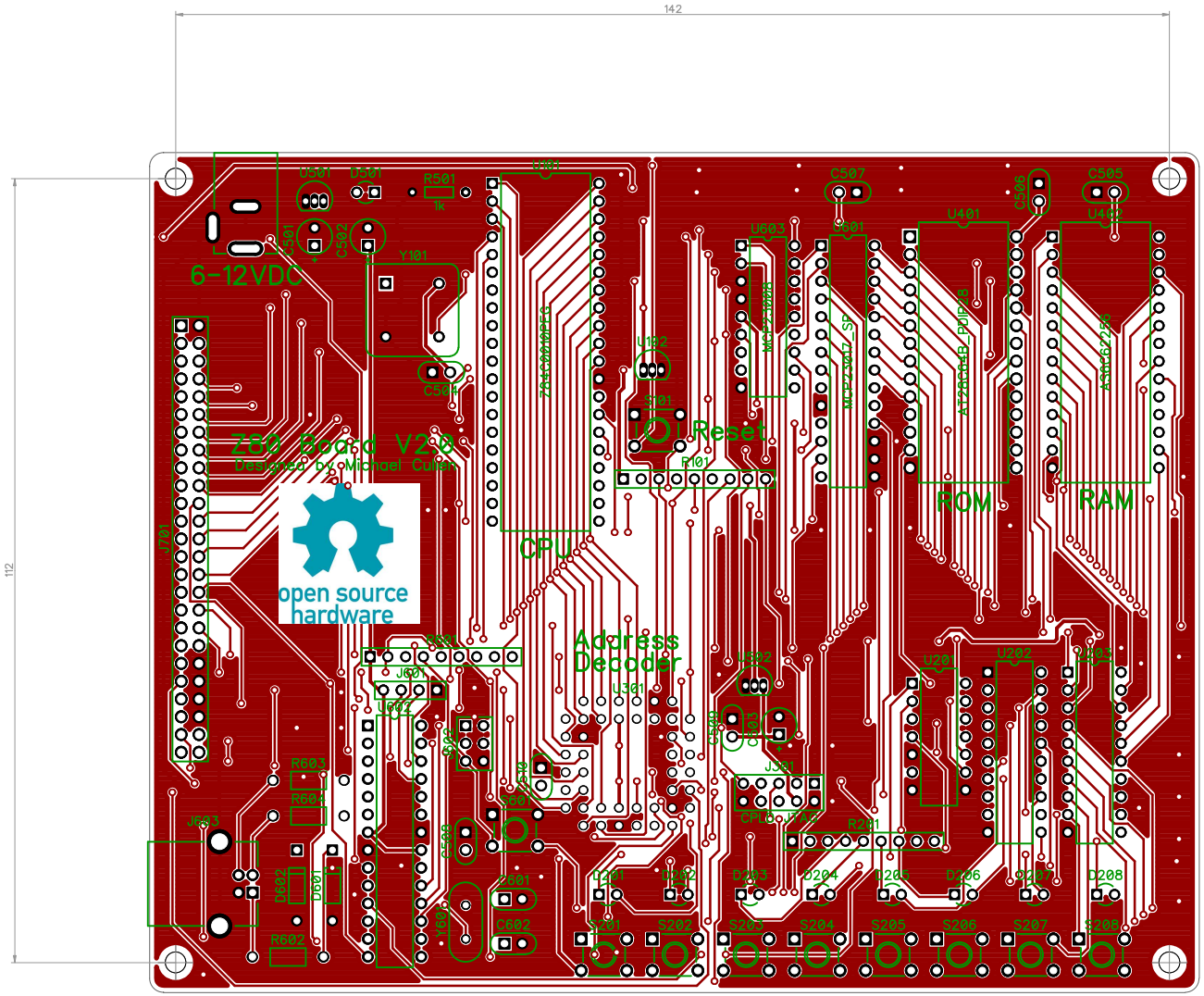
142
112
6-12VDC
Z80 Board V2.0
Designed By Michael Cullen
open source
hardware
CPU
Reset
Address
Decoder
ROM
RAM
ZASCOMPEC
MCF73047_SP
ATZAC64B_PDIP40
ASHC62256
JTAG
J601
J603
J301
U601
U301
U401
U403
U201
U202
U203
U603
U102
Y101
C501
C502
C503
C505
C506
C507
C508
C509
C601
C602
C603
C604
C605
C606
C607
C608
C609
C610
C611
C612
C613
C614
C615
C616
C617
C618
C619
C620
C621
C622
C623
C624
C625
C626
C627
C628
C629
C630
C631
C632
C633
C634
C635
C636
C637
C638
C639
C640