



JīngJīng HAO
Résidence F. Faure
21 rue Robespierre
91120 PALAISEAU

06.50.25.83.37
haojingjing.hjj@foxmail.com

<https://lightercoder.github.io/jingjinghao.github.io/>

Diploma

Master Degree in Control Science and Engineering, Tsinghua University

(Sept. 2011 - Jul. 2014, average grade : 82.9/100)

- Produced research work on evaluating RNA mapping tools
- Worked as server cluster administrator of bioinformatics laboratory
- Helped as teaching assistant for statistic course

Bachelor Degree in Software Engineering, Harbin Institute of Technology

(Aug. 2007 - Jul. 2011, average grade : 90.05/100)

- Won Honorable Mention of Interdisciplinary Contest in Modelling, 2010
- Gained IBM China Scholarship for Excellent Students, 2010
- Earned China National Scholarship(2008,2010) and People's Scholarship(08-09)

Professional Experience

PhD Candidate majored in Signal and Image Processing, Oct. 2017 - Oct. 2018

à l'Ecole Polytechnique (Palaiseau, France), financed by l'Institut national de recherche en informatique et en automatisation(Inria), worked and studied in Université Paris-Saclay.

- Analysed time series encephalographic data recorded from EEG headset device and tried classifying two states of eye-open/ eye-close states or even detecting blinking events from raw data.
- Explored pipeline for preprocessing noisy EEG recording based on EEGLab Matlab package.
- Learned PDE and calculated Finite Element Matrix coded by hand in Matlab/Maple.

Software Developer(IC-II) of Network Interface Controller Team, Jul. 2014 - Mar. 2017

at Oracle (Beijing, China) worked with system engineers in Oracle Asia Research and Development(Beijing) Center on X86 and SPARC platforms for Solaris 11/12.

- Maintaining normal functioning of Solaris network driver include ixgbe/igb on Intel NICs at Oracle M2/X5/T7 series platforms.
- Fixing and reproducing bugs reported by QA/QE teams and from customers under lines of Sun development process.
- Developing driver such as i40e and e1000g for higher speed NICs and implementing new features for kernel module.
- Fixed 28 bugs in related to load/unload, hotplug or vf/vnic and flapping links by integrating 347 lines of code to Solaris 11.x/12 assisted by experiences of senior engineers and by using debuting tools like GDB, DTrace or printf.
- Implemented new features include flow control and rx/tx ring size configuration, and also auto-negotiation upon connected ports and firmware parameter consultation.
- Designed complete test strategy for validating alien C lines into gate before RTI.
- Designed show and lead young team members to perform group dance in annual party.

Tech Lead of Embedded System Project, intern Jul. - Aug. 2009

Neusoft Company (Dalian, China) “Intelligent Electrical Appliances Control System(IEACS)” was student project which simulated signal communication between PC and Renesas Electronics to implement remote wireless control within internal space thus performing intelligent home automation.

1. Designed, modularised system and implanted communication protocol module.
2. Managed all technical documents in team.
3. Presented product demo to advisors from company and other teams.

Skills

Algorithme : Sorting, Divide and Conquer, Greedy, String Matching, Graph algorithms

Machine Learning : SVM, logistic regression, neural network, Matlab, Python, R

(Kernel) Development : C/Assembly, MDB/DTrace, Mercurial, Git/GitHub, Linux/Unix & Shell Script, C/C++/C#

English : CET4(458) CET6(557) TOEFL(iBT 98) IELTS(6.0 B2 at 2017)

Chinese : Mother tongue

French : Simple (oral) communication