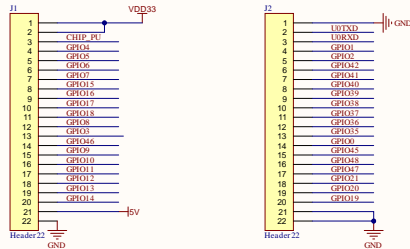
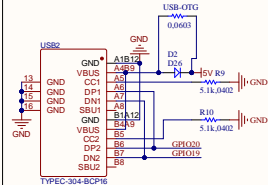
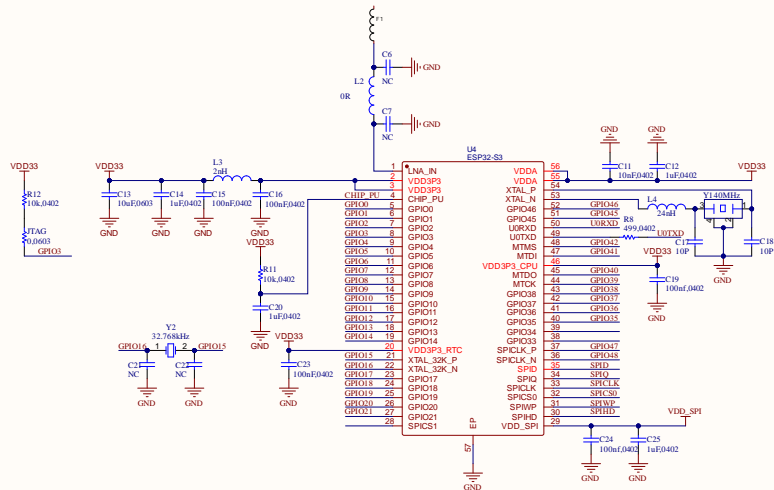
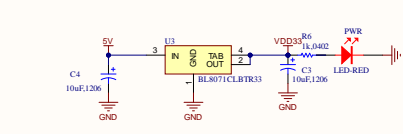
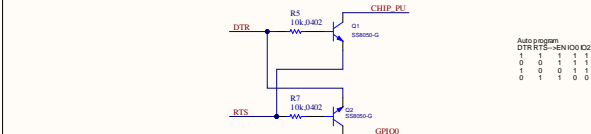
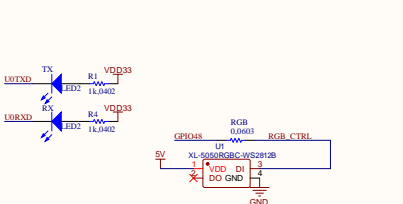
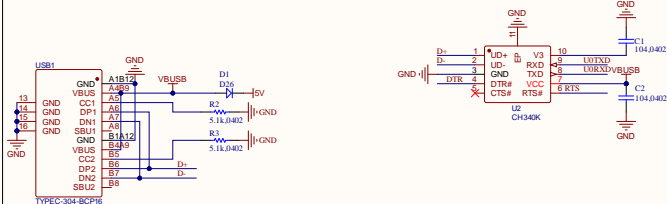


	8
REVISION	DESCRIPTION



APPROVALS	DATE	PROJECT	Altium	
ENG:				
CSN:		PROJECT REVISION:	DOCUMENT REVISION:	DESIGN ITEM:
CHK:		<i>Not in version control</i> <i>Not in version control</i> <i>Not in version control</i>		
REFERENCE DOCUMENTS		TITLE		
		YD-ESP32-S3-COREBOARD		
BOM				
ASSY DWG:	SIZE	CAGE CODE	DWG NO.	REV
FAB DWG:	A2			ItemRevision no
PCB DWG:	SCALE:	FILE NAME	ESP32_SchDoc	SHEET * OF

Comment	Description	Designator	Footprint	LibRef	Quantity
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