RTL8762E RCU MP Test Mode Design Spec

v1.1 by Realtek

2022/04/15



Revision History

Data	Version	Description	
2021/07/07	V1.0	Initial Version	
2022/04/15	V1.1	Modify the value of TX POWER and channel num in single tone test mode	



Contents

Revision History	2
1 Overview	5
2 Test Mode Switching Method	6
3 HCI UART Test Mode	10
3.1 Introduction	10
3.2 Software	11
4 Single Tone Test Mode	12
4.1 Introduction	
4.2 Software	14
5 Data UART Test Mode	16
5.1 Introduction	16
5.2 Data UART Command Format	
5.3 Software	17
6 Fast Pair Test Mode	
6.1 Introduction	20
6.2 Configure Bluetooth Address and Pairing Information	20
6.3 Software	21
7 References	25



Figure

Figure 1 Switch the Mass Production Test Mode Flow	6
Figure 2 System Block Diagram of HCI UART Test Mode	10
Figure 3 Single Tone Waveform	13
Figure 4 Single Tone Test Mode Flow	14
Figure 5 Data UART Request Command Format	16
Figure 6 Data UART Response Command Format	16
Figure 7 Data UART Test Mode Flow	17
Figure 8 Fast Pair Test Mode Flow	21



1 Overview

This paper mainly introduces the related test modes and behavior specifications of the mass production test software of the RTL8762E voice remote control program, including the HCI UART test mode, Data UART test mode, Single Tone test mode and Fast Pair test mode. This article is mainly used to guide the problems encountered in the test with the RCU mass production test software.

In the following part, the RTL8762E voice remote control is referred to as Bee3 RCU.

Test modes:

- HCI UART test mode
- Single Tone test mode
- Data UART test mode
- Fast Pair test mode



2 Test Mode Switching Method

When the set of GPIO signals or combined keys are triggered, the Bee3 RCU switches from the normal mode to the mass production test mode through the Test Mode AON register and the watchdog reboot. The flow of switching the mass production test mode is as follows.

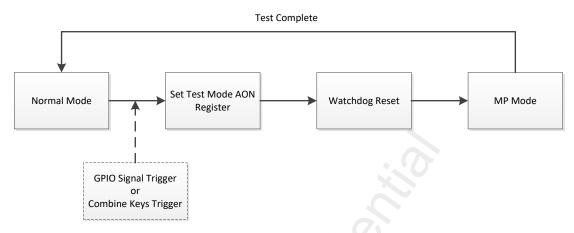


Figure 1 Switch the Mass Production Test Mode Flow

The Bee3 RCU SDK supports two ways to trigger to enter test mode, including GPIO signal and combined key mode.

RCU needs to prevent false triggering in user mode. According to the combined key mode, the value of flag bit in FLASH is used to determine whether the combined key is allowed to trigger the test mode.

- 1) When the value of flag in Flash is MP_TEST_MODE_FLG_ENABLE_VALUE, Bee3 RCU is allowed to enter the mass production test mode by specified combined keys.
- 2) When the value of flag in Flash is MP_TEST_MODE_FLG_DISABLE_VALUE, Bee3 RCU isn't allowed to enter the mass production test mode by specified combined keys.

In the final stage of mass production testing, set the flag in FLASH to MP_TEST_MODE_FLG_DISABLE_VALUE, which means that it is not allowed to enter the mass production mode through the combined keys. The code for MP Test Mode is shown in the mp_test.c in the SDK. Part of the reference code is as below.



```
9.
   */
10. void mp_test_init_data(void)
11. {
12.
      uint32_t ftl_res = 0;
13.
14.
      ftl_res = ftl_load(&test_flag_ftl_value, MP_TEST_FTL_PARAMS_TEST_MODE_FLG_OFFSET,
15.
                     MP_TEST_FTL_PARAMS_TEST_MODE_FLG_LEN);
16.
      if (ftl_res == FTL_READ_ERROR_READ_NOT_FOUND)
17.
18.
19.
         APP_PRINT_WARNO("[mp_test_is_test_mode_flag_en] test mode ftl flag is invalid, reset t
   o enabled!");
20.
         mp_test_enable_test_mode_flag();
         ftl_load(&test_flag_ftl_value, MP_TEST_FTL_PARAMS_TEST_MODE_FLG_OFFSET,
21.
                MP_TEST_FTL_PARAMS_TEST_MODE_FLG_LEN);
22.
23.
      }
24.
25.
      APP_PRINT_INFO2("[mp_test_is_test_mode_flag_en] ftl_res is %d, value is 0x%08X", ftl_res,
26.
                   test_flag_ftl_value);
27. }
28.
29. /***************************
30. * @brief MP Test enable test mode flag.
31. * @param none
32. * @return result
33. * @retval true of false
34. */
35. bool mp_test_enable_test_mode_flag(void)
36. {
37.
     uint32_t result = false;
38.
     uint32_t test_mode_value = MP_TEST_MODE_FLG_ENABLE_VALUE;
39.
40.
     result = ftl_save(&test_mode_value, MP_TEST_FTL_PARAMS_TEST_MODE_FLG_OFFSET,
41.
               MP_TEST_FTL_PARAMS_TEST_MODE_FLG_LEN);
42.
43.
     return (result == 0);
44. }
45.
47. * @brief MP Test disable test mode flag.
48. * @param none
49. * @return result
50. * @retval true of false
```



```
52. bool mp_test_disable_test_mode_flag(void)
53. {
54.
     uint32_t result = false;
     uint32_t test_mode_value = MP_TEST_MODE_FLG_DISABLE_VALUE;
56.
57.
     result = ftl_save(&test_mode_value, MP_TEST_FTL_PARAMS_TEST_MODE_FLG_OFFSET,
58.
              MP_TEST_FTL_PARAMS_TEST_MODE_FLG_LEN);
59.
60. return (result == 0);
61.}
62.
64. * @brief MP Test check if test mode is enabled or not.
65. * @param none
66. * @return result
67. * @retval true of false
68. */
69. bool mp_test_is_test_mode_flag_en(void)
70. {
71. return (test_flag_ftl_value == MP_TEST_MODE_FLG_ENABLE_VALUE);
72. }
```

In the Bee3 RCU SDK, there are related macro definitions in board.h to support the mass production test mode.

```
1. #define FEATURE_SUPPORT_MP_TEST_MODE 1 /* set 1 to enable MP test */
2.
3. #define MP_TEST_MODE_SUPPORT_HCI_UART_TEST 1 /* set 1 to support HCI Uart T
   est Mode */
4. #define MP_TEST_MODE_SUPPORT_DATA_UART_TEST 1 /* set 1 to support Data Uar
  t Test Mode */
5. #define MP_TEST_MODE_SUPPORT_SINGLE_TONE_TEST 1 /* set 1 to support SingleTo
   ne Test Mode */
6. #define MP_TEST_MODE_SUPPORT_FAST_PAIR_TEST 1 /* set 1 to support Fast Pair T
   est */
7. #define MP_TEST_MODE_SUPPORT_AUTO_K_RF 0 /* set 1 to support Auto K RF *
8. #define MP_TEST_MODE_SUPPORT_DATA_UART_DOWNLOAD 0 /* set 1 to support Dat
   a UART download */
9.
10. #define MP_TEST_MODE_TRIG_BY_GPIO 0x0001 /* GPIO signal while power o
   n to trigger MP test mode */
```



11. #define MP_TEST_MODE_TRIG_BY_COMBINE_KEYS 0x0002 /* Combine keys to trigg er MP test mode */
12.
13. #define MP_TEST_MODE_TRIG_SEL (MP_TEST_MODE_TRIG_BY_GPIO | MP_TEST_MODE_TRIG_BY_COMBINE_KEYS)



3 HCI UART Test Mode

3.1 Introduction

The HCI UART test mode allows the BLE RCU device in normal APP mode to expose the HCI layer through UART when it is triggered by an external trigger (GPIO signals or combined keys mode). The purpose of this is to allow RCUs that have been programmed with the final product firmware to be connected directly to the Bluetooth tester via the HCI UART and to run the standard HCI command for production line test. At the same time, ensure that the UART can be used for other purposes in normal mode.

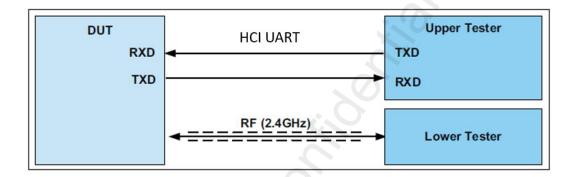


Figure 2 System Block Diagram of HCI UART Test Mode

The figure above is the system block diagram of HCI UART test mode. Like standard BLE test mode DTM, RCU supports a series of standard common HCI commands in HCI UART test mode. With Bluetooth test equipment (such as Anritsu MT8852B), it can verify the RF performance of BLE RCU, including output power, modulation characteristics, carrier frequency drift, sensitivity, etc. Specific test command description can refer to the relevant chapter of the Bluetooth Core Specification.



3.2 Software

The main code and logic of the HCI UART test mode are implemented in the Patch part. APP code provides SwitchToHciMode to switch to the HCI UART test mode. Part of the reference code is as below.

```
    static inline void switch_to_hci_mode(void)
    {
    set_hci_mode_flag(true);
    WDG_SystemReset(RESET_ALL_EXCEPT_AON, SWITCH_HCI_MODE);
    }
```



4 Single Tone Test Mode

4.1 Introduction

Although the HCI UART test mode and professional Bluetooth test instruments can perform a more comprehensive and detailed analysis of the Bluetooth performance, the actual test on the production line also has the following limitations.

- Professional Bluetooth test equipment is generally expensive and the equipment investment cost is high.
- Need to be conducted in a wireless shielded room environment.
- The test item is detailed, but it also takes time.

If the production line just wants to simplify the RF performance test, you can use the Single Tone test mode of RCU. The RCU enters the Single Tone test mode by external trigger (GPIO signal mode or combined key mode), and it will transmit a single carrier signal on a certain set of channels. By using a spectrum analyzer to observe and measure the spectrum waveform of a single carrier, the RCU RF transmit power and frequency offset values can be determined.

Single Tone can be controlled by modifying the parameters of p_vend_cmd_params:

- 1. P_vend_cmd_params->channel can specify Single Tone frequency, e.g. set channel to 20, the frequency of single tone single shall be 2442MHz.
- 2. p_vend_cmd_params->tx_power specifies Tx Power. The specified Tx Power cannot be set to larger than the value of config file.

```
void single_tone_start(uint8_t channel_num)
2.
   {
3.
     APP_PRINT_INFOO("Single Tone Start!");
4.
5.
     T_SINGLE_TONE_VEND_CMD_PARAMS *p_vend_cmd_params = os_mem_alloc(RAM_TYPE_DATA_
   ON, sizeof(T_SINGLE_TONE_VEND_CMD_PARAMS));
6.
7.
     if (p_vend_cmd_params)
8.
9.
        p_vend_cmd_params->pkt_type = 1;
10.
        p_vend_cmd_params->opcode = 0xfc78;
        p vend cmd params->length = 4;
11.
```



```
p_vend_cmd_params->start = 1;
13.
       p_vend_cmd_params->channle = 0x80 | channel_num;
14.
       /** note:
        * tx_power config:
15.
        * 0x00/(-20dBm), 0x70/0dBm, 0xA0/3dBm,
        * 0xB0/4dBm, 0xE0/7.5dBm(only for rtl876x)
17.
18.
19.
       p_vend_cmd_params->tx_power = 0x70;
20.
       p_vend_cmd_params->is_le = 1;
21.
       22.
   S));
23.
       single_tone_is_sent_start_cmd = true;
24.
25.
26. }
```

The Single Tone waveform seen by the spectrum analyzer is similar to the one shown below.



Figure 3 Single Tone Waveform



4.2 Software

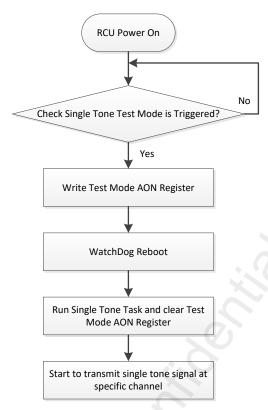


Figure 4 Single Tone Test Mode Flow

- 1) When the RCU detects an external trigger condition (if a particular key combination is pressed), write the flag to AON Register.
- 2) Then software reset.
- 3) Reboot into APP Main () and then judge AON Register to enter single tone test mode.
- 4) Clear AON Register so that the next reboot is normal remote control mode.
- 5) Start to use single carrier signal at fixed frequency.

Part of the reference code is as below.

```
switch_to_test_mode(SINGLE_TONE_MODE);
2.
   static inline void switch_to_test_mode(T_TEST_MODE test_mode)
3.
4.
5.
     T_BTAON_FAST_TEST_MODE_TYPE aon;
6.
     aon.d8 = btaon_fast_read_safe(BTAON_FAST_TEST_MODE);
7.
     aon.s.test_mode = test_mode;
     btaon_fast_write_safe(BTAON_FAST_TEST_MODE, aon.d8);
8.
9.
     WDG_SystemReset(RESET_ALL_EXCEPT_AON, SWITCH_TEST_MODE);
10.
```



```
    void single_tone_init(void)

2. {
3.
      APP_PRINT_INFOO("Single Tone Init");
4.
   #if EXIT_SINGLE_TONE_TEST_WHEN_TIMEOUT
5.
      if (true == os_timer_create(&single_tone_exit_timer, "single_tone_exit_timer", 1,
7.
                        EXIT_SINGLE_TONE_TIME, false, single_tone_exit_timeout_cb))
8.
      {
9.
         os_timer_start(&single_tone_exit_timer);
10.
11. #endif
12.
      os_task_create(&single_tone_task_handle, "single_tone", single_tone_task, 0, 256, 1);
13.
14. }
```



5 Data UART Test Mode

5.1 Introduction

The Data UART test mode allows the BLE RCU device in normal APP mode to be temporarily switch out of the Data UART by an external trigger (such as by pulling down a GPIO pin in the process). The purpose of this is to enable the RCU that has already programmed the firmware of the final product to directly control the remote controller through the Data UART command for various tests. At the same time, ensure that the UART can be used for other purposes in normal mode, and does not affect the normal use of the user.

5.2 Data UART Command Format

Currently, Data UART test mode supports reading software version number, MAC Address, voice MIC test and so on. Refer to "RTL8762E_DATA_UART_MP_Command.xlsx" for more information. Users can modify and add or subtract orders according to actual production line requirements. The DATA UART command and Status return format is as below.

Request Format	Size in Byte	Comment
Start Byte	1	Must be 0x87
Request Opcode	2	
Payload Data	N	
CRC16	2	

Figure 5 Data UART Request Command Format

Response Format	Size in Byte	Comment
Start Byte	1	Must be 0x87
Response Opcode	2	
Response Status	1	0 – Success, 1 - Fail
Payload Size	4	
Payload Data	N	
CRC16	2	

Figure 6 Data UART Response Command Format



5.3 Software

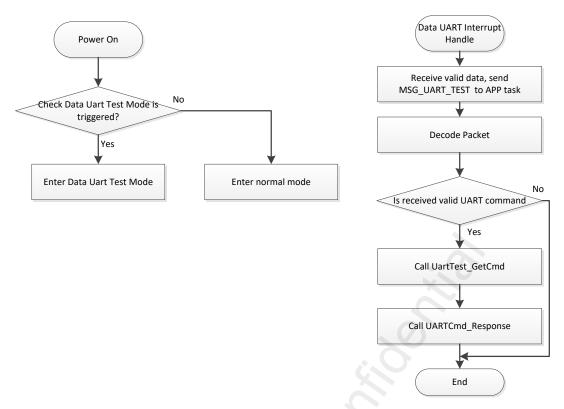


Figure 7 Data UART Test Mode Flow

Part of the Bee3 RCU SDK related code in the file data uart test.c is as below.

```
2.
   * @brief initialize uart test function.
3.
  * @return none
   * @retval void
4.
5.
   */
void uart_test_init(void)
7. {
    UART_DBG_BUFFER(MODULE_APP, LEVEL_INFO, "[uart_test_init] initialize uart test mode", 0);
8.
9.
    data_uart_transport_init();
10.
    uart_test_is_dlps_allowed = false;
11. }
12.
14. * @brief handle UART message.
15. * @param io_driver_msg_recv - recieved io message.
16. * @return none
17. * @retval void
18. */
19. void uart_test_handle_uart_msg(T_IO_MSG io_driver_msg_recv)
```



```
21.
     T_UART_PACKET_DEF *pUartTestPacket = (T_UART_PACKET_DEF *)(io_driver_msg_recv.u.bu
   f);
22.
23.
      if (data_uart_packet_decode(pUartTestPacket))
24.
     {
25.
        uart_test_get_cmd_func(pUartTestPacket);
26.
     }
27. }
28.
29. /**< Array of all used test function informations */
30. const T_UART_TEST_PROTOCOL uart_test_func_map[UART_TEST_SUPPORT_NUM] =
31. {
32.
     /* Opcode, Parameter Length, Function */
33.
      {READ_PATCH_VERSION_CMD, 0, uart_test_read_patch_version},
34.
      {READ_APP_VERSION_CMD, 0, uart_test_read_app_version},
35.
      {READ_MAC_ADDR_CMD, 0, uart_test_read_mac_addr},
36.
      {ENTER_FAST_PAIR_MODE_CMD, 1, uart_test_enter_fast_pair_mode},
37.
      {GET_DEVICE_STATE_CMD, 0, uart_test_get_dev_state},
38.
      {VOICE_TEST_START_CMD, 0, uart_test_voice_test_start},
39.
      {VOICE_TEST_STOP_CMD, 0, uart_test_voice_test_stop},
40.
      {SET_VOICE0_CONFIG_CMD, 16, uart_test_set_voice0_config},
41.
      {GET_VOICE0_CONFIG_CMD, 0, uart_test_get_voice0_config},
42.
      {ENTER_DLPS_TEST_MODE_CMD, 0, uart_test_enter_dlps},
43.
      {START_STOP_ADV_CMD, 1, uart_test_start_stop_adv},
44.
      {START_IR_TEST_MODE_CMD, 0, NULL},
      {ENTER_HCI_TEST_MODE_CMD, 0, uart_test_enter_hci_mode},
45.
46.
      {DISABLE_TEST_MODE_FLG_CMD, 0, uart_test_disable_test_mode_flag},
47.
      {ENABLE_TEST_MODE_FLG_CMD, 0, uart_test_enable_test_mode_flag},
      {ERASE_PAIR_INFO_CMD, 0, uart_test_erase_pair_info},
48.
      {CHANGE_BAUDRATE_CMD, 1, uart_test_change_baudrate},
49.
50.
      {DIRECT_K_RF_FREQ_CMD, 2, uart_test_direct_k_rf_freq},
51.
      {GET_GLODEN_INFO_CMD, 0, NULL},
52.
      {GET_DUT_INFO_CMD, 32, uart_test_get_dut_info},
53.
      {VERIFY_DUT_INFO_CMD, 0, NULL},
54.
      {AUTO_K_RF_FREQ_CMD, 18, uart_test_auto_k_rf_freq},
55.
      {FIND_DEVICE_TYPE_CMD, 0, uart_test_find_device_type},
56.
      {REBOOT_DEVICE_CMD, 0, uart_test_reboot_device},
      {UPDATE_MAC_ADDR_CMD, 0, NULL},
57.
58.
      {ENTER_SINGLE_TONE_MODE_CMD, 1, uart_test_enter_single_tone},
59.
      {READ_HARDWARE_VERSION_CMD, 0, NULL},
60.
      {TERMINATE_CONNECT_CMD, 0, uart_test_terminate_connect},
      {MANUAL_K_RF_FREQ_CMD, 1, NULL},
61.
      {ENTER_HCI_DOWNLOAD_MODE_CMD, 0, uart_test_enter_hci_download_mode},
62.
```



```
63. #if SUPPORT_CODEC_EQ_CONFIG_FEATURE
     {SET_CODEC_EQ_CH0_CONFIG_CMD, 22, uart_test_set_codec_eq_ch0_config},
     {GET_CODEC_EQ_CH0_CONFIG_CMD, 1, uart_test_get_codec_eq_ch0_config},
65.
66. #else
     {SET_CODEC_EQ_CH0_CONFIG_CMD, 22, NULL},
68.
     {GET_CODEC_EQ_CH0_CONFIG_CMD, 1, NULL},
69. #endif
70. #if SUPPORT_DUAL_MIC_FEATURE
     {SET_VOICE1_CONFIG_CMD, 16, uart_test_set_voice1_config},
     {GET_VOICE1_CONFIG_CMD, 0, uart_test_get_voice1_config},
72.
73. #if SUPPORT_CODEC_EQ_CONFIG_FEATURE
     {SET_CODEC_EQ_CH1_CONFIG_CMD, 22, uart_test_set_codec_eq_ch1_config},
74.
     {GET_CODEC_EQ_CH1_CONFIG_CMD, 1, uart_test_get_codec_eq_ch1_config},
75.
76. #else
77.
     {SET_CODEC_EQ_CH1_CONFIG_CMD, 0, NULL},
     {GET_CODEC_EQ_CH1_CONFIG_CMD, 0, NULL},
79. #endif
80. #else
     {SET_VOICE1_CONFIG_CMD, 16, NULL},
81.
82.
     {GET_VOICE1_CONFIG_CMD, 0, NULL},
     {SET_CODEC_EQ_CH1_CONFIG_CMD, 22, NULL},
83.
84. {GET_CODEC_EQ_CH1_CONFIG_CMD, 1, NULL},
85. #endif
     /* Add more command here, Please store in order according to opcode! */
86.
87. };
```



6 Fast Pair Test Mode

6.1 Introduction

RTL8762E remote control uses Windows RCU Tool and Windows RCU test dongle for functional testing during mass production.

At present, functional testing includes Bluetooth connection, key test and voice test. In order to improve the production efficiency, multiple stations can be arranged for functional testing on the production line. At the same time, the RCU also supports a variety of combined keys to enter the measurement mode to establish connection with the corresponding test dongle. When the various combinations of keys described above are pressed, the RCU will restart and send a direct link broadcast using the pre-agreed Mac Address and Link Key to attempt to establish a connection with the corresponding test dongle.

6.2 Configure Bluetooth Address and Pairing Information

In order to realize the requirement to complete the connection and encrypt the link with the RCU Tool by pressing the combined key, the pairing information must be preconfigured in the RCU Tool and the remote controller. The method is as follows (taking 5 stations as an example).

- 1) After prior agreement, the consistent pairing information is solidified into RCU and Tool, including virtual MAC address of remote controller, virtual MAC address of Test Dongle, Link key, and CCCD information. The pairing information of RCU Tool will be recorded in the windows registry. The pairing information of the remote control will be recorded in the specified location in flash.
- 2) Manually pair 5 stations' RCU Tool (named RCU Tool 1, RCU Tool 2, RCU Tool 3, RCU Tool 4, RCU Tool 5) with 5 remote controllers (RCU1, RCU2, RCU3, RCU4, RCU5).
- 3) After RCU program starts, RCU will restart according to different combined keys. Different configuration information is initialized according to different combined keys. After RCU starts, RCU Tool can connect the designated remote controller according to the matching device information stored in the windows registry. Once the connection is successful, the link key is used to encrypt the link directly, saving time by eliminating the need for a matching process.



6.3 Software

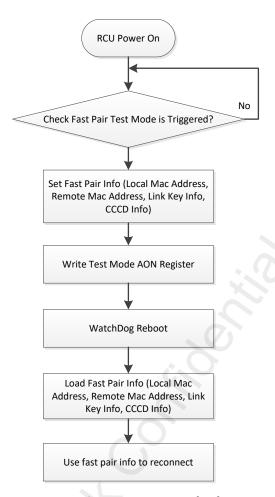


Figure 8 Fast Pair Test Mode Flow

Specific implementation code can refer to mp_test.c, part of the reference code is as below.

```
1. /* Fast Pairing Local Mac Address Config List */
static const uint8_t mp_fp_local_addr_config_list[FP_MAX_LINE_NUM][FP_MAC_ADDR_LEN] =
3. {
      {0x01, 0x88, 0x23, 0x4c, 0xe0, 0x00},
4.
      {0x02, 0x88, 0x23, 0x4c, 0xe0, 0x00},
5.
6.
      {0x03, 0x88, 0x23, 0x4c, 0xe0, 0x00},
7.
      {0x04, 0x88, 0x23, 0x4c, 0xe0, 0x00},
      {0x05, 0x88, 0x23, 0x4c, 0xe0, 0x00},
8.
9. };
10.
11. /* Fast Pairing Remote Mac Address Config List */
12. static const uint8_t mp_fp_remote_addr_config_list[FP_MAX_LINE_NUM][FP_MAC_ADDR_LEN] =
13. {
      {0x87, 0x99, 0x23, 0x4c, 0xe0, 0x00},
14.
15.
      {0x87, 0x99, 0x23, 0x4c, 0xe0, 0x00},
```



```
{0x87, 0x99, 0x23, 0x4c, 0xe0, 0x00},
17.
      {0x87, 0x99, 0x23, 0x4c, 0xe0, 0x00},
      {0x87, 0x99, 0x23, 0x4c, 0xe0, 0x00},
18.
19. };
20.
21. /* Fast Pairing Link Key Config List */
22. static const uint8_t mp_fp_link_key_config_list[FP_MAX_LINE_NUM][FP_LINK_KEY_LEN] =
23. {
24.
     {0xcb, 0x84, 0xaa, 0x4d, 0x66, 0x42, 0xd5, 0xa2, 0x33, 0xa1, 0x6a, 0x51, 0x9a, 0x50, 0xb5, 0
     {0xcb, 0x84, 0xaa, 0x4d, 0x66, 0x42, 0xd5, 0xa2, 0x33, 0xa1, 0x6a, 0x51, 0x9a, 0x50, 0xb5, 0
25.
26. {0xcb, 0x84, 0xaa, 0x4d, 0x66, 0x42, 0xd5, 0xa2, 0x33, 0xa1, 0x6a, 0x51, 0x9a, 0x50, 0xb5, 0
27. {0xcb, 0x84, 0xaa, 0x4d, 0x66, 0x42, 0xd5, 0xa2, 0x33, 0xa1, 0x6a, 0x51, 0x9a, 0x50, 0xb5, 0
   xac},
     {0xcb, 0x84, 0xaa, 0x4d, 0x66, 0x42, 0xd5, 0xa2, 0x33, 0xa1, 0x6a, 0x51, 0x9a, 0x50, 0xb5, 0
28.
29. };
30.
31. /* Fast Pairing CCCD Information */
32. static const uint8_t mp_fp_cccd_info[FP_CCCD_DATA_LEN] =
33. {
34.
     0x3c, 0x00, 0x01, 0x00, 0x30, 0x00, 0x01, 0x00,
35.
      0x34, 0x00, 0x01, 0x00
36. };
37.
39. * @brief MP Test Set Fast Pair Info.
40. * @param index
41. * @return result
42. * @retval true of false
43. */
44. bool mp_test_set_fast_pair_info(uint8_t index)
45. {
46.
     bool result = false;
47.
     uint8_t ltk_length;
48.
     uint8_t mp_local_bd[8] = \{0\};
     uint8_t remote_mac_addr[6] = {0};
49.
50.
     T_LE_KEY_TYPE link_key_type = LE_KEY_UNAUTHEN;
51.
     uint8_t local_ltk[FP_LINK_KEY_LEN];
52.
     T_GAP_REMOTE_ADDR_TYPE remote_addr_type;
53.
     uint8_t ccc_bits_count;
     T_LE_CCCD *p_cccd_data;
54.
```



```
56.
     if (index >= FP_MAX_LINE_NUM)
57.
58.
        return false;
59.
     }
60.
61.
     /* set local bd addr info */
62.
     memcpy(mp_local_bd, mp_fp_local_addr_config_list[index], FP_MAC_ADDR_LEN);
63.
     result = ftl_save(mp_local_bd, MP_TEST_FTL_PARAMS_LOCAL_BD_ADDR_OFFSET,
                MP_TEST_FTL_PARAMS_LOCAL_BD_ADDR_LEN);
64.
     if (result != 0)
65.
66.
     {
        return false; /* ftl save failed */
67.
68.
     }
69.
70.
     /* set remote mac addr info */
71.
     remote_addr_type = GAP_REMOTE_ADDR_LE_PUBLIC;
     memcpy(remote_mac_addr, mp_fp_remote_addr_config_list[index], FP_MAX_LINE_NUM);
72.
73.
74.
     /* set link key info */
75.
     ltk_length = FP_LINK_KEY_LEN;
76.
     memcpy(local_ltk, mp_fp_link_key_config_list[index], FP_LINK_KEY_LEN);
77.
78.
     /* set cccd info */
79.
     ccc_bits_count = FP_CCCD_BITS_CNT;
80.
     p_cccd_data = os_mem_alloc(RAM_TYPE_DATA_ON, 4 + ccc_bits_count * 4);
     p_cccd_data->data_length = FP_CCCD_DATA_LEN;
81.
82.
     memcpy(p_cccd_data->data, mp_fp_cccd_info, FP_CCCD_DATA_LEN);
83.
     /* generate bond dev info */
84.
85.
     result = le_gen_bond_dev(remote_mac_addr, remote_addr_type, GAP_LOCAL_ADDR_LE_PUBLI
   C,
86.
                    ltk_length, local_ltk, link_key_type, p_cccd_data);
87.
88.
     os_mem_free(p_cccd_data);
89.
90.
     return result;
91. }
92.
94. * @brief MP Test load fast pair mac addr.
95. * @param none
96. * @return result
97. * @retval true of false
```



```
99. bool mp_test_load_fp_mac_addr(void)
100. {
101.
        bool result = false;
102.
        uint8_t mp_mac[8] = \{0\};
103.
104.
        if (0 == ftl_load(mp_mac, MP_TEST_FTL_PARAMS_LOCAL_BD_ADDR_OFFSET, sizeof(mp_ma
   c)))
105.
      {
          memcpy((uint8_t *)0x00200197, mp_mac, 6);
106.
107.
          APP_PRINT_INFO1("[mp_test_load_fp_mac_addr] MP MAC Addr: %b", TRACE_BINARY(6, m
   p_mac));
108.
          result = true;
109.
        }
110.
        else
111.
        {
          APP_PRINT_ERROR0("[mp_test_load_fp_mac_addr] load mp mac addr failed!");
112.
113.
          result = false;
114.
        }
115.
116.
        return result;
117. }
```



7 References

- [1] RTL8762E_DATA_UART_MP_Command.xlsx
- [2] Realtek RCU Test Tool User Guide