LIJIN C

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EDUCATION

National Institute of Technology, Calicut *May 2014*
Bachelor of Technology in Computer Science & Engineering
Overall GPA: 7.62/10

Kendriya Vidyalaya, Malappuram *April 2010*
AISSCE, Class XII exam by CBSE
Overall Percentage: 90.2

Kendriya Vidyalaya, Malappuram *April 2008*
AISSE, Class X exam by CBSE
Overall Percentage: 87.4

TECHNICAL STRENGTHS

Computer Languages	C, C++, Java, MIT-Scheme
Kernel programing	Linux kernel 3.11.0 & 2.6.29
Tools	Eclipse-IDT, IDA Disassembler

EXPERIENCE

Samsung R&D Institute Noida, India June 2014 - Present

Engineer

PROJECTS

Design and Implementation of a New CPU Governor *July 2013 - April 2014*
A new CPU governor that enables user to save power when battery percentage goes below certain threshold value by lowering the CPU frequency and by reducing the time interval between the frequency switches.

Android application to log theory hours missed by a student *May 2012*
An application that enables students to keep track of their attendance for their respective subjects in collage and also set reminders so as to prevent attendance shortage issues that are common in collage life.

PUBLICATIONS

Arjun Haris, Johns Paul, Lijin C, Jayaraj P B. Design and Implementation of a New CPU Governor-Power Aware Governor. In proceedings of 1st International Conference on Advances in Computing, Communications and Information Science, pages 454-461. Elsevier S&T 2014.

ACTIVITIES

Event manager of Befunge *2013*
Organised Befunge, a 2 Dimensional Coding competition held at Tathva, Annual Techno-Management festival of NIT Calicut.

Participated in INCTF *2013*
Completed as one of the finalists in INCTF, a national level CTF style coding contest held at Amrita University, Kollam.