

Table 18. Alternate functions (STM32WB55xx)

Port	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
	SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SPI2/ SAI1/ TIM1	I2C1/ I2C3	SPI1/ SPI2	RF	USART1	LPUART1	TSC	USB/ QUADSPI	LCD	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
A	PA0	-	TIM2_ CH1	-	-	-	-	-	-	-	-	-	COMP1_ OUT	SAI1_ EXTCLK	TIM2_ ETR	CM4_ EVENTOUT
	PA1	-	TIM2_ CH2	-	-	I2C1_ SMBA	SPI1_ SCK	-	-	-	-	LCD_SEG0	-	-	-	CM4_ EVENTOUT
	PA2	LSCO	TIM2_ CH3	-	-	-	-	-	LPUART1_ TX	-	QUADSPI_ BK1_NCS	LCD_SEG1	COMP2_ OUT	-	-	CM4_ EVENTOUT
	PA3	-	TIM2_ CH4	-	SAI1_ PDM_CK1	-	-	-	LPUART1_ RX	-	QUADSPI_ CLK	LCD_SEG2	-	SAI1_ MCLK_A	-	CM4_ EVENTOUT
	PA4	-	-	-	-	SPI1_ NSS	-	-	-	-	-	LCD_SEG5	-	SAI1_ FS_B	LPTIM2_ OUT	CM4_ EVENTOUT
	PA5	-	TIM2_ CH1	TIM2_ ETR	-	SPI1_ SCK	-	-	-	-	-	-	-	SAI1_ SD_B	LPTIM2_ ETR	CM4_ EVENTOUT
	PA6	-	TIM1_ BKIN	-	-	SPI1_ MISO	-	-	LPUART1_ CTS	-	QUADSPI_ BK1_IO3	LCD_SEG3	TIM1_ BKIN	-	TIM16_ CH1	CM4_ EVENTOUT
	PA7	-	TIM1_ CH1N	-	-	I2C3_ SCL	SPI1_ MOSI	-	-	-	QUADSPI_ BK1_IO2	LCD_SEG4	COMP2_ OUT	-	TIM17_ CH1	CM4_ EVENTOUT
	PA8	MCO	TIM1_ CH1	-	SAI1_ PDM_CK2	-	-	USART1_ CK	-	-	-	LCD_COM0	-	SAI1_ SCK_A	LPTIM2_ OUT	CM4_ EVENTOUT
	PA9	-	TIM1_ CH2	-	SAI1_ PDM_DI2	I2C1_ SCL	SPI2_ SCK	-	USART1_ TX	-	-	LCD_COM1	-	SAI1_ FS_A	-	CM4_ EVENTOUT
	PA10	-	TIM1_ CH3	-	SAI1_ PDM_DI1	I2C1_ SDA	-	USART1_ RX	-	-	USB_CRD_ SYNC	LCD_COM2	-	SAI1_ SD_A	TIM17_ BKIN	CM4_ EVENTOUT
	PA11	-	TIM1_ CH4	TIM1_ BKIN2	-	-	SPI1_ MISO	-	USART1_ CTS	-	-	USB_DM	-	TIM1_ BKIN2	-	CM4_ EVENTOUT
	PA12	-	TIM1_ ETR	-	-	-	SPI1_ MOSI	-	USART1_ RTS_DE	LPUART1_ RX	-	USB_DP	-	-	-	CM4_ EVENTOUT
	PA13	JTMS- SWDIO	-	-	-	-	-	-	IR_OUT	-	USB_NOE	-	-	SAI1_ SD_B	-	CM4_ EVENTOUT
	PA14	JTCK- SWCLK	LPTIM1_ OUT	-	-	I2C1_ SMBA	-	-	-	-	-	LCD_SEG5	-	SAI1_ FS_B	-	CM4_ EVENTOUT
	PA15	JTDI	TIM2_ CH1	TIM2_ ETR	-	-	SPI1_ NSS	MCO	-	-	-	LCD_SEG17	-	-	-	CM4_ EVENTOUT



Table 18. Alternate functions (STM32WB55xx) (continued)

Port		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
		SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SPI2/ SAI1/ TIM1	I2C1/ I2C3	SPI1/ SPI2	RF	USART1	LPUART1	TSC	USB/ QUADSPI	LCD	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
B	PB0	-	-	-	-	-	-	EXT _PA _TX	-		-	-	-	COMP1_ OUT	-	-	CM4_ EVENTOUT
	PB1	-	-	-	-	-	-	-	-	LPUART1 _RTS_DE	-	-	-	-	-	LPTIM2_ IN1	CM4_ EVENTOUT
	PB2	RTC_ OUT	LPTIM1_ OUT	-	-	I2C3_ SMBA	SPI1_ NSS	-	-	-	-	-	LCD_VLCD	-	SAI1_ EXTCLK	-	CM4_ EVENTOUT
	PB3	JTDO- TRACE SWO	TIM2_ CH2	-	-	-	SPI1_ SCK	-	USART1_ RTS_DE	-	-	-	LCD_SEG7	-	SAI1_ SCK_B	-	CM4_ EVENTOUT
	PB4	NJTRST	-	-	-	I2C3_ SDA	SPI1_ MISO	-	USART1_ CTS	-	TSC_G2_ IO1	-	LCD_SEG8	-	SAI1_ MCLK_B	TIM17_ BKIN	CM4_ EVENTOUT
	PB5	-	LPTIM1_ IN1	-	-	I2C1_ SMBA	SPI1_ MOSI	-	USART1_ CK	LPUART1_ _TX	TSC_G2_ IO2	-	LCD_SEG9	COMP2_ OUT	SAI1_ SD_B	TIM16_ BKIN	CM4_ EVENTOUT
	PB6	MCO	LPTIM1_ ETR	-	-	I2C1_ SCL	-	-	USART1_ TX	-	TSC_G2_ IO3	-	LCD_SEG6	-	SAI1_ FS_B	TIM16_ CH1N	CM4_ EVENTOUT
	PB7	-	LPTIM1_ IN2	-	TIM1_ BKIN	I2C1_ SDA	-	-	USART1_ RX	-	TSC_G2_ IO4	-	LCD_SEG21	-	-	TIM17_ CH1N	CM4_ EVENTOUT
	PB8	-	TIM1_ CH2N	-	SAI1_ PDM_CK1	I2C1_ SCL	-	-	-	-	-	QUADSPI_ BK1_IO1	LCD_SEG16	-	SAI1_ MCLK_A	TIM16_ CH1	CM4_ EVENTOUT
	PB9	-	TIM1_ CH3N	-	SAI1_ PDM_DI2	I2C1_ SDA	SPI2_ NSS	-	-	IR_OUT	TSC_G7_ IO4	QUADSPI_ BK1_IO0	LCD_COM3	-	SAI1_ FS_A	TIM17_ CH1	CM4_ EVENTOUT
	PB10	-	TIM2_ CH3	-	-	I2C3_ SCL	SPI2_SC K	-	-	LPUART1_ RX	TSC_ SYNC	QUADSPI_ CLK	LCD_SEG10	COMP1_ OUT	SAI1_ SCK_A	-	CM4_ EVENTOUT
	PB11	-	TIM2_ CH4	-	-	I2C3_ SDA	-	-	-	LPUART1_ TX	-	QUADSPI_ BK1_NCS	LCD_SEG11	COMP2_ OUT	-	-	CM4_ EVENTOUT
	PB12	-	TIM1_ BKIN	-	TIM1_ BKIN	I2C3_ SMBA	SPI2_ NSS	-	-	LPUART1_ RTS	TSC_G1_ IO1	-	LCD_SEG12	-	SAI1_ FS_A	-	CM4_ EVENTOUT
	PB13	-	TIM1_ CH1N	-	-	I2C3_ SCL	SPI2_ SCK	-	-	LPUART1_ CTS	TSC_G1_ IO2	-	LCD_SEG13	-	SAI1_ SCK_A	-	CM4_ EVENTOUT
	PB14	-	TIM1_ CH2N	-	-	I2C3_ SDA	SPI2_ MISO	-	-	-	TSC_G1_ IO3	-	LCD_SEG14	-	SAI1_ MCLK_A	-	CM4_ EVENTOUT
	PB15	RTC_ REFIN	TIM1_ CH3N	-	-	-	SPI2_ MOSI	-	-	-	TSC_G1_ IO4	-	LCD_SEG15	-	SAI1_ SD_A	-	CM4_ EVENTOUT

Table 18. Alternate functions (STM32WB55xx) (continued)

Port		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
		SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SPI2/ SAI1/ TIM1	I2C1/ I2C3	SPI1/ SPI2	RF	USART1	LPUART1	TSC	USB/ QUADSPI	LCD	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
C	PC0	-	LPTIM1_IN1	-	-	I2C3_SCL	-	-	-	LPUART1_RX	-	-	LCD_SEG18	-	-	LPTIM2_IN1	CM4_EVENTOUT
	PC1	-	LPTIM1_OUT	-	SPI2_MOSI	I2C3_SDA		-	-	LPUART1_TX	-	-	LCD_SEG19	-	-	-	CM4_EVENTOUT
	PC2	-	LPTIM1_IN2	-	-	-	SPI2_MISO	-	-	-	-	-	LCD_SEG20	-	-	-	CM4_EVENTOUT
	PC3	-	LPTIM1_ETR	-	SAI1_PDM_DI1	-	SPI2_MOSI	-	-	-	-	-	LCD_VLCD	-	SAI1_SD_A	LPTIM2_ETR	CM4_EVENTOUT
	PC4	-	-	-	-	-	-	-	-	-	-	-	LCD_SEG22	-	-	-	CM4_EVENTOUT
	PC5	-	-	-	SAI1_PDM_DI3	-	-	-	-	-	-	-	LCD_SEG23	-	-	-	CM4_EVENTOUT
	PC6	-	-	-	-	-	-	-	-	-	TSC_G4_IO1	-	LCD_SEG24	-	-	-	CM4_EVENTOUT
	PC7	-	-	-	-	-	-	-	-	-	TSC_G4_IO2	-	LCD_SEG25	-	-	-	CM4_EVENTOUT
	PC8	-	-	-	-	-	-	-	-	-	TSC_G4_IO3	-	LCD_SEG26	-	-	-	CM4_EVENTOUT
	PC9	-	-	-	TIM1_BKIN	-	-	-	-	-	TSC_G4_IO4	USB_NOE	LCD_SEG27	-	SAI1_SCK_B	-	CM4_EVENTOUT
	PC10	TRACE_D1	-	-	-	-	-	-	-	-	TSC_G3_IO2	-	LCD_COM4 LCD_SEG28 LCD_SEG40	-	-	-	CM4_EVENTOUT
	PC11	-	-	-	-	-	-	-	-	-	TSC_G3_IO3	-	LCD_COM5 LCD_SEG29 LCD_SEG41	-	-	-	CM4_EVENTOUT
	PC12	TRACE_D3	-	-	-	-	-	-	LSCO	-	TSC_G3_IO4	-	LCD_COM6 LCD_SEG30 LCD_SEG42	-	-	-	CM4_EVENTOUT
	PC13	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_EVENTOUT
	PC14	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_EVENTOUT
	PC15	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_EVENTOUT



Table 18. Alternate functions (STM32WB55xx) (continued)

Port		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
		SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SPI2/ SAI1/ TIM1	I2C1/ I2C3	SPI1/ SPI2	RF	USART1	LPUART1	TSC	USB/ QUADSPI	LCD	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
D	PD0	-	-	-	-	-	SPI2_NSS	-	-	-	-	-	-	-	-	-	CM4_EVENTOUT
	PD1	-	-	-	-	-	SPI2_SCK	-	-	-	-	-	-	-	-	-	CM4_EVENTOUT
	PD2	TRACE_D2	-	-	-	-	-	-	-	-	TSC_SYNC	-	LCD_COM7 LCD_SEG31 LCD_SEG43	-	-	-	CM4_EVENTOUT
	PD3	-	-	-	SPI2_SCK	-	SPI2_MISO	-	-	-	-	QUADSPI_BK1_NCS	-	-	-	-	CM4_EVENTOUT
	PD4	-	-	-	-	-	SPI2_MOSI	-	-	-	TSC_G5_IO1	QUADSPI_BK1_IO0	-	-	-	-	CM4_EVENTOUT
	PD5	-	-	-	-	-	-	-	-	-	TSC_G5_IO2	QUADSPI_BK1_IO1	-	-	SAI1_MCLK_B	-	CM4_EVENTOUT
	PD6	-	-	-	SAI1_PDM_DI1	-	-	-	-	-	TSC_G5_IO3	QUADSPI_BK1_IO2	-	-	SAI1_SD_A	-	CM4_EVENTOUT
	PD7	-	-	-	-	-	-	-	-	-	TSC_G5_IO4	QUADSPI_BK1_IO3	LCD_SEG39	-	-	-	CM4_EVENTOUT
	PD8	-	-	TIM1_BKIN2	-	-	-	-	-	-	-	-	LCD_SEG28	-	-	-	CM4_EVENTOUT
	PD9	TRACE_D0	-	-	-	-	-	-	-	-	-	-	LCD_SEG29	-	-	-	CM4_EVENTOUT
	PD10	TRIG_INOUT	-	-	-	-	-	-	-	-	TSC_G6_IO1	-	LCD_SEG30	-	-	-	CM4_EVENTOUT
	PD11	-	-	-	-	-	-	-	-	-	TSC_G6_IO2	-	LCD_SEG31	-	-	LPTIM2_ETR	CM4_EVENTOUT
	PD12	-	-	-	-	-	-	-	-	-	TSC_G6_IO3	-	LCD_SEG32	-	-	LPTIM2_IN1	CM4_EVENTOUT
	PD13	-	-	-	-	-	-	-	-	-	TSC_G6_IO4	-	LCD_SEG33	-	-	LPTIM2_OUT	CM4_EVENTOUT
	PD14	-	TIM1_CH1	-	-	-	-	-	-	-	-	-	LCD_SEG34	-	-	-	CM4_EVENTOUT
	PD15	-	TIM1_CH2	-	-	-	-	-	-	-	-	-	LCD_SEG35	-	-	-	CM4_EVENTOUT



Table 18. Alternate functions (STM32WB55xx) (continued)

Port		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
		SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SPI2/ SAI1/ TIM1	I2C1/ I2C3	SPI1/ SPI2	RF	USART1	LPUART1	TSC	USB/ QUADSPI	LCD	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
E	PE0	-	TIM1_ ETR	-	-	-	-	-	-	-	TSC_ G7_IO3	-	LCD_SEG36	-	-	TIM16_ CH1	CM4_ EVENTOUT
	PE1	-	-	-	-	-	-	-	-	-	TSC_ G7_IO2	-	LCD_SEG37	-	-	TIM17_ CH1	CM4_ EVENTOUT
	PE2	TRACECK	-	-	SAI1_ PDM_CK1	-	-	-	-	-	TSC_ G7_IO1	-	LCD_SEG38	-	SAI1_ MCLK_A	-	CM4_ EVENTOUT
	PE3	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
	PE4	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
H	PH0	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
	PH1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
	PH3	LSCO	-	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT



Table 19. Alternate functions (STM32WB35xx)

Port	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF10	AF12	AF13	AF14	AF15
	SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SAI1/ TIM1	I2C1/ I2C3	SPI1	RF	USART1	LPUART1	USB/ QUADSPI	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
A	PA0	-	TIM2_ CH1	-	-	-	-	-	-	-	COMP1_ OUT	SAI1_ EXTCLK	TIM2_ ETR	CM4_ EVENTOUT
	PA1	-	TIM2_ CH2	-	-	I2C1_ SMBA	SPI1_ SCK	-	-	-	-	-	-	CM4_ EVENTOUT
	PA2	LSCO	TIM2_ CH3	-	-	-	-	-	LPUART1_ TX	QUADSPI_ BK1_NCS	COMP2_ OUT	-	-	CM4_ EVENTOUT
	PA3	-	TIM2_ CH4	-	SAI1_ PDM_CK1	-	-	-	LPUART1_ RX	QUADSPI_ CLK	-	SAI1_ MCLK_A	-	CM4_ EVENTOUT
	PA4	-	-	-	-	SPI1_ NSS	-	-	-	-	-	SAI1_ FS_B	LPTIM2_ OUT	CM4_ EVENTOUT
	PA5	-	TIM2_ CH1	TIM2_ ETR	-	SPI1_ SCK	-	-	-	-	-	SAI1_ SD_B	LPTIM2_ ETR	CM4_ EVENTOUT
	PA6	-	TIM1_ BKIN	-	-	SPI1_ MISO	-	-	LPUART1_ CTS	QUADSPI_ BK1_IO3	TIM1_ BKIN	-	TIM16_ CH1	CM4_ EVENTOUT
	PA7	-	TIM1_ CH1N	-	-	I2C3_ SCL	SPI1_ MOSI	-	-	QUADSPI_ BK1_IO2	COMP2_ OUT	-	TIM17_ CH1	CM4_ EVENTOUT
	PA8	MCO	TIM1_ CH1	-	SAI1_ PDM_CK2	-	-	USART1_ CK	-	-	-	SAI1_ SCK_A	LPTIM2_ OUT	CM4_ EVENTOUT
	PA9	-	TIM1_ CH2	-	SAI1_ PDM_DI2	I2C1_ SCL	-	USART1_ TX	-	-	-	SAI1_ FS_A	-	CM4_ EVENTOUT
	PA10	-	TIM1_ CH3	-	SAI1_ PDM_DI1	I2C1_ SDA	-	USART1_ RX	-	USB_CRD_ SYNC	-	SAI1_ SD_A	TIM17_ BKIN	CM4_ EVENTOUT
	PA11	-	TIM1_ CH4	TIM1_ BKIN2	-	SPI1_ MISO	-	USART1_ CTS	-	USB_DM	TIM1_ BKIN2	-	-	CM4_ EVENTOUT
	PA12	-	TIM1_ ETR	-	-	SPI1_ MOSI	-	USART1_ RTS_DE	LPUART1_ RX	USB_DP	-	-	-	CM4_ EVENTOUT
	PA13	JTMS- SWDIO	-	-	-	-	-	-	IR_OUT	USB_NOE	-	SAI1_ SD_B	-	CM4_ EVENTOUT
	PA14	JTCK- SWCLK	LPTIM1_ OUT	-	-	I2C1_ SMBA	-	-	-	-	-	SAI1_ FS_B	-	CM4_ EVENTOUT
	PA15	JTDI	TIM2_ CH1	TIM2_ ETR	-	SPI1_ NSS	MCO	-	-	-	-	-	-	CM4_ EVENTOUT

Table 19. Alternate functions (STM32WB35xx) (continued)

Port		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF10	AF12	AF13	AF14	AF15
		SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SAI1/ TIM1	I2C1/ I2C3	SPI1	RF	USART1	LPUART1	USB/ QUADSPI	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
B	PB0	-	-	-	-	-	-	EXT _PA _TX	-		-	COMP1_ OUT	-	-	CM4_ EVENTOUT
	PB1	-	-	-	-	-	-	-	-	LPUART1 _RTS_DE	-	-	-	LPTIM2_ IN1	CM4_ EVENTOUT
	PB2	RTC_ OUT	LPTIM1_ OUT	-	-	I2C3_ SMBA	SPI1_ NSS	-	-	-	-	-	SAI1_ EXTCLK	-	CM4_ EVENTOUT
	PB3	JTDO- TRACE SWO	TIM2_ CH2	-	-	-	SPI1_ SCK	-	USART1_ RTS_DE	-	-	-	SAI1_ SCK_B	-	CM4_ EVENTOUT
	PB4	NJTRST	-	-	-	I2C3_ SDA	SPI1_ MISO	-	USART1_ CTS	-	-	-	SAI1_ MCLK_B	TIM17_ BKIN	CM4_ EVENTOUT
	PB5	-	LPTIM1_ IN1	-	-	I2C1_ SMBA	SPI1_ MOSI	-	USART1_ CK	LPUART1 _TX	-	COMP2_ OUT	SAI1_ SD_B	TIM16_ BKIN	CM4_ EVENTOUT
	PB6	MCO	LPTIM1_ ETR	-	-	I2C1_ SCL	-	-	USART1_ TX	-	-	-	SAI1_ FS_B	TIM16_ CH1N	CM4_ EVENTOUT
	PB7	-	LPTIM1_ IN2	-	TIM1_ BKIN	I2C1_ SDA	-	-	USART1_ RX	-	-	-	-	TIM17_ CH1N	CM4_ EVENTOUT
	PB8	-	TIM1_ CH2N	-	SAI1_ PDM_CK1	I2C1_ SCL	-	-	-	-	QUADSPI_ BK1_IO1	-	SAI1_ MCLK_A	TIM16_ CH1	CM4_ EVENTOUT
	PB9	-	TIM1_ CH3N	-	SAI1_ PDM_DI2	I2C1_ SDA	-	-	-	IR_OUT	QUADSPI_ BK1_IO0	-	SAI1_ FS_A	TIM17_ CH1	CM4_ EVENTOUT
C	PC14	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
	PC15	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
E	PE4	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
H	PH3	LSCO	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT