

Table 18. Alternate functions (STM32WB55xx)

	Table 16. Alternate functions (31 Wi32 VVB33XX)																
		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
F	Port	SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SPI2/ SAI1/ TIM1	I2C1/ I2C3	SPI1/ SPI2	RF	USART1	LPUART1	TSC	USB/ QUADSPI	LCD	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
	PA0	-	TIM2_ CH1	-	-	-	-	-	-	-	-	-	-	COMP1_ OUT	SAI1_ EXTCLK	TIM2_ ETR	CM4_ EVENTOUT
	PA1	-	TIM2_ CH2	-	-	I2C1_ SMBA	SPI1_ SCK	-	-		-	-	LCD_SEG0	-	-	-	CM4_ EVENTOUT
	PA2	LSCO	TIM2_ CH3	-	-	-	-	-	-	LPUART1 _TX	-	QUADSPI_ BK1_NCS	LCD_SEG1	COMP2_ OUT	-	-	CM4_ EVENTOUT
	PA3	-	TIM2_ CH4	-	SAI1_ PDM_CK1	-	-	-	-	LPUART1 _RX	-	QUADSPI_ CLK	LCD_SEG2	-	SAI1 _MCLK_A	-	CM4_ EVENTOUT
	PA4	-	-	-	-		SPI1_ NSS	-	-	-	-	-	LCD_SEG5	-	SAI1 _FS_B	LPTIM2_ OUT	CM4_ EVENTOUT
	PA5	-	TIM2_ CH1	TIM2_ ETR	-		SPI1_ SCK	-	-	-	ı	-	-	-	SAI1 _SD_B	LPTIM2_ ETR	CM4_ EVENTOUT
	PA6	ı	TIM1_ BKIN	-	-		SPI1_ MISO	-	-	LPUART1 _CTS	i	QUADSPI_ BK1_IO3	LCD_SEG3	TIM1_ BKIN	-	TIM16 _CH1	CM4_ EVENTOUT
Α	PA7	1	TIM1_ CH1N	ı	-	I2C3_ SCL	SPI1_ MOSI	-	-	-	i	QUADSPI_ BK1_IO2	LCD_SEG4	COMP2_ OUT	-	TIM17 _CH1	CM4_ EVENTOUT
^	PA8	MCO	TIM1_ CH1	-	SAI1_ PDM_CK2	-	-	-	USART1_ CK	-	ı		LCD_COM0	-	SAI1 _SCK_A	LPTIM2_ OUT	CM4_ EVENTOUT
	PA9	ı	TIM1_ CH2	-	SAI1_ PDM_DI2	I2C1_ SCL	SPI2_ SCK	-	USART1_ TX	-	i		LCD_COM1	-	SAI1 _FS_A	-	CM4_ EVENTOUT
	PA10	1	TIM1_ CH3	ı	SAI1_ PDM_DI1	I2C1_ SDA		-	USART1_ RX	-	i	USB_CRS _SYNC	LCD_COM2	-	SAI1 _SD_A	TIM17 _BKIN	CM4_ EVENTOUT
	PA11	1	TIM1_ CH4	TIM1_ BKIN2	-	ı	SPI1_ MISO	-	USART1_ CTS	-	ı	USB_DM	-	TIM1_ BKIN2	-	-	CM4_ EVENTOUT
	PA12	-	TIM1_ ETR	-	-	-	SPI1_ MOSI	-	USART1_ RTS_DE	LPUART1 _RX	-	USB_DP	-	-	-	-	CM4_ EVENTOUT
	PA13	JTMS- SWDIO	-	-	-	-	-	-	-	IR_OUT	ı	USB_NOE	-	-	SAI1 _SD_B	-	CM4_ EVENTOUT
	PA14	JTCK- SWCLK	LPTIM1_ OUT	-	-	I2C1_ SMBA	-	-	-	-	1	-	LCD_SEG5	-	SAI1 _FS_B	-	CM4_ EVENTOUT
	PA15	JTDI	TIM2_ CH1	TIM2_ ETR	-		SPI1_ NSS	мсо	-	-		-	LCD_SEG17	-	-	-	CM4_ EVENTOUT

EVENTOUT

SD_A

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CH3N

74/193 Table 18. Alternate functions (STM32WB55xx) (continued) AF3 AF13 AF0 AF1 AF2 AF4 AF5 AF6 AF7 AF8 AF9 AF10 AF11 AF12 AF14 AF15 TIM2/ Port COMP1/ TIM1/ SPI2/ TIM1/ I2C1/ USB/ SPI1/ TIM16/ SYS_AF TIM2/ SAI1/ RF USART1 LPUART1 TSC LCD COMP2/ SAI1 **EVENTOUT** QUADSPI TIM2 I2C3 SPI2 TIM17/ LPTIM1 TIM1 TIM1 LPTIM2 EXT COMP1 CM4 PB0 OUT **EVENTOUT** TX LPTIM2 LPUART1 CM4 PB1 RTS DE EVENTŌUT IN1 RTC LPTIM1 I2C3 SPI1 SAI1 CM4 PB2 LCD VLCD OUT OUT **SMBA** NSS EXTCLK **EVENTOUT** JTDO-TIM2 SPI1 USART1 SAI1 CM4 PB3 TRACE -LCD SEG7 CH2 SCK RTS DE SCK B **EVENTOUT** SWO TSC_G2 TIM17 I2C3_ SPI1 USART1_ SAI1_ CM4 PB4 **NJTRST** LCD SEG8 SDA MISO 101 MCLK B BKIN **EVENTOUT** CTS DS11929 Rev 10 LPTIM1 USART1 LPUART1 TSC G2 I2C1 COMP2 TIM16 CM4 PB5 LCD SEG9 _102 SD \bar{B} **EVENTOUT** IN1 SMBĀ MOSĪ OUT BKIN CK _TX LPTIM1_ USART1 TSC_G2 CM4 I2C1 SAI1 TIM16 PB6 MCO LCD SEG6 **EVENTOUT** ETR SCL TX _IO3 FS_B CH1N LPTIM1 TIM1 I2C1 USART1 TSC_G2 TIM17 CM4 PB7 LCD_SEG21 В _104 **EVENTOUT** IN2 BKIN SDA RX CH1N QUADSPI CM4 TIM1 SAI1 I2C1 SAI1 TIM16 PB8 LCD SEG16 BK1_IO1 **EVENTOUT** CH2N PDM_CK1 SCL MCLK_A CH1 QUADSPI_ TIM17 TIM1 SAI1 I2C1 SPI2 TSC_G7 SAI1 CM4 PB9 IR OUT LCD COM3 CH3N PDM_DI2 SDA NSS _104 BK1_IO0 FS_A CH1 **EVENTOUT** TIM2 12C3_ SPI2_SC LPUART1 TSC QUADSPI COMP1 CM4 LCD_SEG10 **PB10** CH3 SCL CLK SCK A **EVENTOUT** _RX _SYNC OUT LPUART1 QUADSPI CM4 TIM2 I2C3 COMP2 **PB11** LCD_SEG11 CH4 SDA _TX BK1 NCS OUT **EVENTOUT** LPUART1 TSC G1 TIM1 SPI2 CM4 TIM1 I2C3 SAI1 **PB12** LCD SEG12 BKIN BKIN SMBA NSS _RTS _IO1 FS_A **EVENTOUT** TIM1 SPI2 LPUART1 TSC G1 CM4 I2C3 SAI1 **PB13** LCD_SEG13 CH1N SCK A **EVENTOUT** SCL SCK _CTS _102 TIM1 I2C3 SPI2 TSC G1 SAI1 CM4 **PB14** LCD_SEG14 CH2N SDA MISO _1Ō3 MCLK A **EVENTOUT** RTC TIM1 TSC_G1 CM4 SAI1 **PB15** LCD_SEG15 REFIN

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MOSI



Table 18. Alternate functions (STM32WB55xx) (continued)

_	Table 16. Alternate functions (STMS2VVBSSXX) (Continued)																
		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
	Port	SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SPI2/ SAI1/ TIM1	I2C1/ I2C3	SPI1/ SPI2	RF	USART1	LPUART1	TSC	USB/ QUADSPI	LCD	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
	PC0	-	LPTIM1_ IN1	-	-	I2C3 _SCL	-	-	-	LPUART1 _RX	-	-	LCD_SEG18	-	-	LPTIM2_ IN1	CM4_ EVENTOUT
	PC1	ı	LPTIM1_ OUT	ı	SPI2_ MOSI	I2C3 _SDA		-	-	LPUART1 _TX	-	-	LCD_SEG19	-	-	-	CM4_ EVENTOUT
	PC2	-	LPTIM1_ IN2	-	-	-	SPI2_ MISO	-	-	-	-	-	LCD_SEG20	1	-	-	CM4_ EVENTOUT
	PC3	-	LPTIM1_ ETR	-	SAI1_ PDM_DI1	-	SPI2_ MOSI	-	-	-	-	-	LCD_VLCD	-	SAI1 _SD_A	LPTIM2_ ETR	CM4_ EVENTOUT
	PC4	-	-	-	-	-	-	-	-	-	-	-	LCD_SEG22	-	-	-	CM4_ EVENTOUT
	PC5	-	-	-	SAI1_ PDM_DI3	-	-	-	-	-	-	-	LCD_SEG23	-	-	-	CM4_ EVENTOUT
	PC6	-	-	-	-	-	-	-	-	-	TSC_G4 _IO1	-	LCD_SEG24	-	-	-	CM4_ EVENTOUT
	PC7	-	-	-	-	-	-	-	-	-	TSC_G4 _IO2	-	LCD_SEG25	-	-	-	CM4_ EVENTOUT
С	PC8	-	-	-	-	-	-	-	-	-	TSC_G4 _IO3	-	LCD_SEG26	-	-	-	CM4_ EVENTOUT
	PC9	-	-	-	TIM1 _BKIN	-	-	-	-	-	TSC_G4 _IO4	USB_NOE	LCD_SEG27	-	SAI1 _SCK_B	-	CM4_ EVENTOUT
	PC10	TRACE D1	-	-	-	-	-	-	-	-	TSC_G3 _IO2	-	LCD_COM4 LCD_SEG28 LCD_SEG40	-	-	-	CM4_ EVENTOUT
	PC11	-	-	-	-	-	-	-	-	-	TSC_G3 _IO3	-	LCD_COM5 LCD_SEG29 LCD_SEG41	-	-	-	CM4_ EVENTOUT
	PC12	TRACE D3	-	-	-	-	-	LSCO	-	-	TSC_G3 _IO4	-	LCD_COM6 LCD_SEG30 LCD_SEG42	-	-	-	CM4_ EVENTOUT
	PC13	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
	PC14	-	-	ı	-	ı	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
	PC15	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT

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					Ta	able 18	. Altern	ate fu	nctions	(STM32V	VB55xx) (continu	ned)				
		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
F	Port	SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SPI2/ SAI1/ TIM1	I2C1/ I2C3	SPI1/ SPI2	RF	USART1	LPUART1	TSC	USB/ QUADSPI	LCD	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
	PD0	-	-	-	-	-	SPI2_ NSS	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
	PD1	-	-	-	-	1	SPI2_ SCK	-	-	-	i	-	-	-	-	-	CM4_ EVENTOUT
	PD2	TRACE D2	-	-	-	ı	-	-	-	-	TSC_ SYNC	-	LCD_COM7 LCD_SEG31 LCD_SEG43	-	-	-	CM4_ EVENTOUT
	PD3	-	-	-	SPI2_SCK	-	SPI2_ MISO	-	-	-	-	QUADSPI_ BK1_NCS	-	-	-	-	CM4_ EVENTOUT
	PD4	-	-	-	-		SPI2_ MOSI	-	-	-	TSC_ G5_IO1	QUADSPI_ BK1_IO0	-	-	-	-	CM4_ EVENTOUT
	PD5	-	-	-	-		-	-	-	-	TSC_ G5_IO2	QUADSPI_ BK1_IO1	-	-	SAI1_ MCLK_B	-	CM4_ EVENTOUT
	PD6	-	-	-	SAI1_ PDM_DI1		-	-	-	-	TSC_ G5_IO3	QUADSPI_ BK1_IO2	-	-	SAI1_ SD_A	-	CM4_ EVENTOUT
D	PD7	-	-	-	-		-	-	-	-	TSC_ G5_IO4	QUADSPI_ BK1_IO3	LCD_SEG39	-	-	-	CM4_ EVENTOUT
	PD8	-	-	TIM1 _BKIN2	-		-	-	-	-	-	-	LCD_SEG28	-	-	-	CM4_ EVENTOUT
	PD9	TRACE D0	-	-	-		-	-	-	-	-	-	LCD_SEG29	-	-	-	CM4_ EVENTOUT
	PD10	TRIG _INOUT	-	-	-		-	-	-	-	TSC_ G6_IO1	-	LCD_SEG30	-	-	-	CM4_ EVENTOUT
	PD11	-	-	-	-	-	-	-	-	-	TSC_ G6_IO2	-	LCD_SEG31	-	-	LPTIM2_ ETR	CM4_ EVENTOUT
	PD12	-	-	-	-	-	-	-	-	-	TSC_ G6_IO3	-	LCD_SEG32	-	-	LPTIM2_ IN1	CM4_ EVENTOUT
	PD13	-	-	-	-	-	-	-	-	-	TSC_ G6_IO4	-	LCD_SEG33	-	-	LPTIM2_ OUT	CM4_ EVENTOUT
	PD14	-	TIM1_ CH1	-	-	-	-	-	-	-	-	-	LCD_SEG34	-	-	-	CM4_ EVENTOUT
	PD15	-	TIM1_ CH2	-	-	-	-	-	-	-	-	-	LCD_SEG35	-	-	-	CM4_ EVENTOUT



Table 18. Alternate functions (STM32WB55xx) (continued)

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			AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
	Port		SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SPI2/ SAI1/ TIM1	I2C1/ I2C3	SPI1/ SPI2	RF	USART1	LPUART1	TSC	USB/ QUADSPI	LCD	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
		PE0	-	TIM1_ ETR	-	-	1	-	-	-	-	TSC_ G7_IO3	-	LCD_SEG36	-	-	TIM16_ CH1	CM4_ EVENTOUT
		PE1	-	ı	i	1	ı	ı	-	-	ı	TSC_ G7_IO2	-	LCD_SEG37	ı	ı	TIM17_ CH1	CM4_ EVENTOUT
E	•	PE2	TRACECK	1	i	SAI1_ PDM_CK1	ı	1	-	-	1	TSC_ G7_IO1	-	LCD_SEG38	1	SAI1_ MCLK_A	1	CM4_ EVENTOUT
		PE3	-	-	i	1	ı	ı	-	-	ı	ı	-	-	1	1	ı	CM4_ EVENTOUT
		PE4	-	-	i	1	ı	ı	-	-	ı	ı	-	-	1	1	ı	CM4_ EVENTOUT
		РН0	-	-	ı	-	ı	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
ŀ	1	PH1	-	-	i	-	ı	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
		РН3	LSCO	-	ı	-	1	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT

Table 19. Alternate functions (STM32WB35xx)

		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF10	AF12	AF13	AF14	AF15
ı	Port	SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SAI1/ TIM1	I2C1/ I2C3	SPI1	RF	USART1	LPUART1	USB/ QUADSPI	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
	PA0	-	TIM2_ CH1	-	-	-	-	-	-	-	-	COMP1_ OUT	SAI1_ EXTCLK	TIM2_ ETR	CM4_ EVENTOUT
	PA1	-	TIM2_ CH2	-	-	I2C1_ SMBA	SPI1_ SCK	-	-		-	-	-	-	CM4_ EVENTOUT
	PA2	LSCO	TIM2_ CH3	-	-	-	-	-	-	LPUART1 _TX	QUADSPI_ BK1_NCS	COMP2_ OUT	-	-	CM4_ EVENTOUT
	PA3	=	TIM2_ CH4	-	SAI1_ PDM_CK1	-	-	-	-	LPUART1 _RX	QUADSPI_ CLK	-	SAI1 _MCLK_A	-	CM4_ EVENTOUT
	PA4	-	-	-	-		SPI1_ NSS	-	-	-	-	-	SAI1 _FS_B	LPTIM2_ OUT	CM4_ EVENTOUT
	PA5	-	TIM2_ CH1	TIM2_ ETR	-		SPI1_ SCK	-	-	-	-	-	SAI1 _SD_B	LPTIM2_ ETR	CM4_ EVENTOUT
	PA6	-	TIM1_ BKIN	-	-		SPI1_ MISO	-	-	LPUART1 _CTS	QUADSPI_ BK1_IO3	TIM1_ BKIN	-	TIM16 _CH1	CM4_ EVENTOUT
	PA7	-	TIM1_ CH1N	-	-	I2C3_ SCL	SPI1_ MOSI	-	-	-	QUADSPI_ BK1_IO2	COMP2_ OUT	-	TIM17 _CH1	CM4_ EVENTOUT
A	PA8	MCO	TIM1_ CH1	-	SAI1_ PDM_CK2	-	-	-	USART1_ CK	-		-	SAI1 _SCK_A	LPTIM2_ OUT	CM4_ EVENTOUT
	PA9	-	TIM1_ CH2	-	SAI1_ PDM_DI2	I2C1_ SCL	-	-	USART1_ TX	-		-	SAI1 _FS_A	-	CM4_ EVENTOUT
	PA10	-	TIM1_ CH3	-	SAI1_ PDM_DI1	I2C1_ SDA	-	-	USART1_ RX	-	USB_CRS _SYNC	-	SAI1 _SD_A	TIM17 _BKIN	CM4_ EVENTOUT
	PA11	-	TIM1_ CH4	TIM1_ BKIN2	-	-	SPI1_ MISO	-	USART1_ CTS	-	USB_DM	TIM1_ BKIN2	-	-	CM4_ EVENTOUT
	PA12	-	TIM1_ ETR	-	-	-	SPI1_ MOSI	-	USART1_ RTS_DE	LPUART1 _RX	USB_DP	-	-	-	CM4_ EVENTOUT
	PA13	JTMS- SWDIO	-	-	-	-	-	-	-	IR_OUT	USB_NOE	-	SAI1 _SD_B	-	CM4_ EVENTOUT
	PA14	JTCK- SWCLK	LPTIM1_ OUT	-	-	I2C1_ SMBA	-	-	-	-	-	-	SAI1 _FS_B	-	CM4_ EVENTOUT
	PA15	JTDI	TIM2_ CH1	TIM2_ ETR	-		SPI1_ NSS	МСО	-	-	-	-	-	-	CM4_ EVENTOUT



Table 19. Alternate functions (STM32WB35xx) (continued)

		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF10	AF12	AF13	AF14	AF15
F	Port	SYS_AF	TIM1/ TIM2/ LPTIM1	TIM1/ TIM2	SAI1/ TIM1	I2C1/ I2C3	SPI1	RF	USART1	LPUART1	USB/ QUADSPI	COMP1/ COMP2/ TIM1	SAI1	TIM2/ TIM16/ TIM17/ LPTIM2	EVENTOUT
	РВ0	-	-	-	-	-	-	EXT _PA _TX	-		-	COMP1_ OUT	-	-	CM4_ EVENTOUT
	PB1	-	-	-	-	-	-	-	-	LPUART1 _RTS_DE	-	-	-	LPTIM2_ IN1	CM4_ EVENTOUT
	PB2	RTC_ OUT	LPTIM1_ OUT	-	-	I2C3_ SMBA	SPI1_ NSS	i	-	-	-	-	SAI1_ EXTCLK	-	CM4_ EVENTOUT
	РВ3	JTDO- TRACE SWO	TIM2_ CH2	-	-	1	SPI1_ SCK	-	USART1_ RTS_DE	-	-	-	SAI1_ SCK_B	-	CM4_ EVENTOUT
В	PB4	NJTRST	-	-	-	I2C3_ SDA	SPI1_ MISO	-	USART1_ CTS	-	-	-	SAI1_ MCLK_B	TIM17_ BKIN	CM4_ EVENTOUT
	PB5	ı	LPTIM1_ IN1	-	-	I2C1_ SMBA	SPI1_ MOSI	ı	USART1_ CK	LPUART1 _TX	-	COMP2_ OUT	SAI1_ SD_B	TIM16_ BKIN	CM4_ EVENTOUT
	PB6	MCO	LPTIM1_ ETR	-	-	I2C1_ SCL	-	i	USART1_ TX	1	-	1	SAI1_ FS_B	TIM16_ CH1N	CM4_ EVENTOUT
	PB7	ı	LPTIM1_ IN2	-	TIM1_ BKIN	I2C1_ SDA	-	i	USART1_ RX	ı	-	ı	ı	TIM17_ CH1N	CM4_ EVENTOUT
	PB8	ı	TIM1_ CH2N	-	SAI1_ PDM_CK1	I2C1_ SCL	-	ı	ı	ı	QUADSPI_ BK1_IO1	ı	SAI1_ MCLK_A	TIM16_ CH1	CM4_ EVENTOUT
	РВ9	-	TIM1_ CH3N	-	SAI1_ PDM_DI2	I2C1_ SDA	-	-	-	IR_OUT	QUADSPI_ BK1_IO0	-	SAI1_ FS_A	TIM17_ CH1	CM4_ EVENTOUT
С	PC14	-	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT
	PC15	-	-	-	-	=	-	ı	-	-	-	-	-	-	CM4_ EVENTOUT
E	PE4	-	-	-	-	=	-	ı	-	-	-	-	-	-	CM4_ EVENTOUT
н	РН3	LSCO	-	-	-	-	-	-	-	-	-	-	-	-	CM4_ EVENTOUT