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CURRENT POSITION	System Engineer, IBM - Client Innovation Center, Kolkata, India Data Analysis, Application Development and Maintenance, Automation	Dec 2014 to Present
EDUCATION	Bachelor of Technology in Electronics and Communication Engineering, West Bengal University of Technology, Kolkata, India Incl. Course: Electrical Engineering, Computer Science, Mathematics, Physics Thesis: RISC processor based on Harvard Architecture	Jun 2014
WORK EXPERIENCE	Programmer, Hi-Q Solutions, Kolkata, India Part-time System Programming and Designing	Aug 2012 to Dec 2013
	Associate Engineer, Bharat Sanchar Nigam Ltd, Kolkata, India Intern Network Designing	Mar 2013 to May 2013
	Associate Engineer, Siemens – Metal technologies , Kolkata, India Intern <i>Automation</i>	Dec 2012 to Feb 2013
RELEVANT SKILLS	Database: RDBMS, HDFS Programming: SQL, OOP, Java, Python, UNIX Shell Scripting Frameworks: Spring, MVC, Hadoop Operating Systems: Windows, Linux All MS Office programs	
RELEVANT	Oracle Certified Associate – Java SE 8 Programmer, Oracle	Jun2017
CERTIFICATIONS	Hadoop Programming, IBM	Mar 2017
	Hadoop Foundations, IBM	Mar 2017
	Big Data Foundations, IBM	Feb 2017
	Design Thinking, IBM	Nov 2016
	Oracle Database 11g SQL Fundamentals, Oracle	Sep 2016
TALKS	Speaker , presentation on RISC Processors and Harvard Architecture in National Seminar on 'Advanced Microprocessor and VLSI Devices' organized at college.	Apr 2014
	Speaker , presentation on Wireless and Cellular Communications in <i>Industry based Educational Program</i> organized at college.	Jul 2012

RESEARCH AND PROJECTS	Kin'ship Tool Desc: Used Twitter data mining and developed algorithm to find out the most related words (kin) to a particular word as used in tweets.	Jul 2017
	Automation of Routine Application Monitoring and Maintenance IBM, Kolkata, India Desc: Developed routine application monitoring and maintenance jobs and set up the same in Cron to automate running and reporting.	2016
	Application-Interface Message Statistics Tool IBM, Kolkata, India Desc: Developed Bash shell Script, Windows GUI, and Web Application to mine application logs and extract data to calculate statistics of application – interface message exchange.	Nov 2015
	RISC Processor based on Harvard Architecture Senior Thesis Desc: Designed, developed and implemented a 16-bit customized Reduced Instruction Set Processor based on Harvard Architecture	Apr 2014
	Bootable Microcontroller (8051) panel with optional ROM Hi-Q Solutions, Kolkata, India Desc: Designed and developed a microcontroller panel which can be used to directly program a microcontroller, without the need to write and burn the monitor program on a ROM *Owner of the intellectual property of the prototype.	Jul 2013
	College Network Bharat Sanchar Nigam Ltd, Kolkata, India Designed college network using a pre-defined pool of IP address and implemented using NetSim (Network Simulator)	May 2013
	Automatic Motor Controller Hi-Q Solutions, Kolkata, India Used Assembly language programming and digital designing to develop a controller attached to sensors to detect fluid level and accordingly switch	Sep 2012
RELEVANT HONORS	Emerging Techie Award, A.P. Moller Maersk Diamond Awards, IBM	2016
MONORU	12th out of 140 students in the ECE department (in Bachelor's) 8 semesters: 86.6%	Jun 2014
	2nd in Technical Paper Contest organized by the Institution of Electronics and Telecommunication Engineers (IETE) India for thesis.	Apr 2013
	1 st in assessment examination of Vocational Training on Communication System software, conducted by Bharat Sanchar Nigam Limited, Kolkata, India	Jul 2012