Synchronous Sequence detector

Sequence: 1001

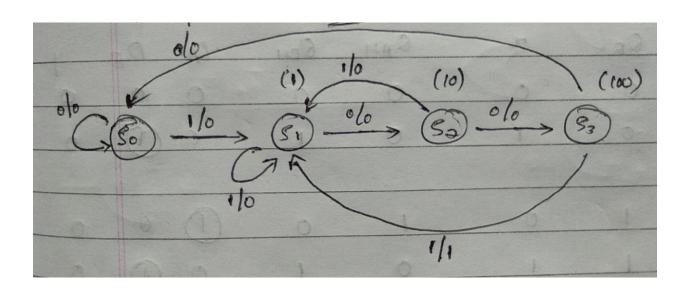
Overlapping is allowed

Flip flop used: D flip flop

States					
S0	00				
S 1	01				
S2	10				
S3	11				

Test Bench input

X	1	0	1	1	0	0	1	0	0	1	0	0
y	0	0	0	0	0	0	1	0	0	1	0	0



- A)	01			.1 5	11 11	SIE TIO	ol:
	Phuen	t Male	lib	NexT	Mate	fle -tip	olp
	O _A	OB	2	QAH	ORH	DA DB	4
0	0	600	0	(6)	0	0 0	0
1	0	0) < 1	0	-(12)	00	0
					75		
2	0	1	0		0	10	0
3	0	1	4	110	1	0 1	0
				00			
4	ı	0	0	18	1	(I) (D)	0
5	1	0	× 1 0	, 0	11	0 0	0
				/			
6	1	10	0	0	0	0 0	0
7	1	1	1	00	- 1	0 0	1
		DA =	1 (QA, Q	B. 91) = E	(A, G)	= DARBZ +	OA OBE
						,5,7)= 71+	
				B, X) = 2			
		1				Vidya	(254)

