Report of Assignment 3 — OpenMP Programming

Multiprocessor Systems, DV2544

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1 Introduction

OpenMP is a portable standard for the programming of shared memory systems. The OpenMP API (application program interface) provides a collection of compiler directives, library routines, and environmental variables [1]

2 Quicksort

2.1 Implementation

This OpenMP version implement of quicksort is adopted Kil and others' algorithm from their article [2] seen as figure 1 which is impletated as figure 2.

Figure.1 Quick sort algorithm from[2].

Figure.2 Implement of algorithm within figure 1.

It is used task decomposition which is OpenMP task directives (#pragram omp task) to do the calculation of sub-arrays. The data distribution is shown as figure 3 with 4 threads. The root of recursion only use one threads and second time it will use two if subarrays is more than threads. Else subarrays will handled by thread which is idle dynamically.

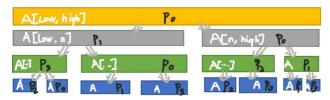


Figure.3 data distribution of 4 threads.

2.2 Measurements

Threads	Seq	2	4	8
Runtime	68.63s	39.01s	28.90s	40.7s
Speedup	-	1.76	2.37	1.56

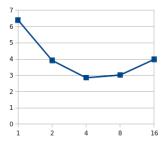


Table.1 runtime and speedup of different threads

Figure.4 runtime of different threads.

Here speedup= Sp(n) = T*(n) / Tp(n) [1]. From table 1 we can see with the decreased number of threads the performance in runtime doesn't get a linear optimization. It may because a larger part of execution is not executed currently such as first iteration and second is done by one thread and two threads.

3 Gauss Elimination

3.1 Implementation

Due to the data dependency, we can't parallel the most outer loop, so the parallelism has happened at the elimination steps. It is used the directive "#pragma omp for schedule(static)" to parallelize the inner nested two for loops. Because its've chosen the schedule way as static, the data structure would be row-wise and block-wise statically. The matrix divided in chunks and assigned to the 8 threads just like figure 5. Then 8 threads would work currently. The algorithm referred S.F. McGinn's pseudocode in *part 2. OpenMP Parallel Version* of his article [3] which is implemented as shown in figure 6.

[0-7][0-63]	P0	
[8-15] [0-63]	P1	
[16-23] [0-63]	P2	
[24-31] [0-63]	Р3	
[32-39] [0-63]	P4	
[40-47] [0-63]	P5	
[48-55] [0-63]	P6	
55-63] [0-63]	P7	
	55-63] [0-63]	55-63] [0-63] P7

Figure.5 data struct example with 8 threads and 64*64 matrix a. Figure.6 core code of implementation

3.2 Measurements

The runtime result and speedup of OpenMP version gaussian elimination implementation which executed with 8 threads shown in table 2.

Gaussian_seq	Gaussian_omp	Speedup[2]
68.754s	40.234s	1.709

 $Table. 2\ the\ speedup\ of\ Open MP\ implement\ of\ gaussian\ elimination.$

Note: speedup= Sp(n) = T*(n) / Tp(n) = 68.754s / 40.234s = 1.709[1].

Reference

- 1. Rauber, Thomas, and Gudula Rünger. *Parallel programming: For multicore and cluster systems*. Springer Science & Business Media, 2013.
- 2. Kim, Kil Jae, Seong Jin Cho, and Jae-Wook Jeon. "Parallel quick sort algorithms analysis using OpenMP 3.0 in embedded system." *Control, Automation and Systems* (ICCAS), 2011 11th International Conference on. IEEE, 2011.
- 3. McGinn, S. F., and Ruth E. Shaw. "Parallel Gaussian elimination using OpenMP and MPI." *High Performance Computing Systems and Applications, 2002. Proceedings. 16th Annual International Symposium on.* IEEE, 2002.