

JS HW

Sheet: /PSU/
File: PSU.kicad_sch

Title: CDRC – Custom DRC for Audison AC Link

Size: A4
Date: 03-09-2024

Rev: A
Id: 2/5

[illegible]

STRAPPING PINS:
 GPIO0: BOOT MODE
 GPIO2: MUST BE FLOATING OR LOW TO ENTER FLASHING MODE
 GPIO6–GPIO11 – INTEGRATED FLASH SPI PINS
 GPIO12: FAILS TO BOOT IF PULLED HIGH
 GPIO14: OUTPUTS PWM ON BOOT
 GPIO15: OUTPUTS PWM ON BOOT & STRAPPING

Sheet: /MCU/
File: MCU.kicad_sch

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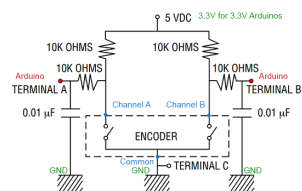
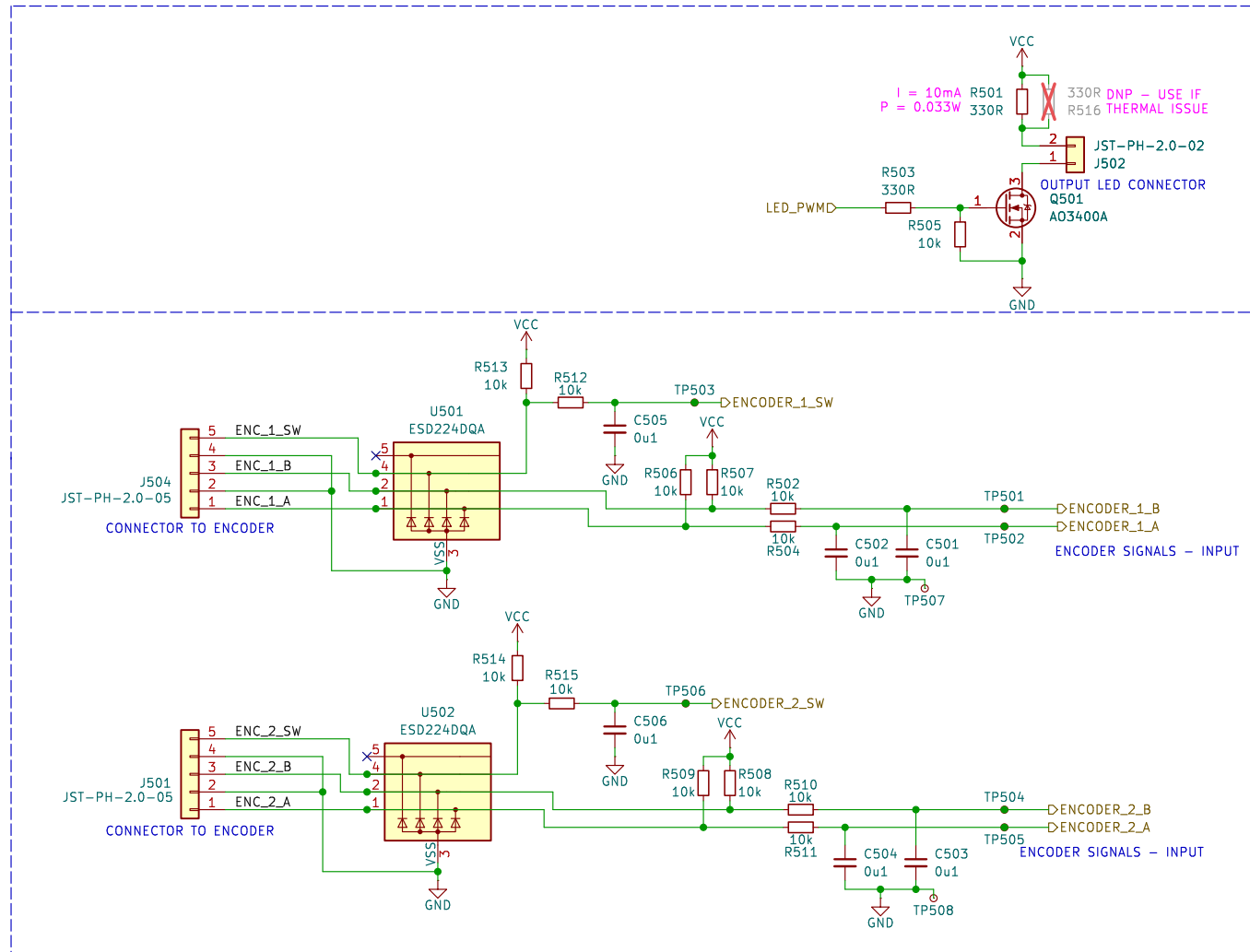
SIZE: A1	
KiCad E.D.A. 8.0.4	

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JS HW

Sheet: /USER_INTERFACE/
File: USER_INTERFACE.kicad_sch

Title: CDRC – Custom DRC for Audison AC Link

Size: A4 Date: 03-09-2024

KiCad E.D.A. 8.0.4

Rev: A

Id: 5/5