

#### REVISION HISTORY:

**A:** Initial design  
**B:** Changed RS485 RX connection to ESP32 from GPIO21 to GPIO27 (RTC IO)  
Removed solder bridge pads to control RS485\_RX\_EN. Confirmed working as intended  
Overhaul PCB outline and layout

JS HW

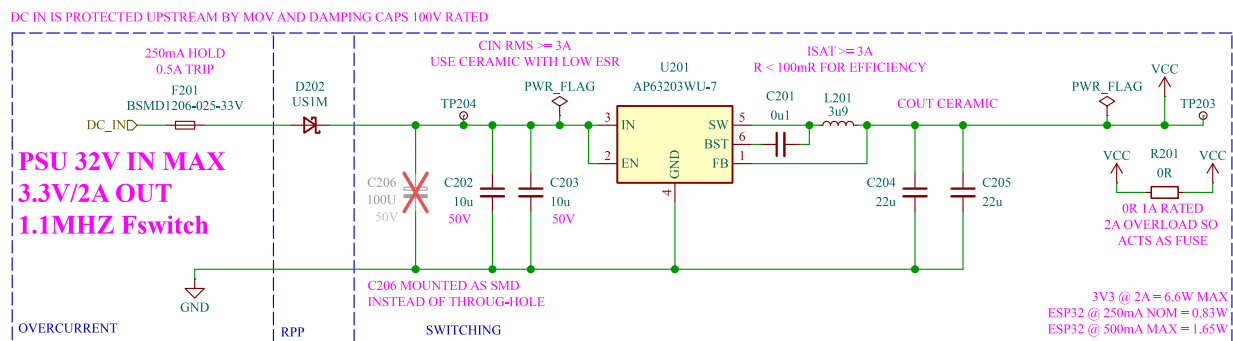
Sheet: /  
File: CDRC.kicad\_sch

**Title: CDRC - Custom DRC for Bit 10D**

Size: A4  
KiCad E.D.A. 8.0.4

Date: 01-11-2024

Rev: **B**  
Id: 1/5



JS HW

Sheet: /PSU/  
File: PSU.kicad\_sch

**Title: CDRC - Custom DRC for Bit 10D**

Size: A4  
KiCad E.D.A. 8.0.4

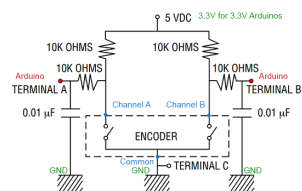
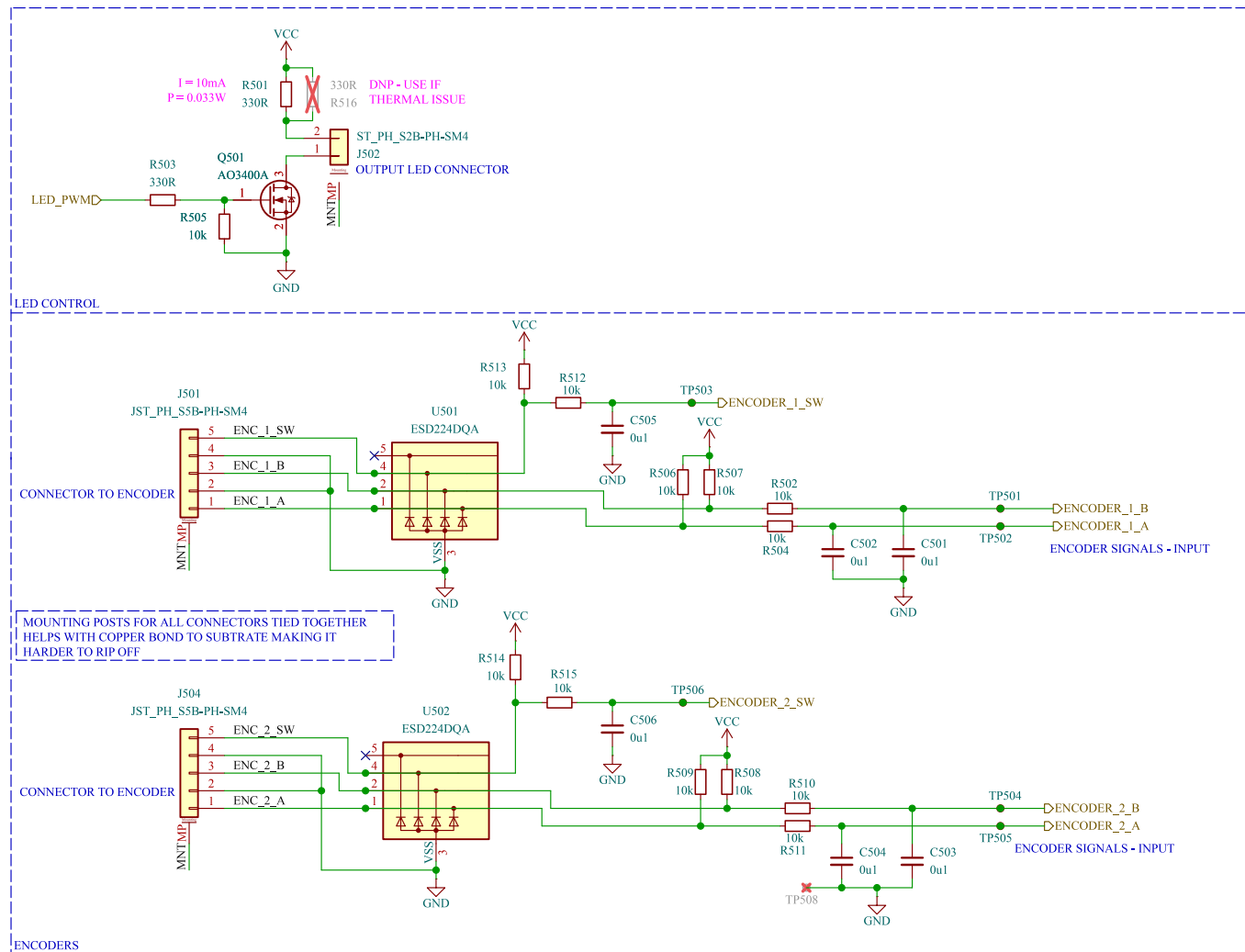
Date: 01-11-2024

Rev: B  
Id: 2/5

[illegible]

Rev: B  
Id: 3/5





# JS HW

Sheet: /USER\_INTERFACE/  
File: USER\_INTERFACE.kicad\_sch

## Title: CDRC - Custom DRC for Bit 10D

Size: A4  
KiCad E.D.A. 8.0.4

Date: 01-11-2024

Rev: B  
Id: 5/5