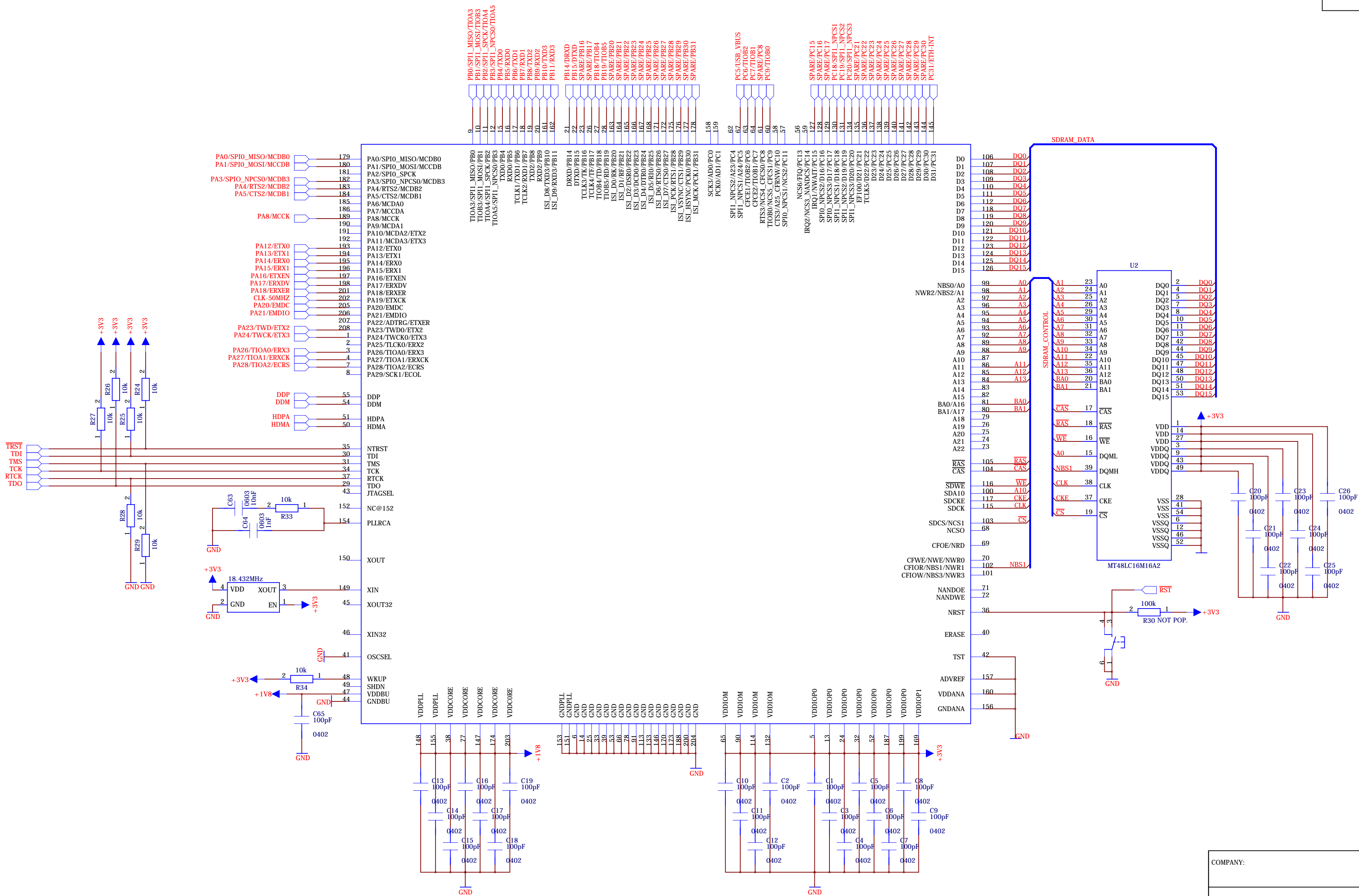


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN: M.QUADRINI	DATED: 27/03/2012
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

DATED:
<Checked Date>

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<QC Date>

DATED:
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COMPANY:				Lilit			
TITLE:							
AT91SAM9XE128							
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6

5

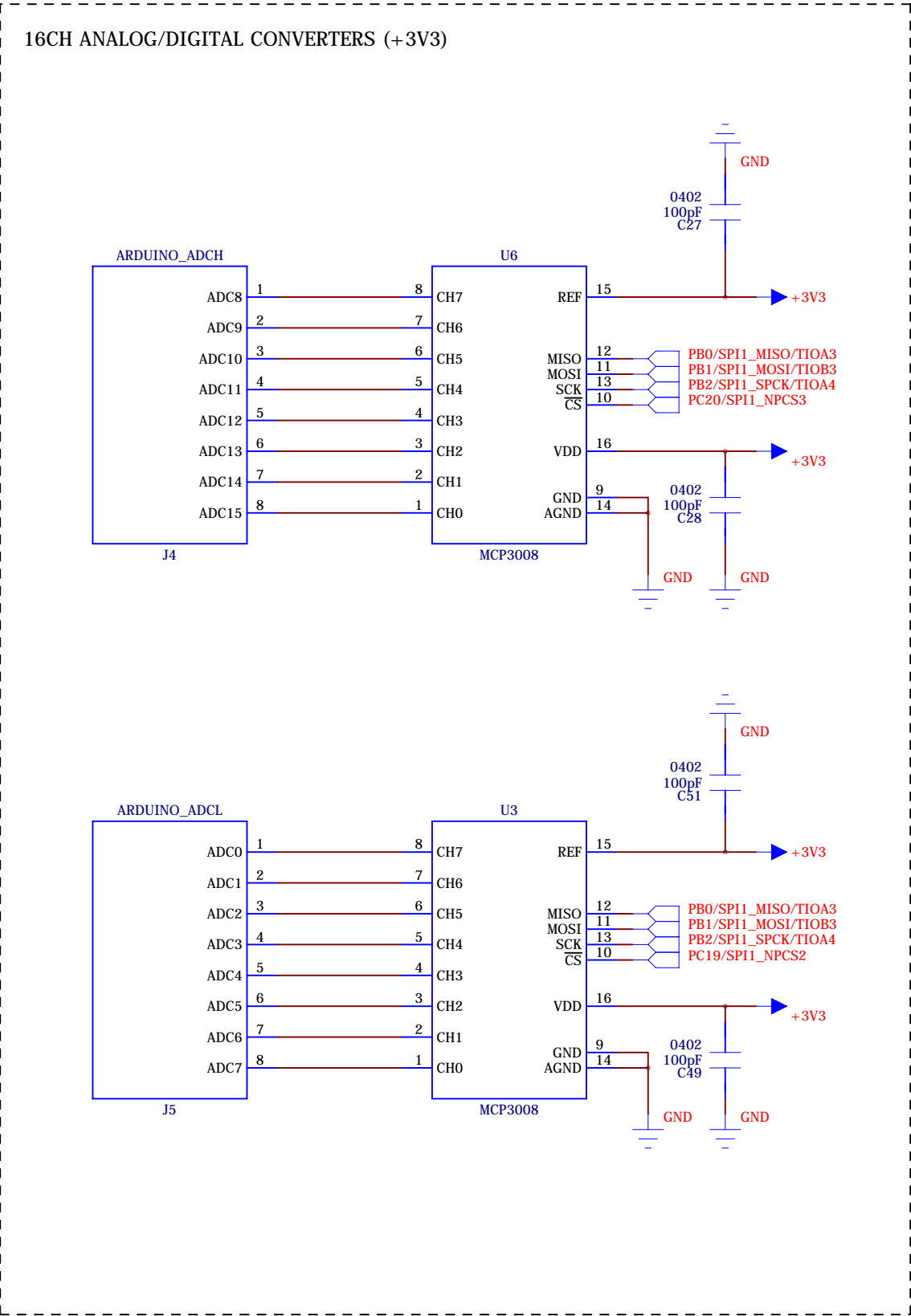
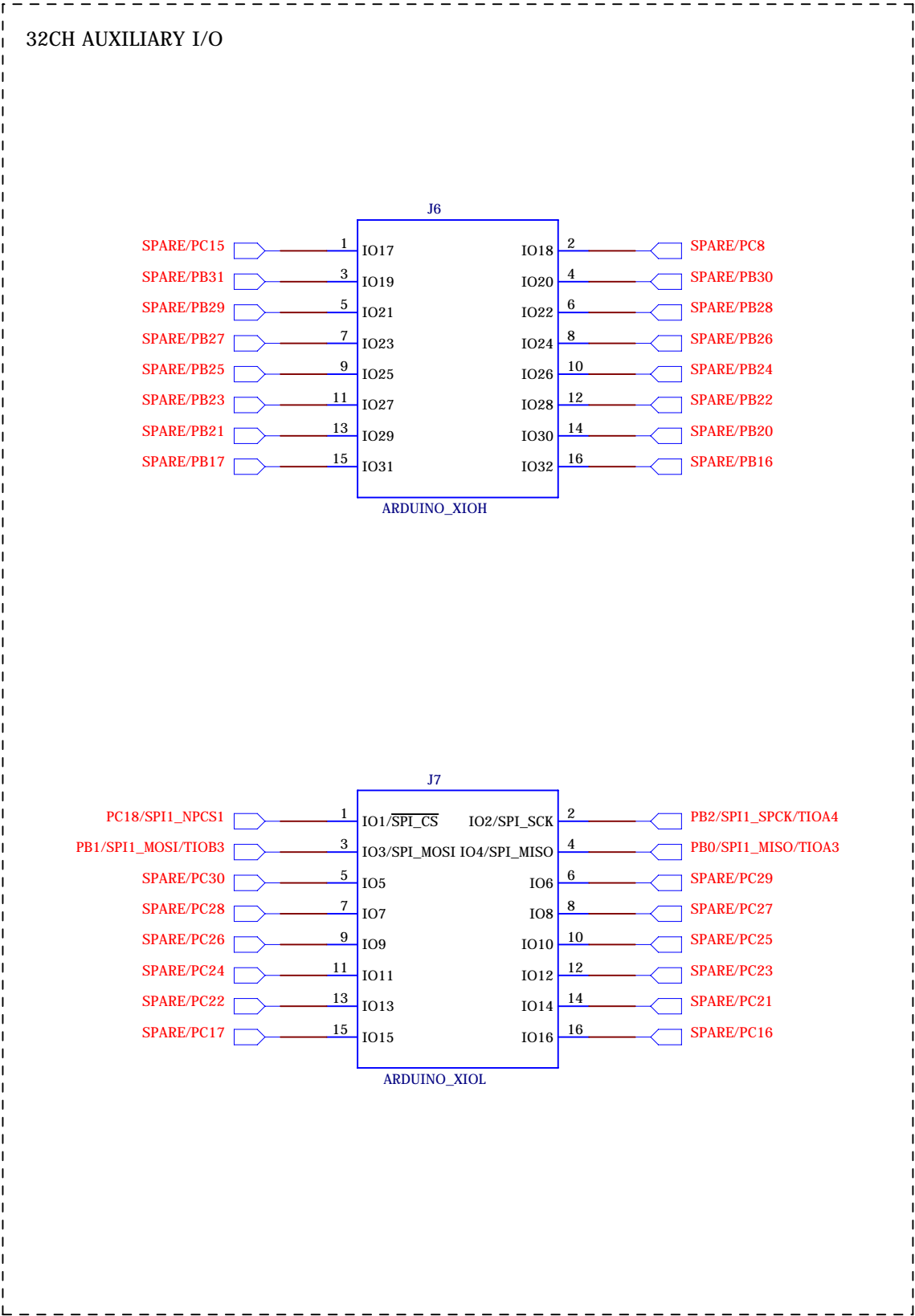
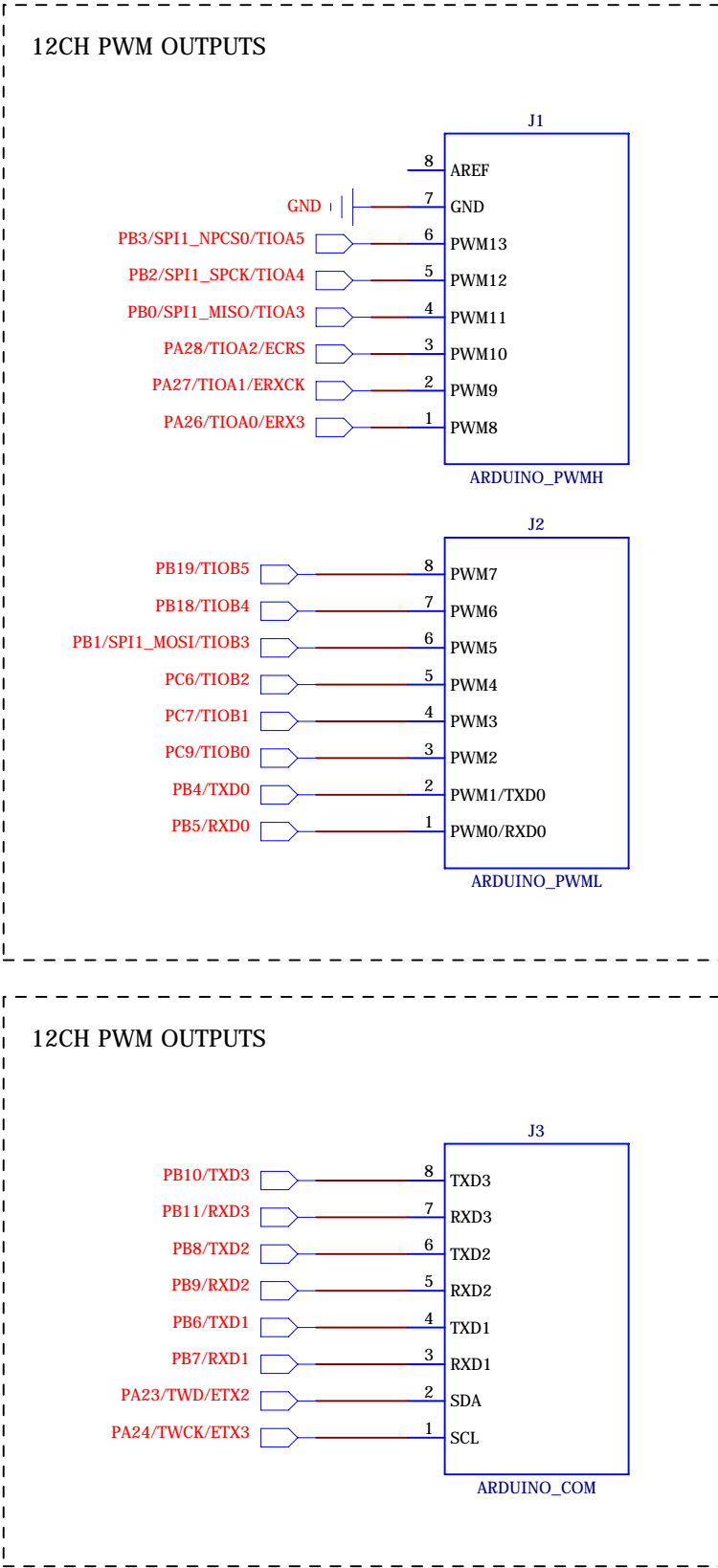
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REVISION RECORD			
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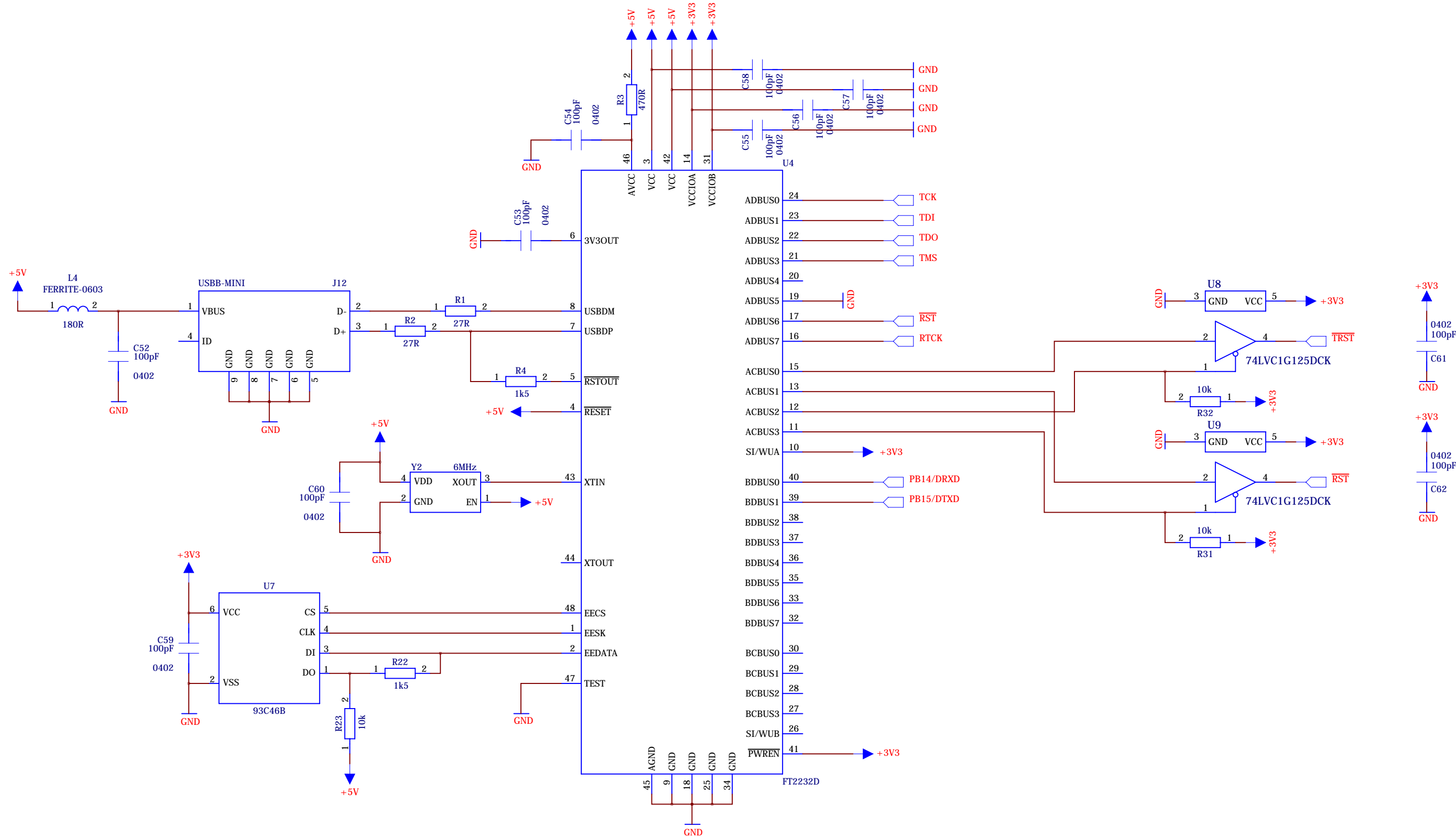


COMPANY: Lilit			
TITLE: Arduino Mega compatible interface			
DRAWN: M.QUADRINI	DATED: 27/03/2012	CODE:	SIZE:
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO:	
QUALITY CONTROL: <QC By>	DATED: <QC Date>	REV:	
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SHEET: 2 OF 5		REV: 5	

DRAWN: M.QUADRINI	DATED: 27/03/2012
CHECKED: <Checked By>	DATED: <Checked Date>
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Lilit			
TITLE: JTAG/Debug RS232 interface			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	A2	<Drawing Number>	<Revision>
SCALE: <Scale>		SHEET: 4 OF 5	

DRAWN: M.QUADRINI	DATED: 27/03/2012
CHECKED: <Checked By>	DATED: <Checked Date>
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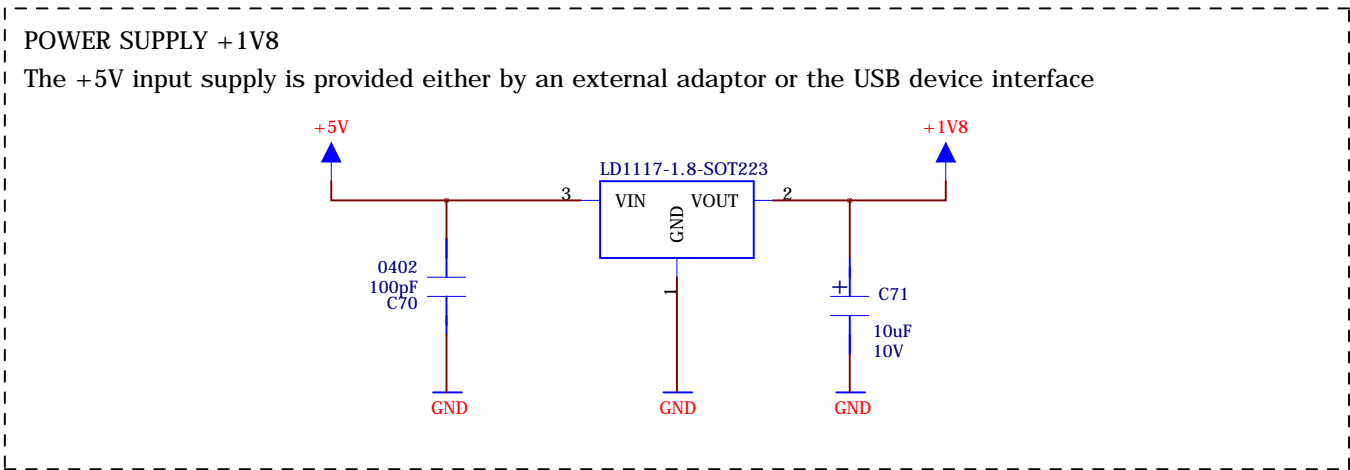
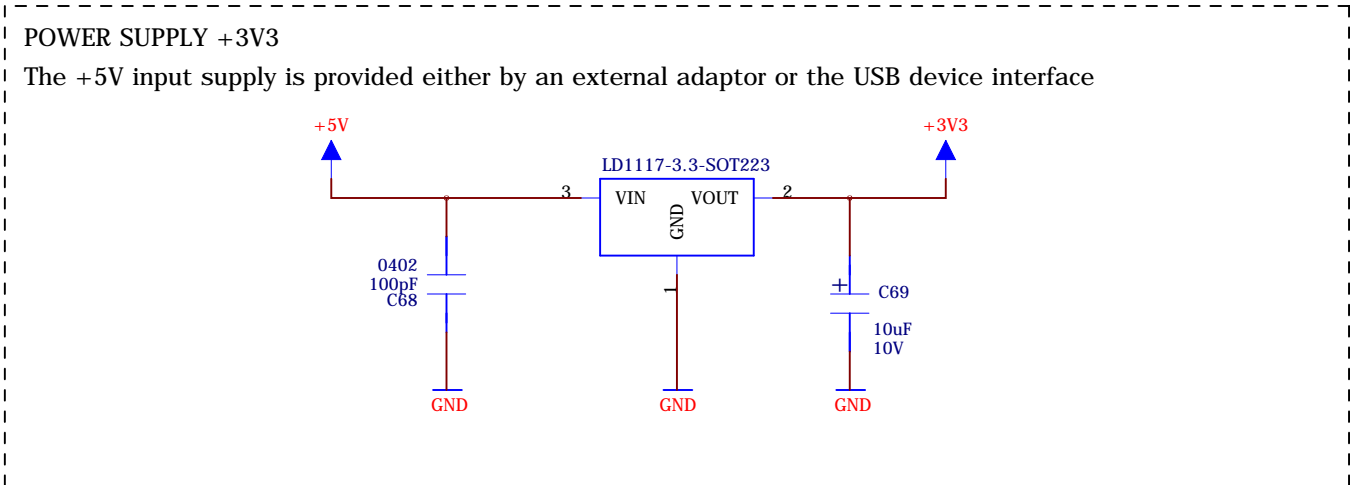
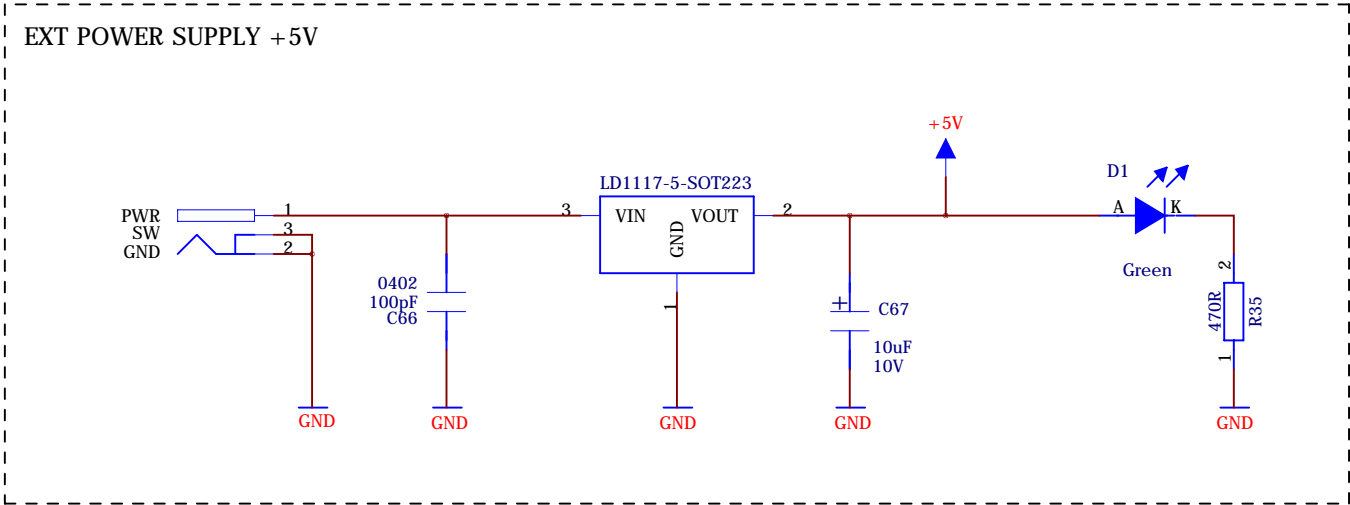
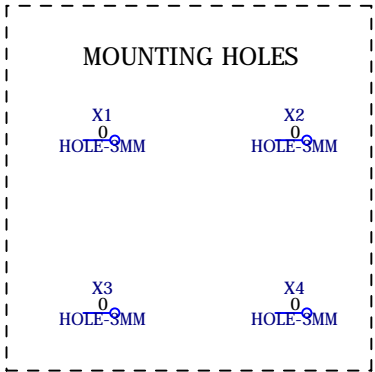
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Lilit			
TITLE: Power supplies			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	A2	<Drawing Number>	<Revision>
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DRAWN: M.QUADRINI	DATED: 27/03/2012
CHECKED: <Checked By>	DATED: <Checked Date>
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