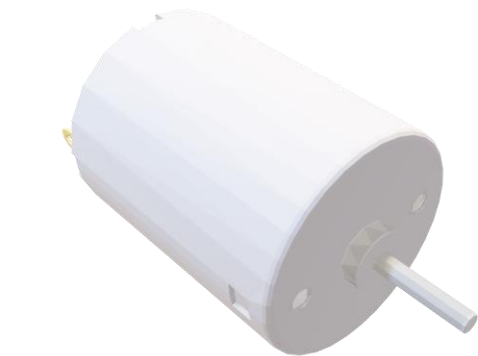
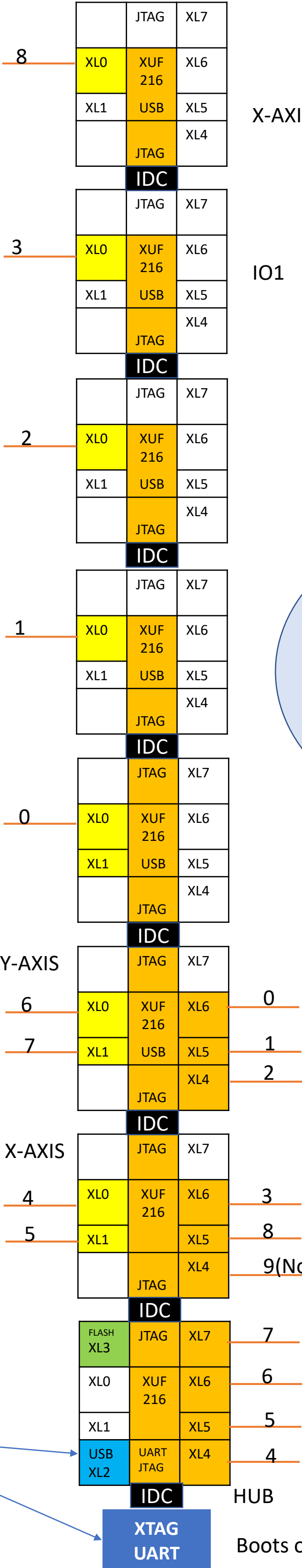


Tree network + JTAG line.
Compiler can create
the routing table for PCBs.
Hub can be flashed, cards separated,
without any change to the code.
Isolation works when separated.
Suitable for network where the
branches does not talk to each other
with high bandwidth.



COLOR = ACTIVATED

Tested with 9 PCBs in tree
+ XTAG line.
Full JTAG debug.
Realtime Stdout over
UART.
Compiles and runs in the
simulator.

