

openPnP

Sheet:

File: XMOS_XUF216_FB236.kicad_pcb

Title: 4 layer 100x100 mm PCB

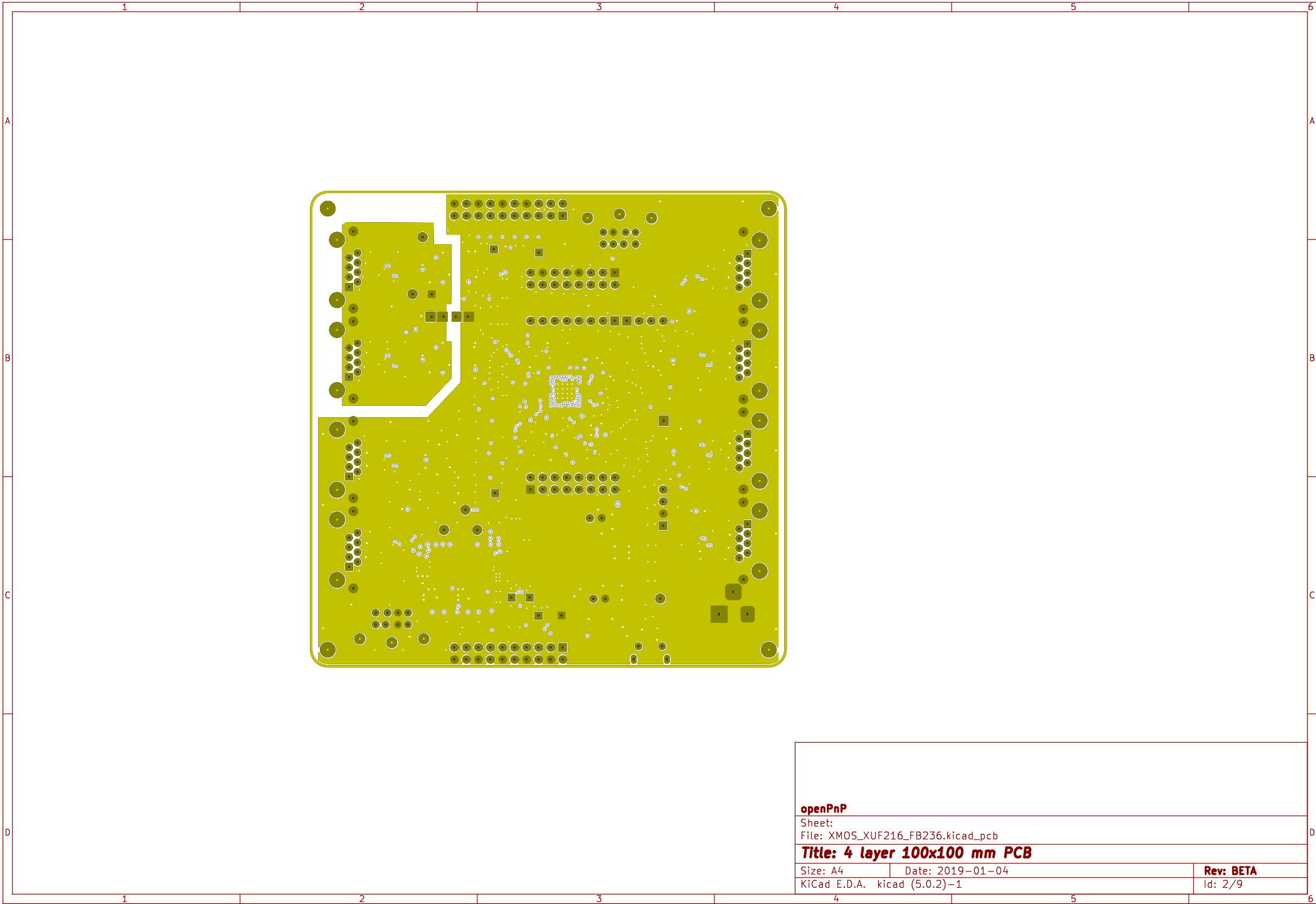
Size: A4

Date: 2019-01-04

Rev: BETA

KiCad E.D.A. kicad (5.0.2)-1

Id: 1/9



openPnP

Sheet:
File: XMOS_XUF216_FB236.kicad_pcb

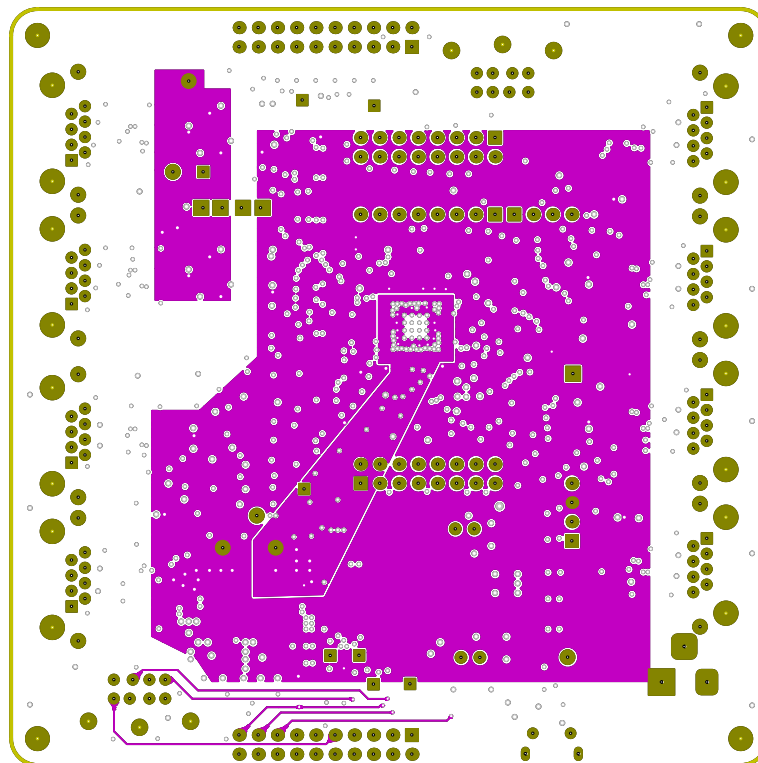
Title: 4 layer 100x100 mm PCB

Size: A4 Date: 2019-01-04

KiCad E.D.A. kicad (5.0.2)-1

Rev: BETA

Id: 2/9



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File: XMOS_XUF216_FB236.kicad_pcb

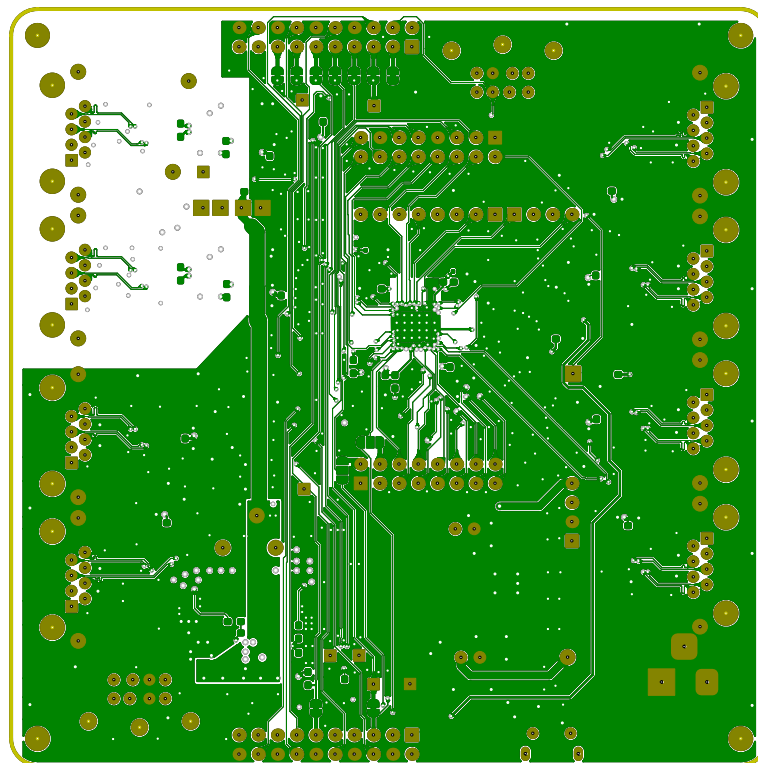
Title: 4 layer 100x100 mm PCB

Size: A4 Date: 2019-01-04

KiCad E.D.A. kicad (5.0.2)-1

Rev: BETA

Id: 3/9



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Sheet:

File: XMOS_XUF216_FB236.kicad_pcb

Title: 4 layer 100x100 mm PCB

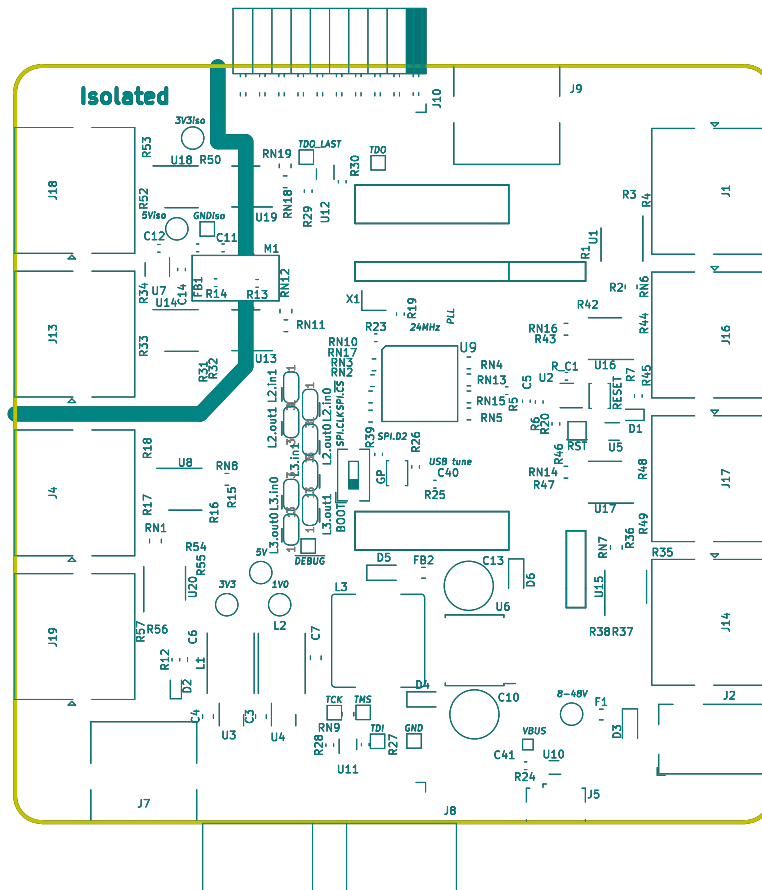
Size: A4

Date: 2019-01-04

Rev: BETA

KiCad E.D.A. kicad (5.0.2)-1

Id: 4/9



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KiCad E.D.A. kicad (5.0.2)-1

Rev: BETA

Id: 5/9



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Title: 4 layer 100x100 mm PCB

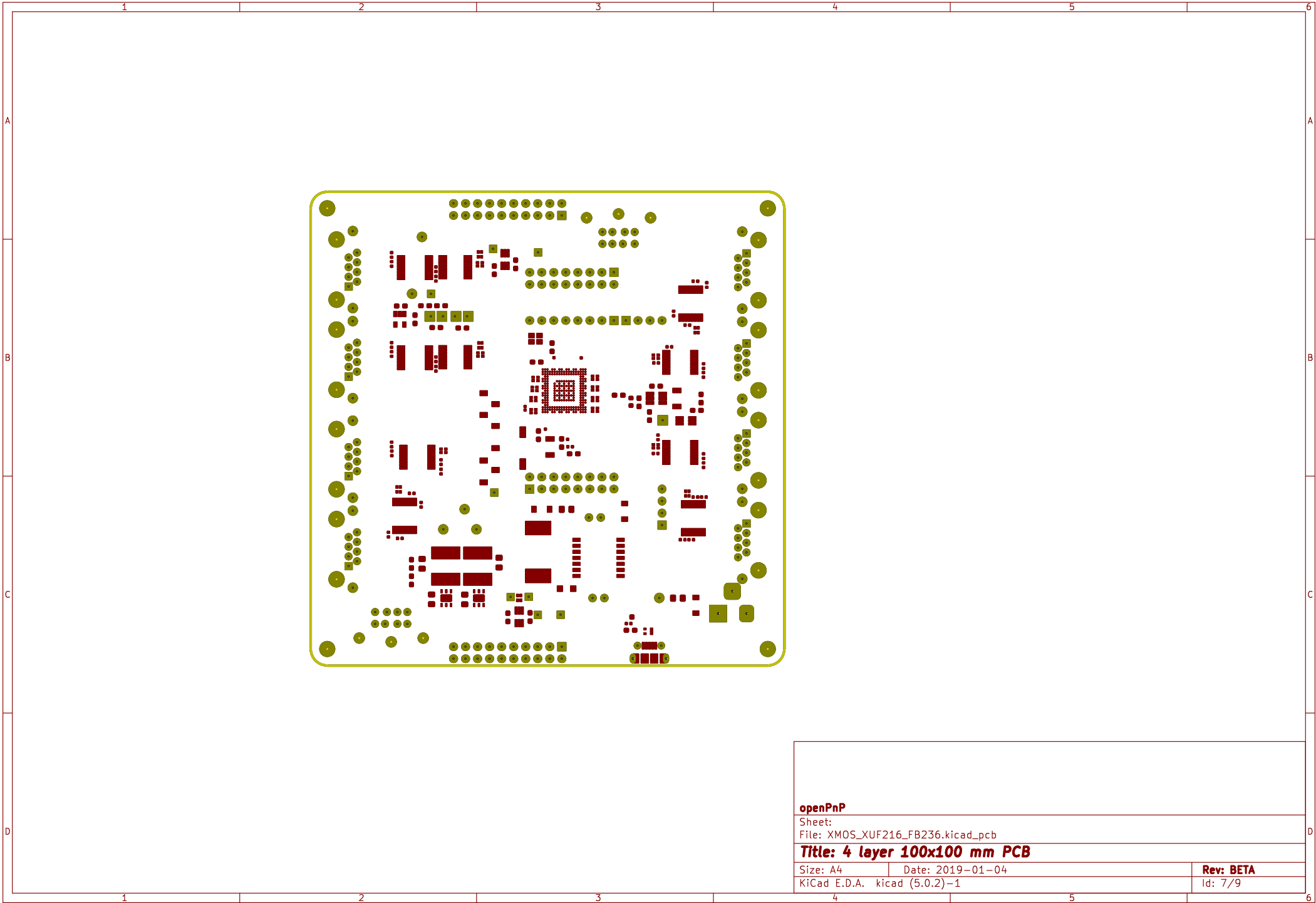
Size: A4

Date: 2019-01-04

KiCad E.D.A. kicad (5.0.2)-1

Rev: BETA

Id: 6/9



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Sheet:
File: XMOS_XUF216_FB236.kicad_pcb

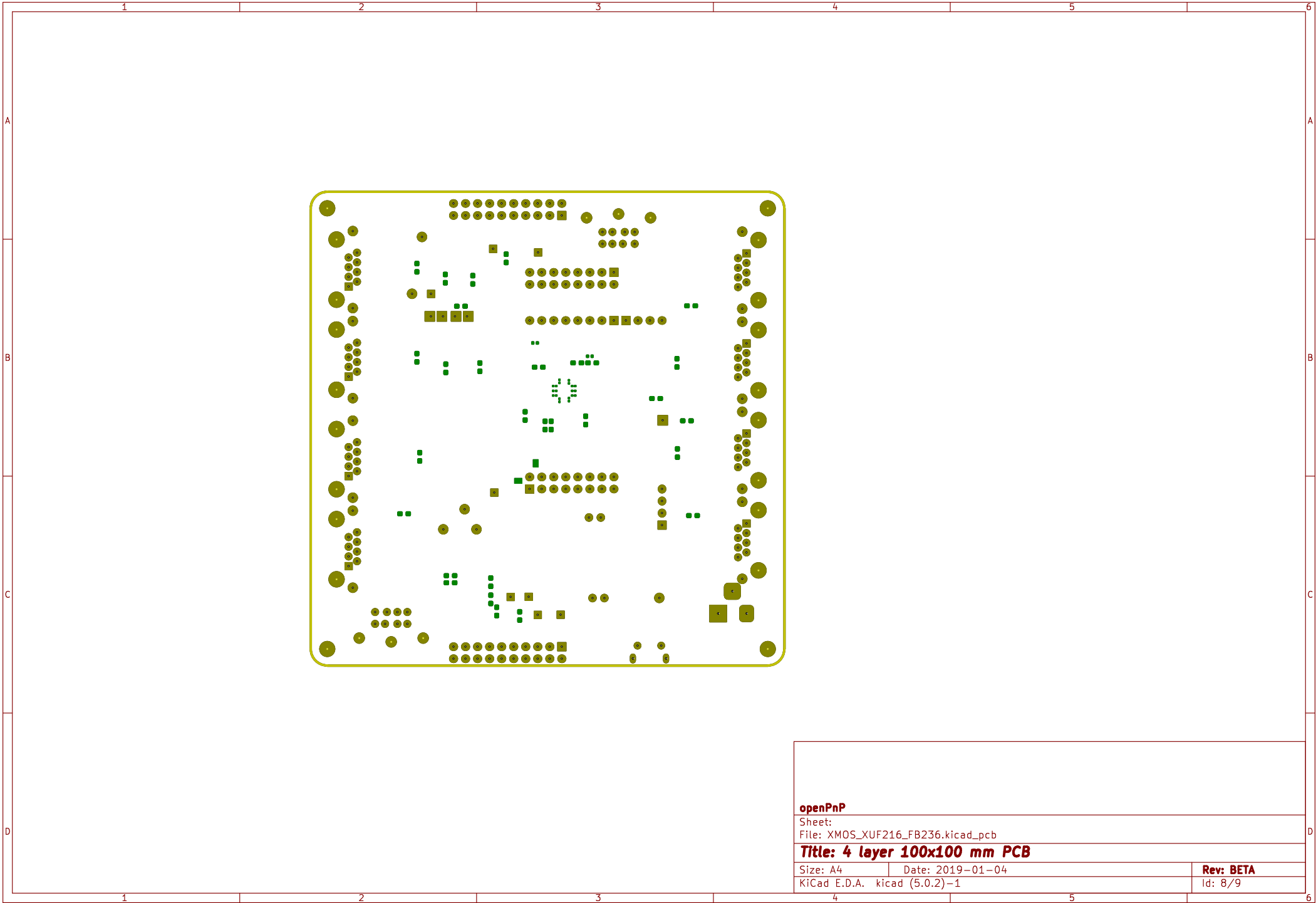
Title: 4 layer 100x100 mm PCB

Size: A4 Date: 2019-01-04

KiCad E.D.A. kicad (5.0.2)-1

Rev: BETA

Id: 7/9



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Sheet:
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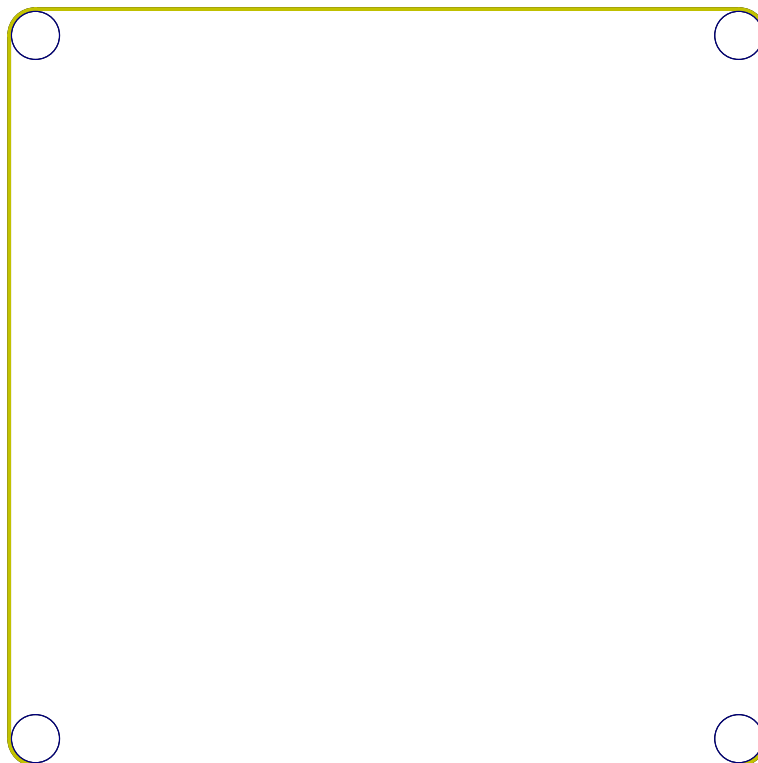
Size: A4 Date: 2019-01-04

KiCad E.D.A. kicad (5.0.2)-1

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Decrease via diameters with 3 mil and redo teardrops before making Gerbers.
Workaround to achieve different clearance for wires and vias.



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Id: 9/9