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File: file5C2628B0.sch

Title:

Size: A4
KiCad E.D.A. kicad (5.0.2)-1

Date:

Rev:

Id: 2/10



Sheet: /LVDS4/
File: file5C2628B0.sch

Title:

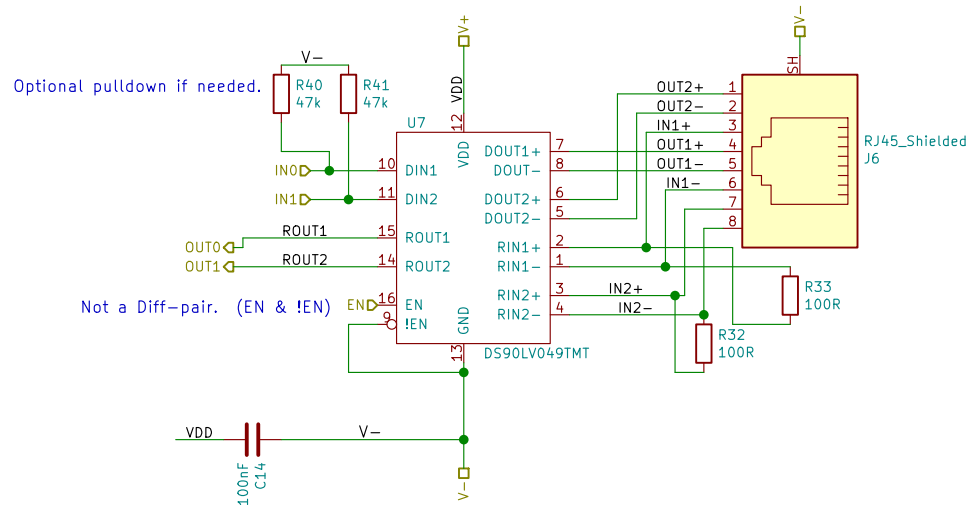
Size: A4

Date:

KiCad E.D.A. kicad (5.0.2)-1

Rev:

Id: 3/10



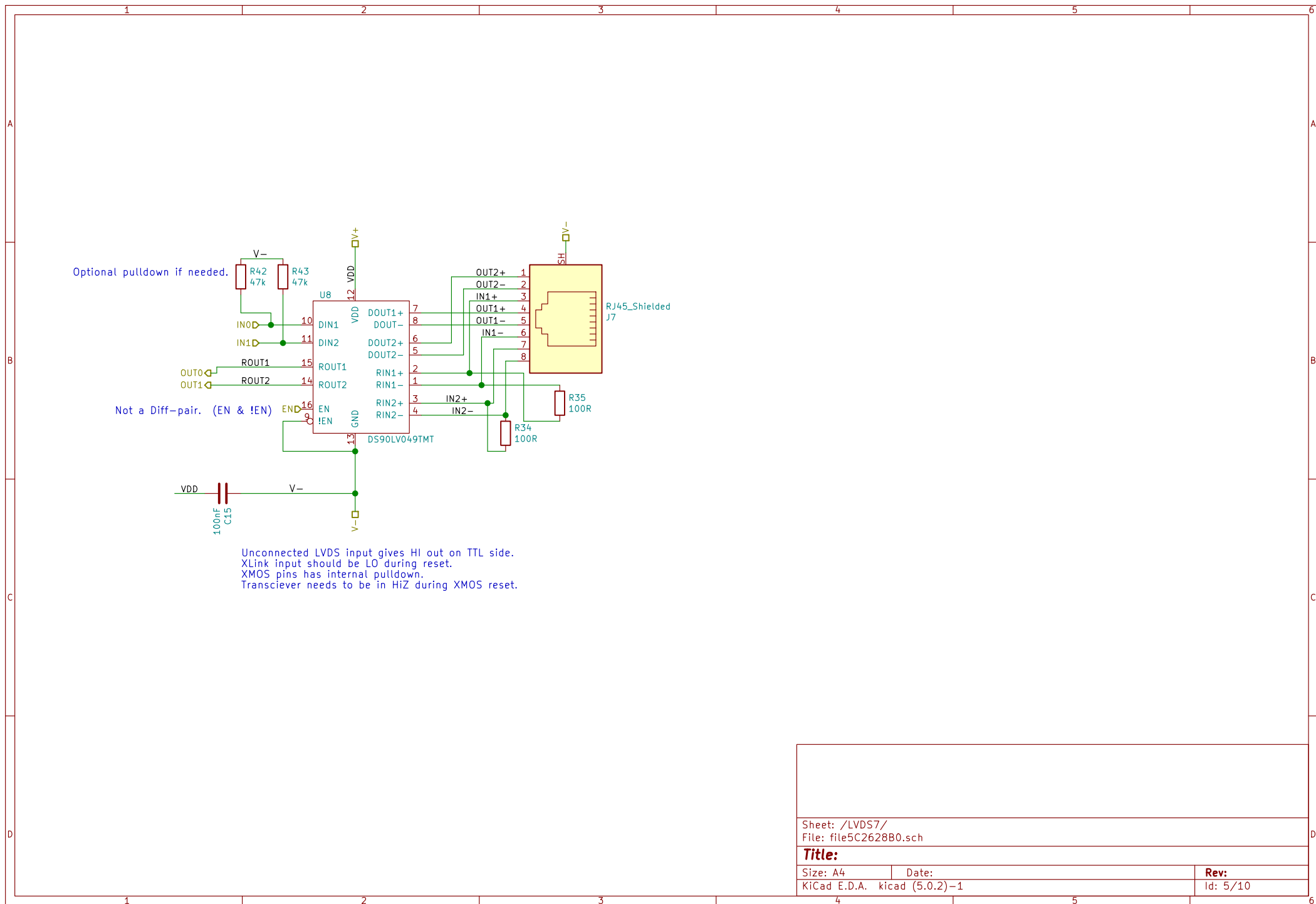
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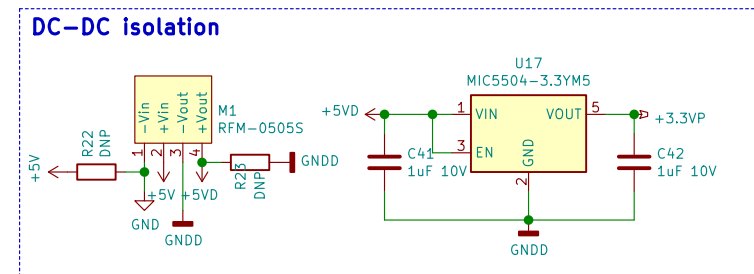
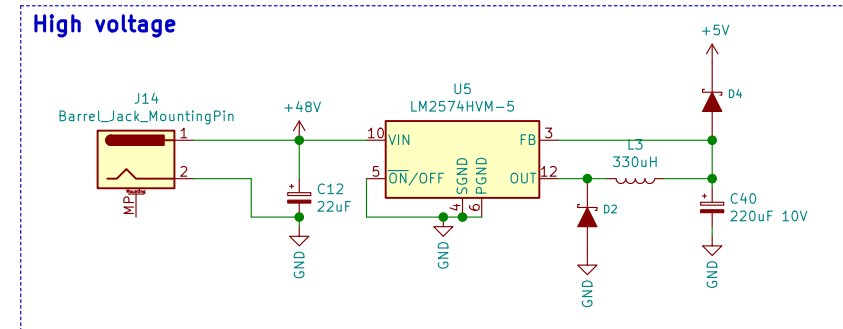
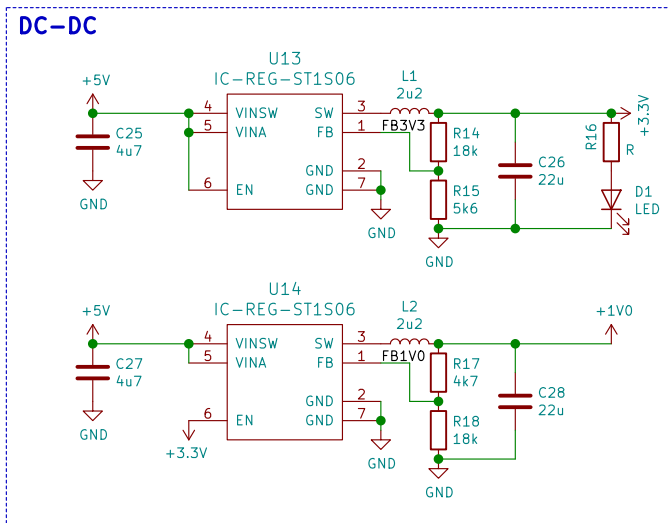
Title:

Size: A4
 KiCad E.D.A. kicad (5.0.2)-1

Date:

Rev:
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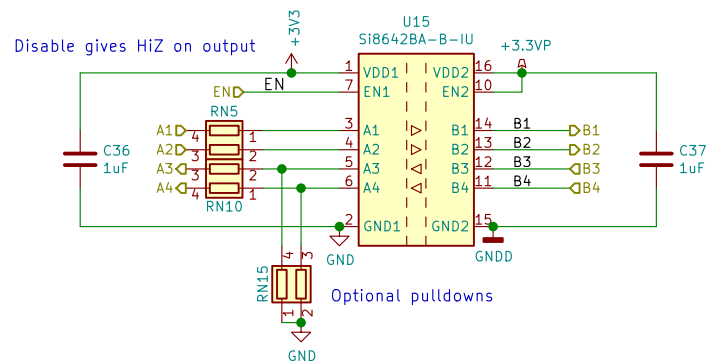
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Size: A4
KiCad E.D.A. kicad (5.0.2)-1

Date:

Rev:
Id: 6/10



On XLinks-2 wire, only one transition per clock cycle can happen within the pair, to avoid jitter/skew sensitivity.

Sheet: /ISOLATION_0/
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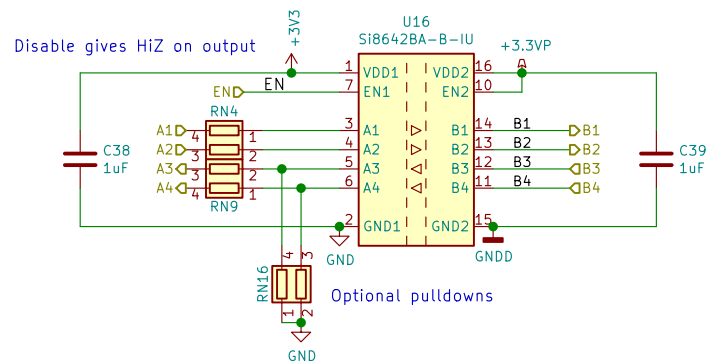
Title:

Size: A4
KiCad E.D.A. kicad (5.0.2)-1

Date:

Rev:

Id: 7/10



On XLinks-2 wire, only one transition per clock cycle can happen within the pair, to avoid jitter/skew sensitivity.

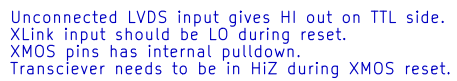
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File: file5D2AE1E1.sch

Title:

Size: A4
KiCad E.D.A. kicad (5.0.2)-1

Date:

Rev:
Id: 8/10





Sheet: /LVDS5/
 File: file5C2628B0.sch

Title:

Size: A4	Date:	Rev:
KiCad E.D.A.	kiCad (5.0.2)–1	Id: 10/10