Simple line network. Compiler can create the routing table for any length. The Xlink bandwidth must be shared between the boards.

Not suitable for robotics.

PC

	XL3 JTAG	XL7
XL0	XUF 216	XL6
XL1	USB	XL5
	XL2 JTAG	XL4
	IDC	
	XL3 JTAG	XL7
XL0	XUF 216	XL6
XL1	USB	XL5
	XL2 JTAG	XL4
	IDC	
	XL3 JTAG	XL7
XL0	XUF 216	XL6
XL1	USB	XL5
	XL2 JTAG	XL4
	IDC	
	XL3 JTAG	XL7
XL0	XUF 216	XL6
XL1		XL5
	XL2 JTAG	XL4
	IDC	
FLASH	XL3 JTAG	XL7
XL0	XUF 216	XL6
XL1		XL5
USB	XL2 JTAG	XL4
	IDC	
XTAG		

COLOR = ACTIVATED

Tested with 8 PCBs in a line + XTAG. Full JTAG debug. Compiles and runs in the simulator.

Boots over JTAG or Flash