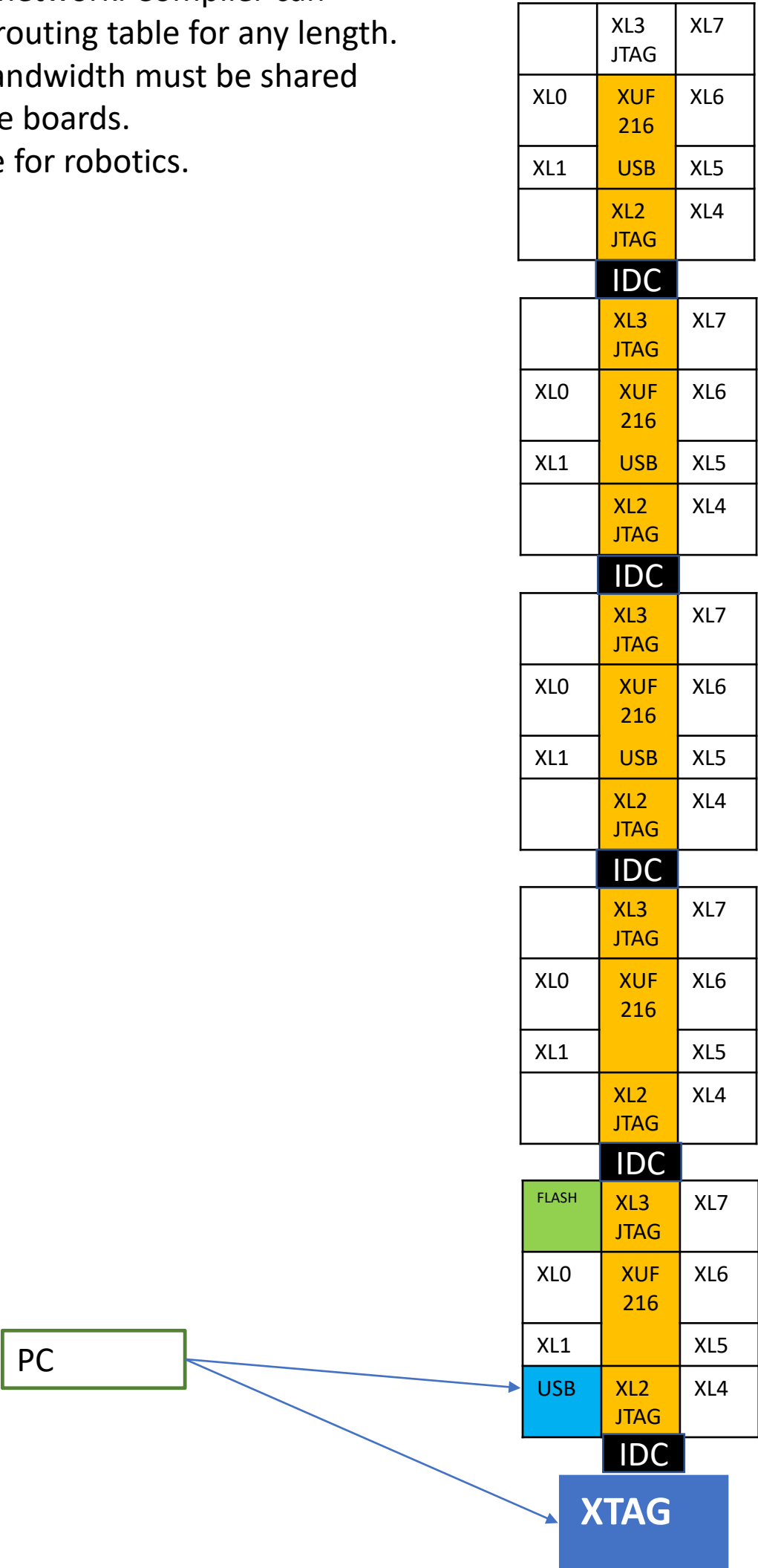


Simple line network. Compiler can  
create the routing table for any length.  
The Xlink bandwidth must be shared  
between the boards.  
Not suitable for robotics.



COLOR = ACTIVATED

Tested with 8 PCBs  
in a line + XTAG.  
Full JTAG debug.  
Compiles and runs  
in the simulator.

Boots over JTAG or Flash