

Homework 4

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CSE 278

1. Components of Von Newman architecture:
 1. ALU
 2. Control Unit
 3. Memory
 4. I/O
2. The EIP register is the instruction pointer register. It points to the location of the next instruction to be run by the processor.
3. The CS register points to the location of the code segment in memory. The DS register points to the data segment in memory. The SS register points to the location of the stack segment in memory. The ES register can be used to point to “extra” segments of memory, like video memory.
4. Addressing modes:
 - (a) Base addressing
 - (b) Displacement addressing
 - (c) Displacement addressing
 - (d) Displacement addressing
5. This instruction would be located at 2100H.