

# YIBO LIN

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## RESEARCH INTERESTS

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Physical design automation, placement and routing

## EDUCATION

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### University of Texas at Austin, TX, USA

*Aug. 2013 – May 2018*

Ph.D. student, Department of Electrical and Computer Engineering (GPA 3.96/4.0)

Advisor: David Z. Pan

### Shanghai Jiao Tong University, Shanghai, P.R.China

*Sep. 2009 – Jun. 2013*

B.S., Department of Microelectronics (GPA 91.17/100)

## TEACHING EXPERIENCE

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Instructor	Introduction to Computing	Fall 2020
Guest Lecture	EE382M: VLSI CAD & Optimizations	Fall 2018
Guest Lecture	EE382M: VLSI Physical Design Automation	Fall 2017

## AWARDS AND HONORS

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Best Paper Award	ISPD	2020
Best Paper Award Nomination	ASPDAC	2020
Best Paper Award (×1) & Nomination (×1)	DAC	2019
Best Paper Award Nomination	ISPD	2019
Inaugural Best Paper Award	Integration, the VLSI Journal	2018
Graduate Continuing Fellowship	University of Texas at Austin	2017
Franco Cerrina Memorial Best Student Paper Award	SPIE	2016
A. Richard Newton Young Student Fellow	DAC	2014

## SERVICES

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TPC members: ICCAD 2018, ICCAD 2019, ICCD 2019, ISPD 2020, DAC 2020, ICCAD 2020

## SELECTED PUBLICATIONS

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- [1] Wei Li, Yuzhe Ma, Qi Sun, Zhang Lu, **Yibo Lin**, Iris Hui-Ru Jiang, Bei Yu and David Z. Pan, “[OpenMPL: An Open Source Layout Decomposer](#)”, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD), 2020. (accepted)
- [2] **Yibo Lin**, Wuxi Li, Jiaqi Gu, Haoxing Ren, Brucek Khailany and David Z. Pan, “[ABCDPlace: Accelerated Batch-based Concurrent Detailed Placement on Multi-threaded CPUs and GPUs](#)”, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD), Feb, 2020. (accepted)
- [3] **Yibo Lin**, Bei Yu, Xiaoqing Xu, Jih-Rong Gao, Natarajan Viswanathan, Wen-Hao Liu, Zhuo Li, Charles J Alpert and David Z. Pan, “[MrDP: Multiple-row detailed placement of heterogeneous-sized cells for advanced nodes](#)”, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD), Jun, 2018.
- [4] **Yibo Lin**, Bei Yu, Biying Xu and David Z. Pan, “[Triple patterning aware detailed placement toward zero cross-row middle-of-line conflict](#)”, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD), Jul, 2017.
- [5] Zizheng Guo, Tsung-Wei Huang and **Yibo Lin**, “[GPU-Accelerated Static Timing Analysis](#)”, IEEE/ACM International Conference on Computer-Aided Design (ICCAD), Nov 2-5, 2020.