

Traveling module checklist

Reception inspection

1. ☐ Enter in table date when module was received
2. ☐ Perform visual inspection of module (particularly wirebonds), including photos
3. ☐ Check jumpers on Single Chip Card (SCC)

Basic communication

1. ☐ Change chip name in DAQ's chip configuration file.
2. ☐ Check that the chip can be configured by running a quick scan (e.g. a digital scan).

Trim IREF and voltage regulators

1. ☐ Measure IREF using a current meter (e.g. Keithley 2400) and trim using IREF_TRIM bits till it is 4 μ A.
2. ☐ Record final IREF current measured and value of IREF_TRIM.
3. ☐ Measure VREF_ADC trim to 0.9 V by editing config file.
4. ☐ Measure VDDA and VDDD, trim to 1.2 V by editing config file.
5. ☐ Record VDDA and VDDD after trimming and note relevant register value in config file.

Scanning and tuning

1. ☐ Set up NTC to record temperature. Keep a temperature log running throughout entire scan procedure.
2. ☐ Record the current drawn on power-up, after configuration, and during some scans (if very different to others).
3. ☐ Run the three pre-tuning scans and save their output.
4. ☐ Run the threshold and ToT tuning for all three FE's individually. Save their output.
5. ☐ Run the three post-tuning scans and save their output.
6. ☐ (YARR only) Run the post-processing threshold and noise distribution ROOT scripts. Save their output.

