Matthew Lim

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EDUCATION

• Georgia Institute of Technology

Candidate for BS in Computer Engineering: Minor in Sustainable Cities: GPA: 3.89

Atlanta, GA

Expected May 2020

• Universidad Carlos III de Madrid

Exchange Program

Madrid, Spain

Sep. 2018 – Dec. 2018

SKILLS

• Languages: C++, JavaScript, C, Java, Python, HTML, CSS, MATLAB, Assembly

• Technologies: Git, React, Node.js, TypeScript, Jira, Sourcetree, OpenGL, OpenMPI, D3js, gtest, Chai, Mocha, Puppeteer, PCB Artist, Adobe After Effects, Illustrator, Photoshop, Premiere Pro

PROFESSIONAL EXPERIENCE

• Design Bloc/University Innovation Fellowship

Atlanta, GA

Teaching Assistant

Aug 2017 - Present

- Plan and manage workshops, events, and media marketing for students, entrepreneurship programs, and companies. Currently working to renovate a campus food court into a premiere innovation space in Georgia Tech's west campus.
- Sponsored by Georgia Tech's College of Design to train at Stanford's Design School. Training included rapid ideation, rapid prototyping, empathy research, interviewing, and rapid iteration.

• Siemens Inc.

Norcross, GA

Software Engineering Intern

May 2019 - July 2019

- Developed mass installation (Clonezilla) procedure and automated test (Chai) procedure for unreleased Siemens IoT meter data collection unit (Industrial Edge).
- Designed and built a metering application (React and Node.js) for unreleased heat monitoring system on Siemens IoT analytics platform (MindSphere).

• Autodesk Inc.

San Francisco, CA

Software Development Intern

 $July\ 2018\ -\ Dec\ 2018$

- Updated and added functionality to Autodesk's Screencast web player, a responsive Javascript application built on an ASP.net framework with over 375,000 monthly users on the Autodesk Knowledge Network. Presented major design overhaul to senior leadership.
- Realized fellow Autodesk UX intern project prototype to map and visualize consumer support stories in a comprehensive supportability web application using D3js and Bootstrap.

• Georgia Tech Research Institute - Electronic Systems Laboratory

Atlanta, GA

Research Intern

July 2017 - Aug 2017

 Designed, manufactured, tested, and wrote software for a PCB prototype for a heat-illness monitoring system. Programmed an embedded FPGA in VHDL and an embedded microcontroller in C as well as circuit analysis and oscilloscope usage.

ACTIVITIES & RESEARCH

- Director of Strategy @ The Hive: Exec Member and Peer Instructor at the largest state-of-the-art electronics and computer engineering design makerspace in the nation. Manage a volunteer workforce of 90+ members including running town halls, social events, and workshops. Direct future initiatives to align to mission statement.
- Research: Created Website for Friendly Cities Research Lab under Dr. Clio Andris, Georgia Tech Ideas to Prototype Graduate (2018), Capstone for University Innovation Fellowship (UIF), Sustainable Undergraduate Research Fellow (SURF).