The Introduction of Porting OpenJDK to RV32G

Overview

- PLCT Lab is porting the OpenJDK 11 to RV32G according BishengJDK. We began this project in January 2021.

- Project ULR is: https://github.com/openjdk-riscv/jdk11u .

- PLCT Lab wrote some docs about the OpenJDK 11:

  https://github.com/openjdk-riscv/jdk11u/wiki

  https://www.zhihu.com/column/c_1287750038518161408

- There are three full time staff in our OpenJDK team now, and Shiningning is the team leader.

Roadmap

- Add RISC-V 32Bit support to Zero. **(Done)**

  https://github.com/openjdk-riscv/jdk11u/pull/12

- Hotspot: Assembler and MacroAssembler porting. **(Done)**

- Hotspot: Interpreter porting. **(WIP)**

- Hotspot: C2 and C1 porting. **(TODO)**

Current Work

- Debug java –version:

  --with-jvm-variants=core

  --with-debug-level=slowdebug

- Modify andr, xorr, orr and other instructions back to basic

  instructions.