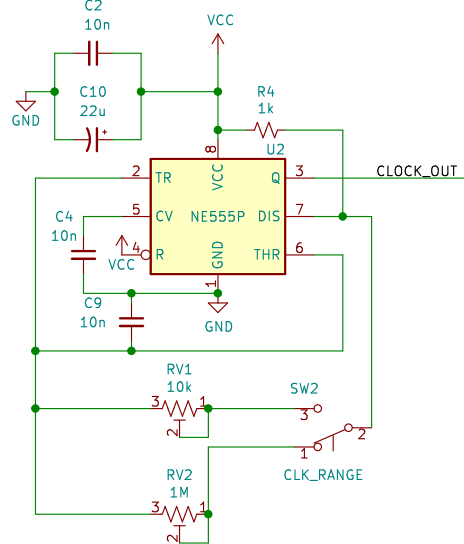
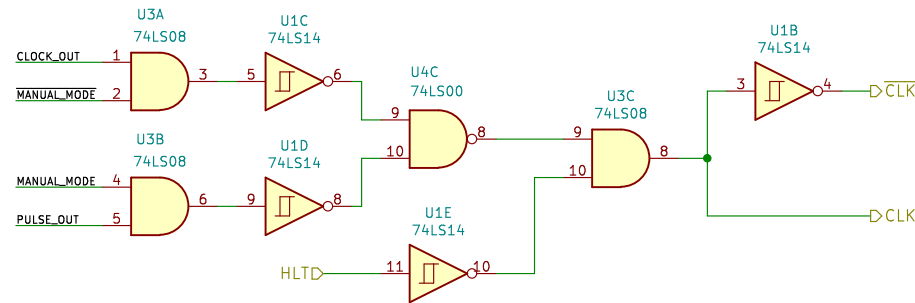


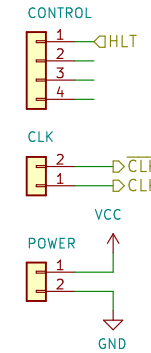
## Clock Generation



## Control Logic

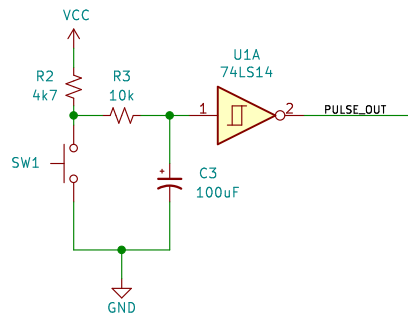


## Offboard Connections

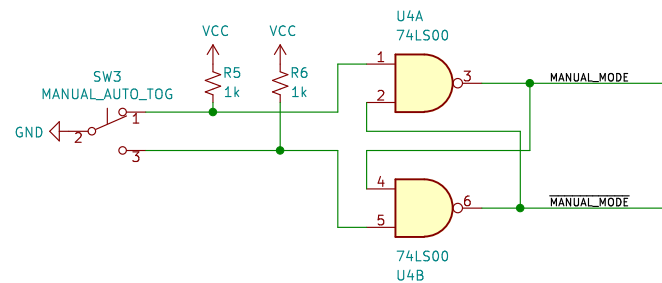


## Manual Pulse Button

( 10ms Time Constant for Debounce )

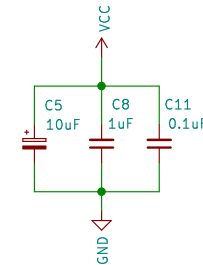


## Manual/Auto Toggle

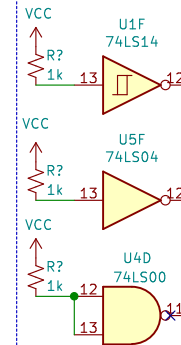


## Bypass Network x 3

< 10mm from IC Vcc Pin



## Unused Gates



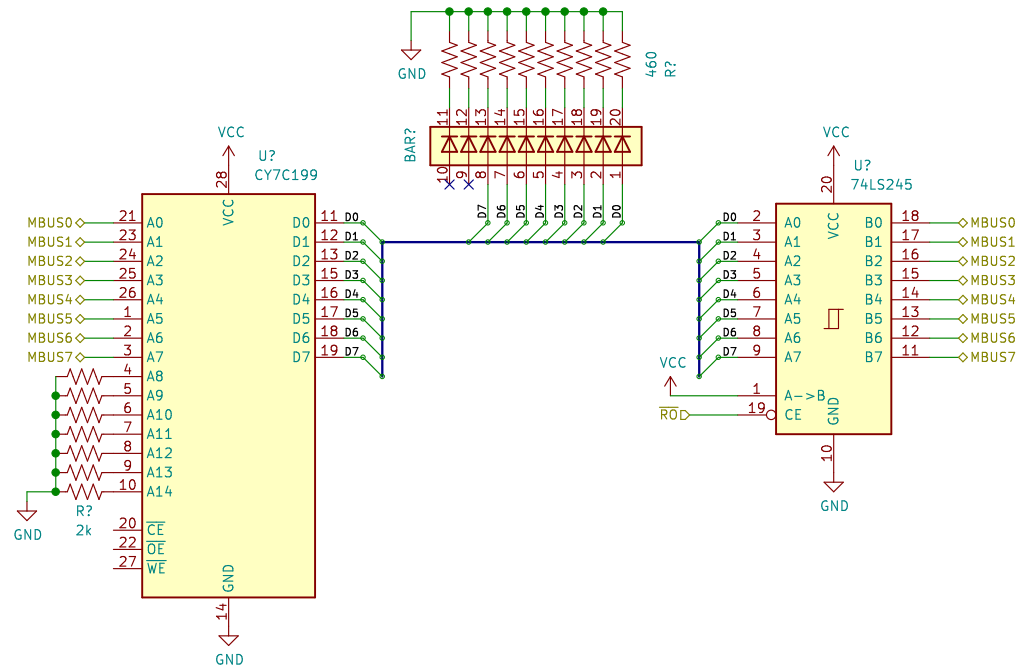
Class : EE120  
Professor : Tracey Vincent  
Author : Lina Adkins

Sheet: /System Clock/  
File: system\_clock.sch

## Title: VS80 Clock Module

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Id: 3/5



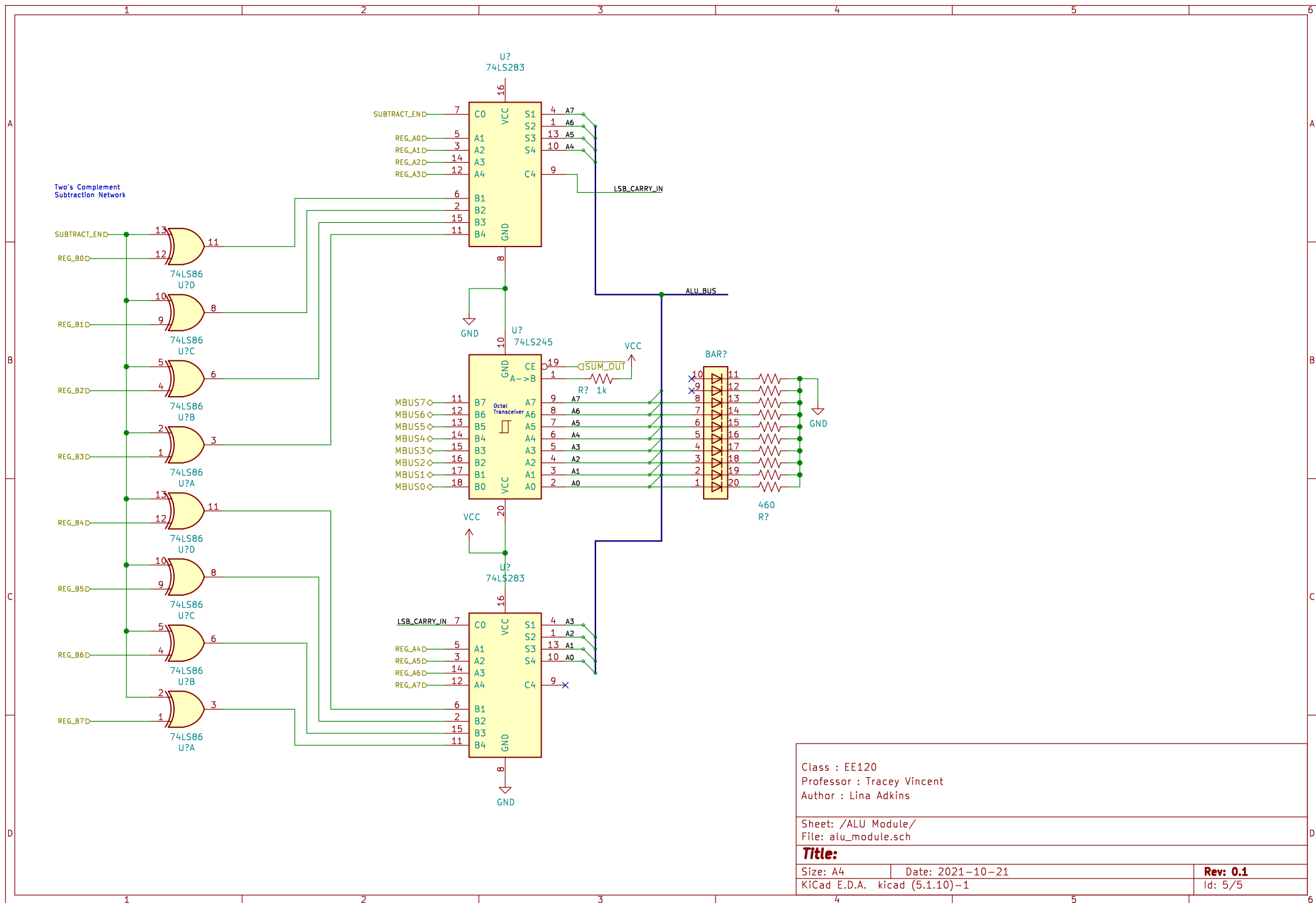
Class : EE120  
 Professor : Tracey Vincent  
 Author : Lina Adkins

Sheet: /Ram Module/  
 File: ram\_module.sch

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Class : EE120  
 Professor : Tracey Vincent  
 Author : Lina Adkins

Sheet: /ALU Module/  
 File: alu\_module.sch

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