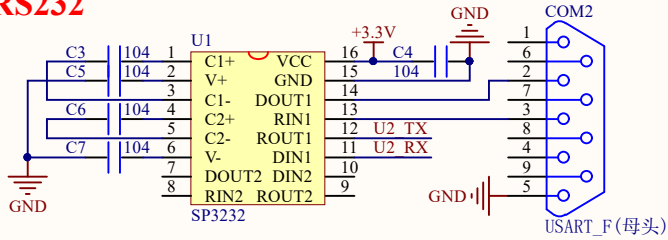
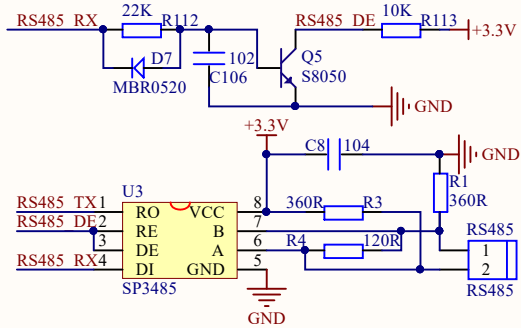


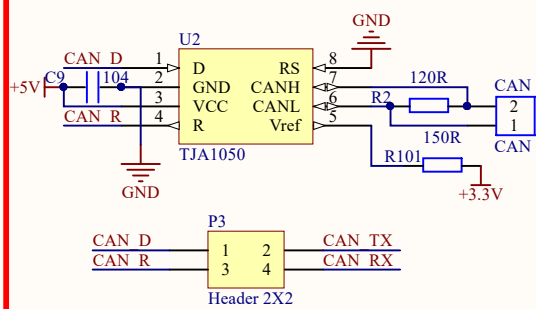
RS232



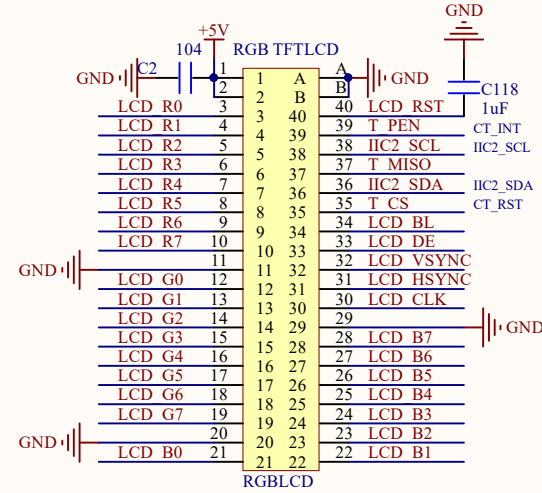
RS485



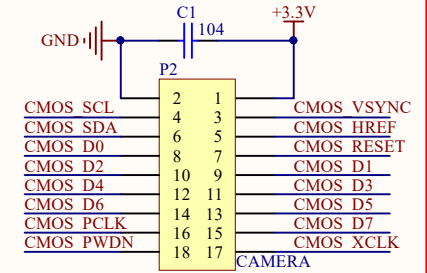
CAN



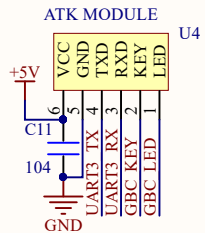
RGB LCD



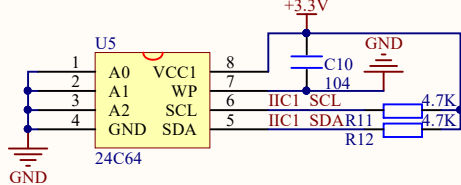
OLED&CAMERA



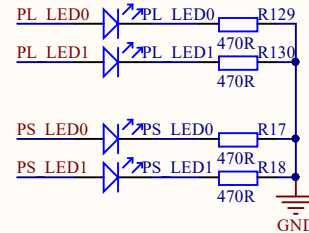
ATK MODULE



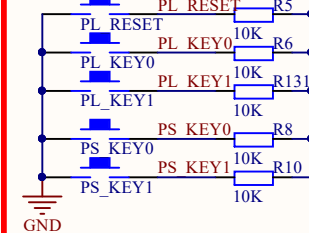
EEPROM



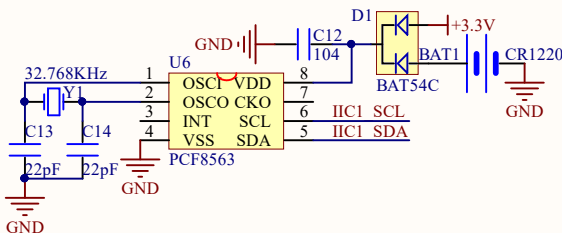
LED



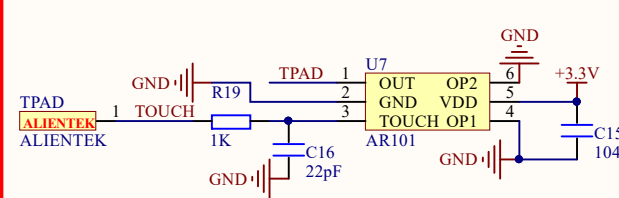
KEY



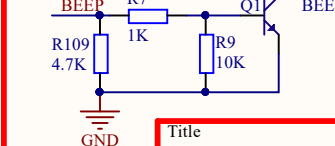
RTC



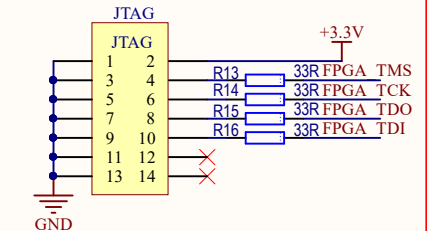
TOUCH KEY



BEEP

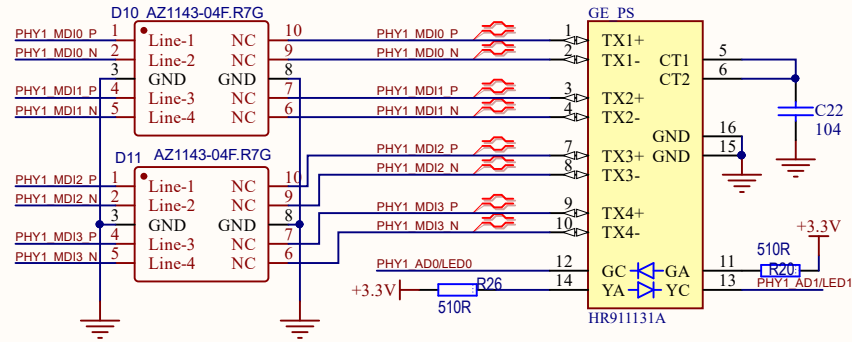


JTAG

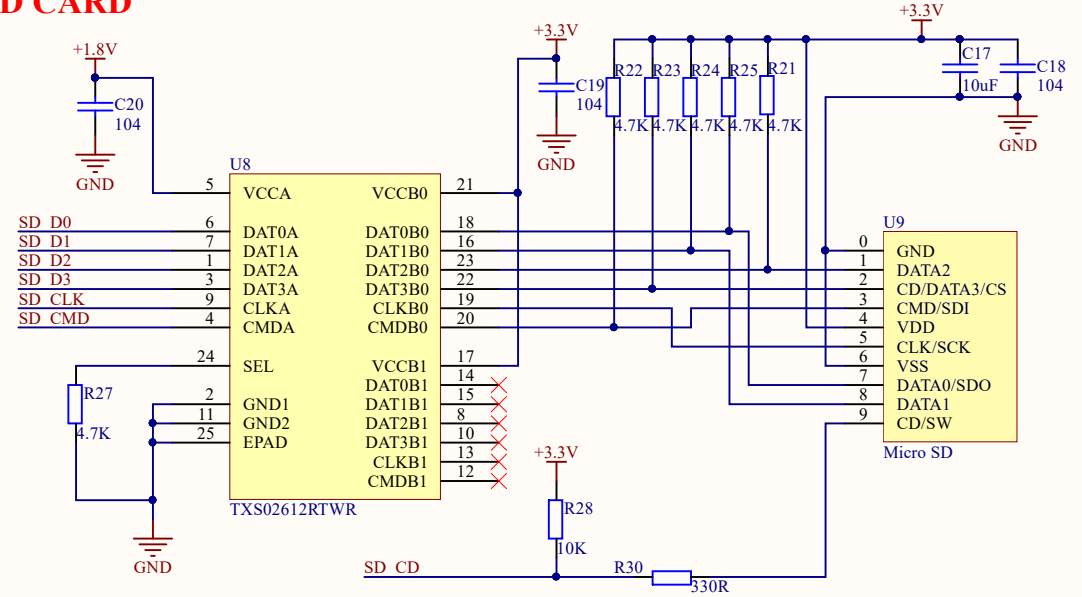


Title		
Size	Number	Revision
A4		
Date:	2023/5/25	Sheet of
File:	E:\ZYNU\DEVICE.SchDoc	Drawn By:

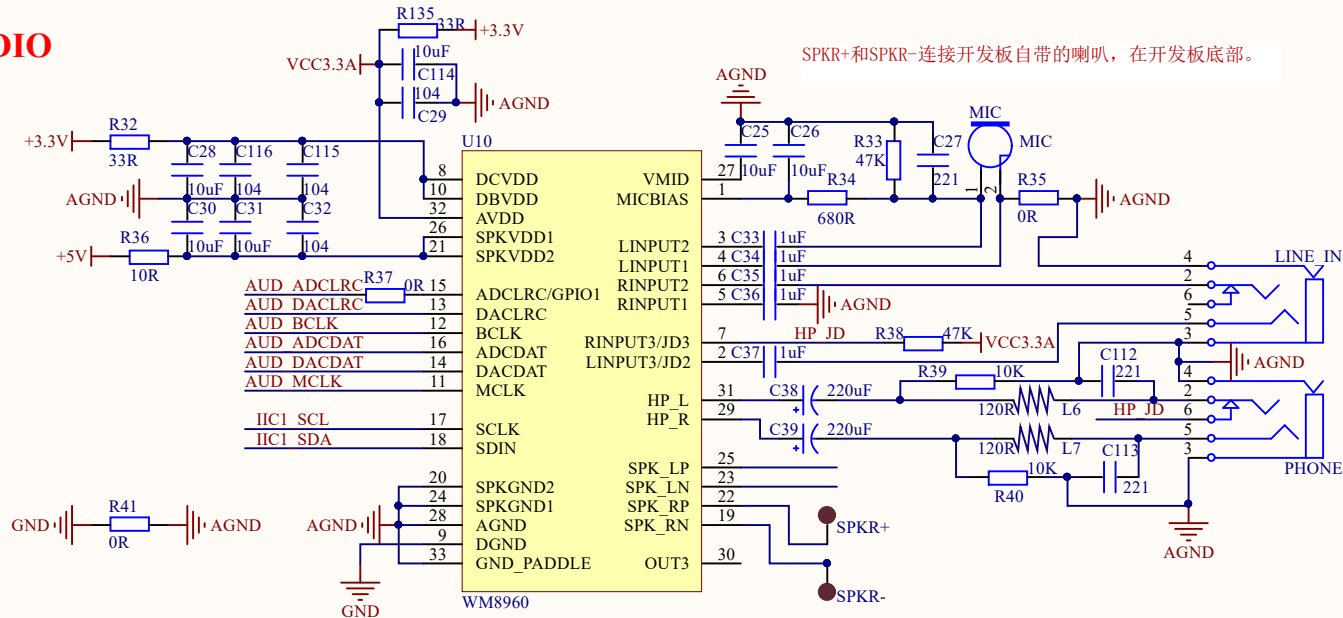
ETHERNET(PS)



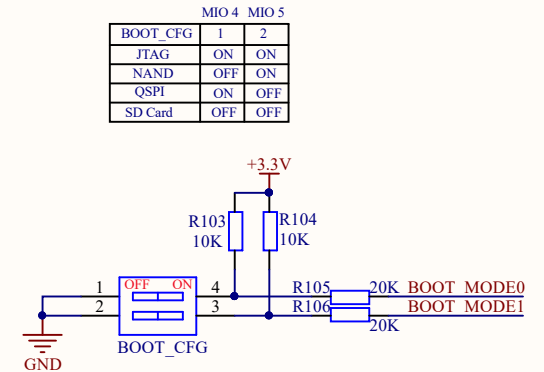
SD CARD



AUDIO



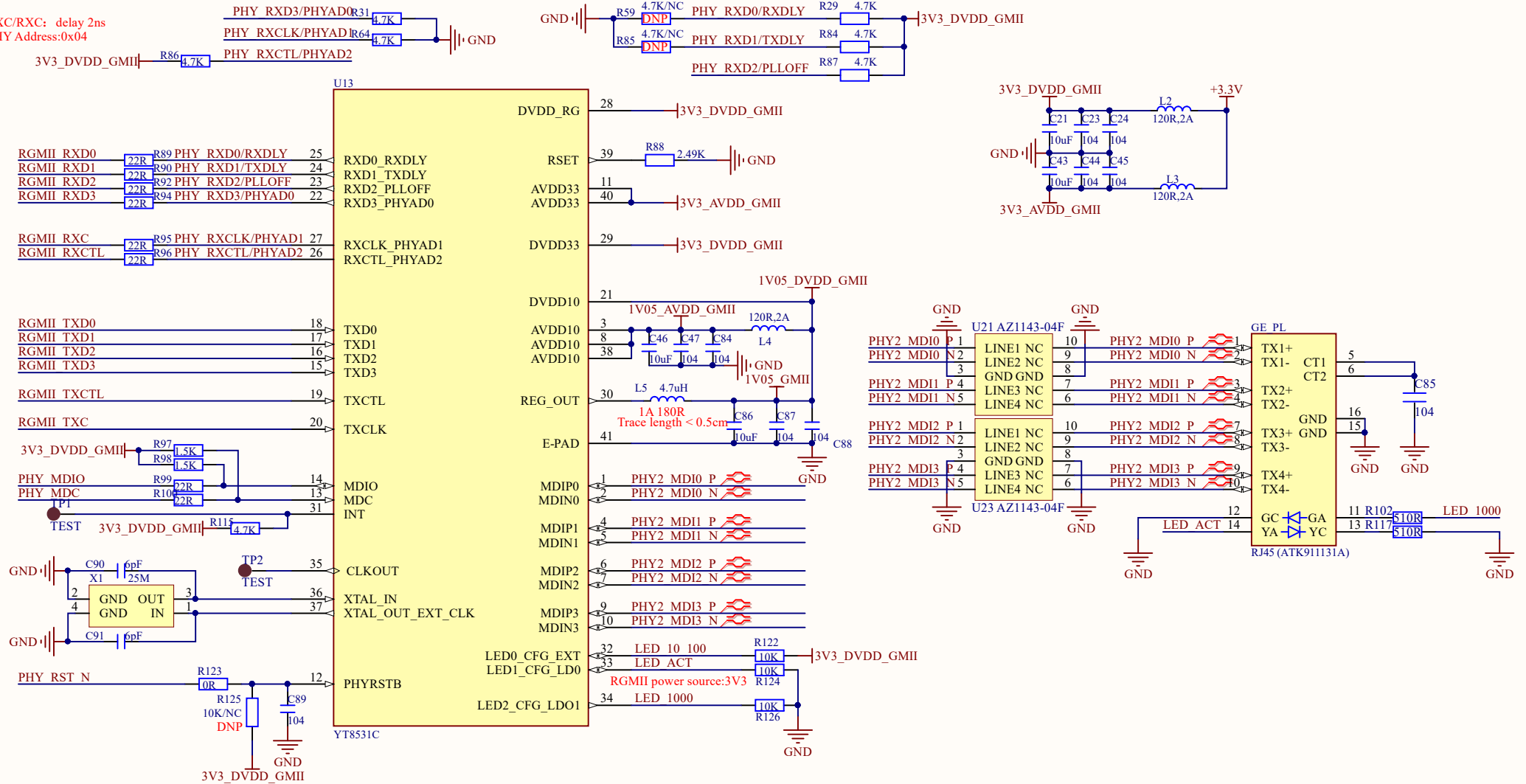
BOOT OPTION



Title

Size	Number	Revision
A4		
Date:	2023/5/25	Sheet of
File:	E:\ZYNU\ETH&SD&AUDIO.SchDoc	Drawn By:

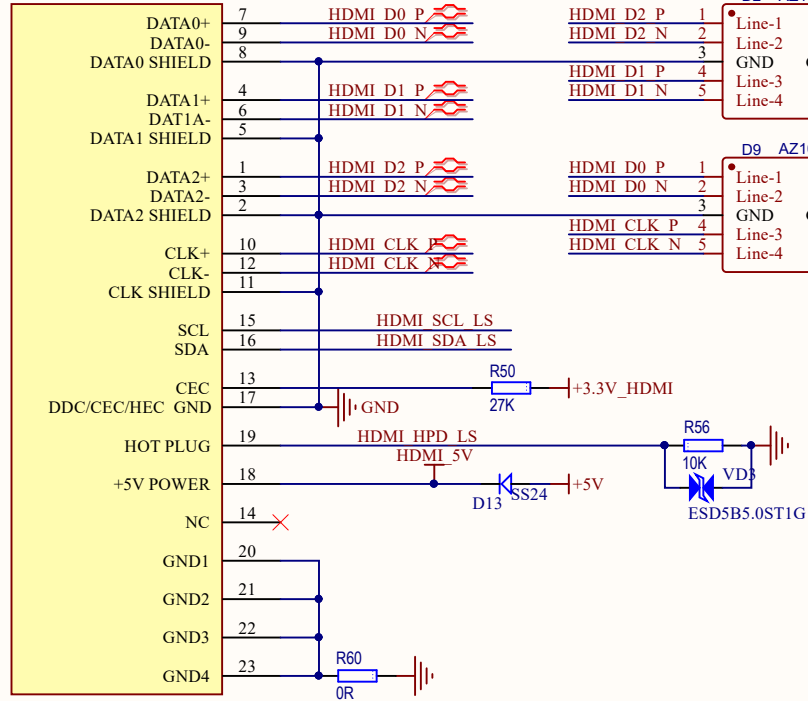
TXC/RXC: delay 2ns
PHY Address:0x04



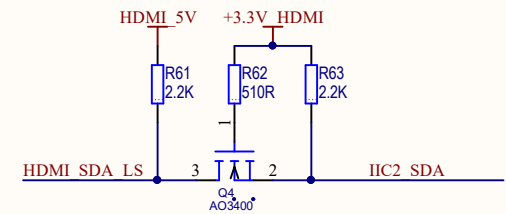
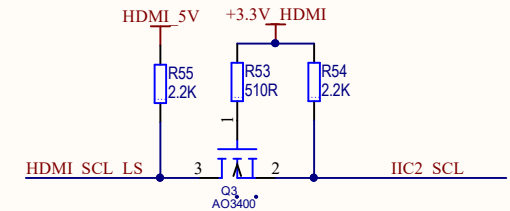
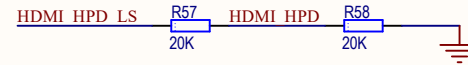
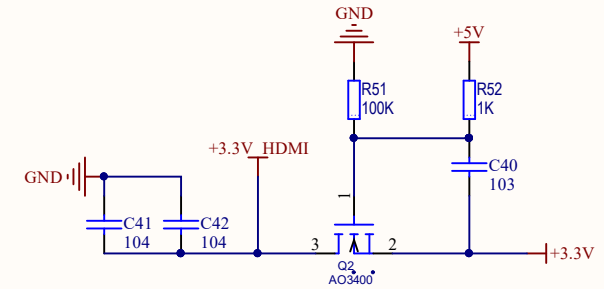
Title: ZYNQ.PrjPCB	Cannot open file
Author: ALIENTEK	G:\AD标准库\字体模板\AD通用模板标题栏\正点LOGO竖.png
Date: 2023/5/25	File: ETH.SchDoc
Revision:	Version: V1.6

HDMI

HDMI
HDMI_A

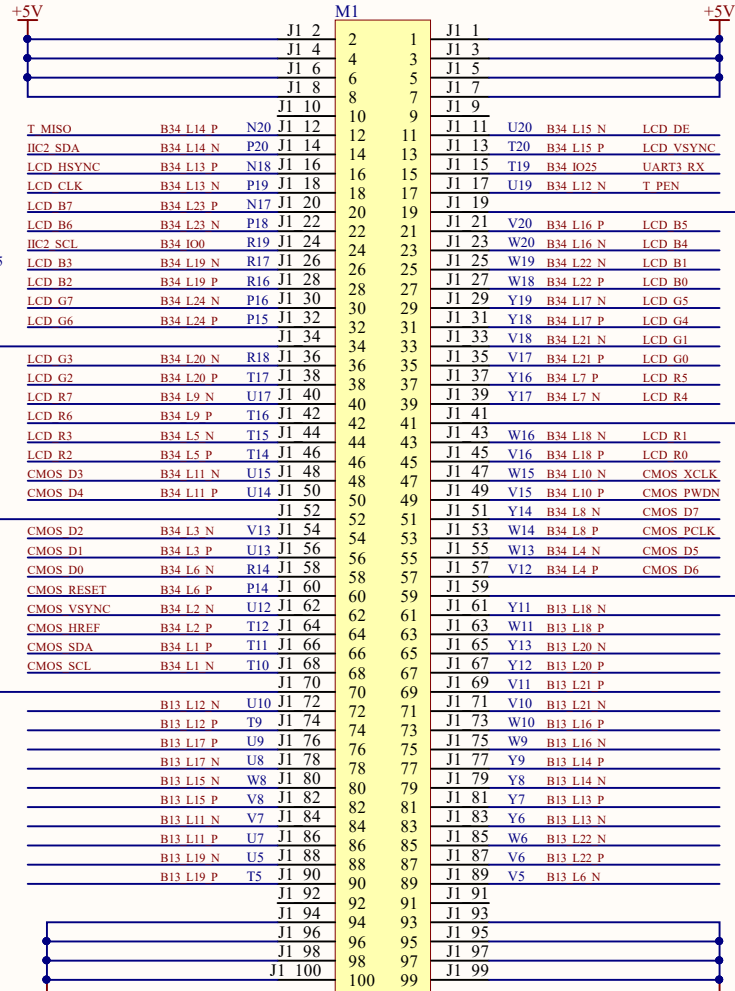


+3.3V HDMI

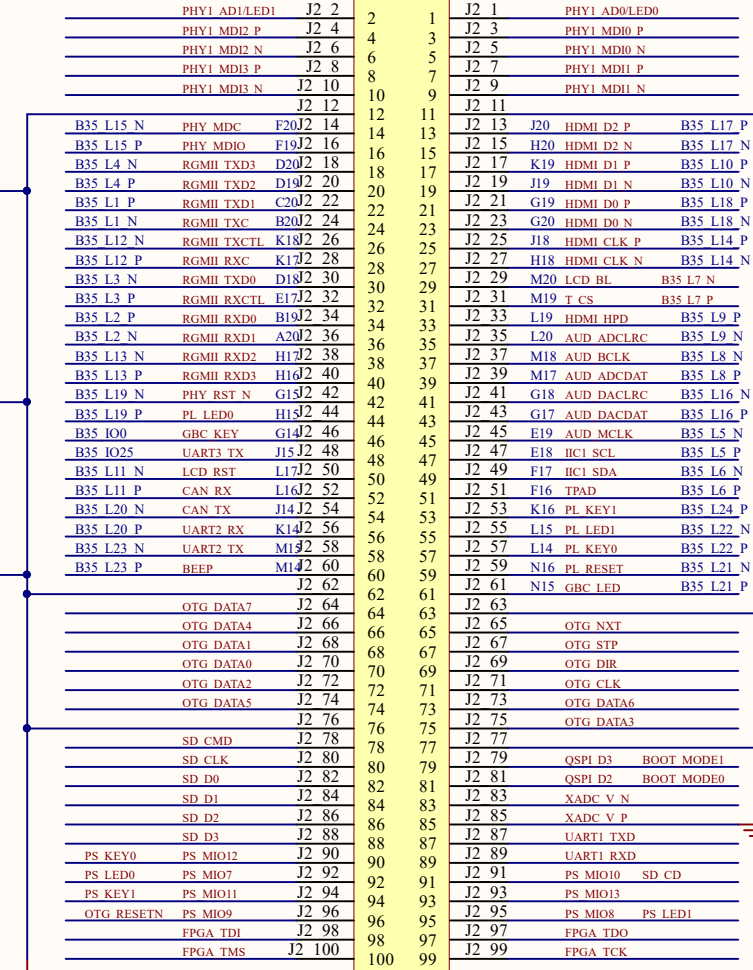


Title		
Size	Number	Revision
A4		
Date:	2023/5/25	Sheet of
File:	E:\ZY\Q\..HDMI.SchDoc	Drawn By:

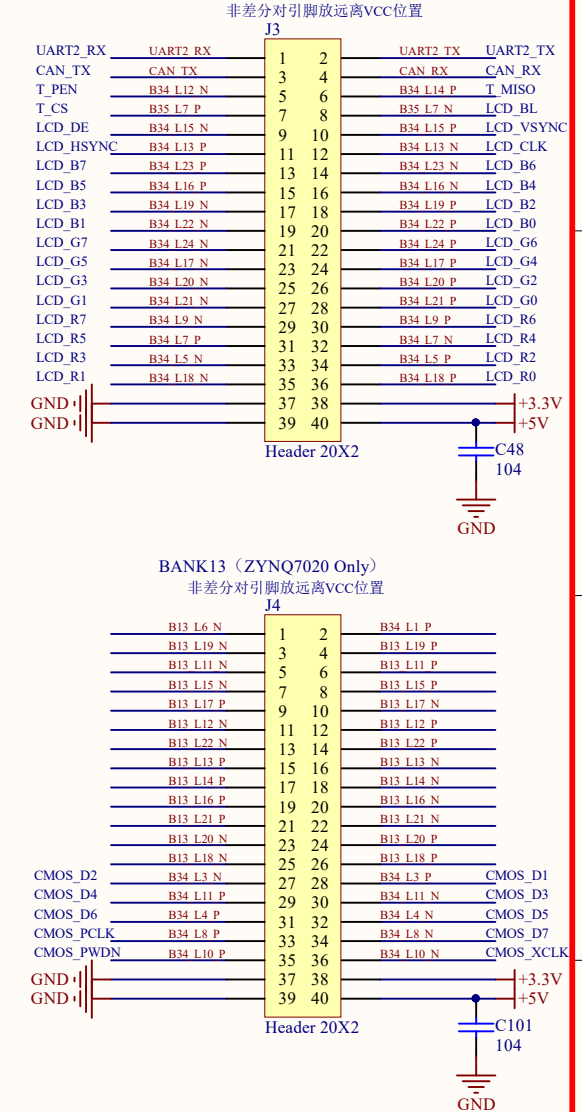
CORE_IO



ZYNQ7010&7020

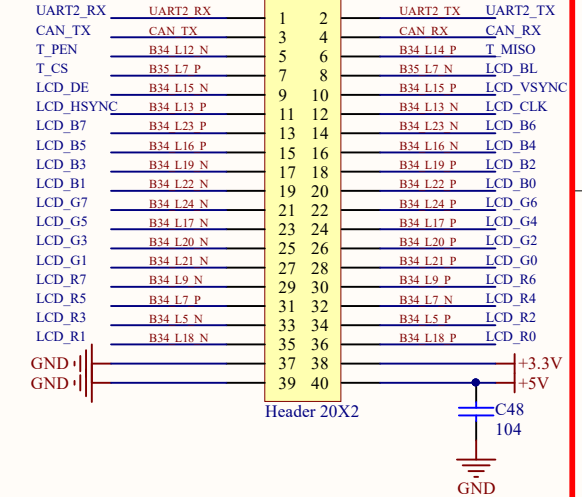


EXT_IO

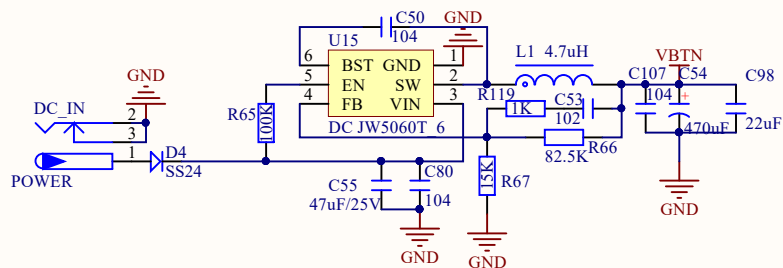


非差分对引脚放远离VCC位置

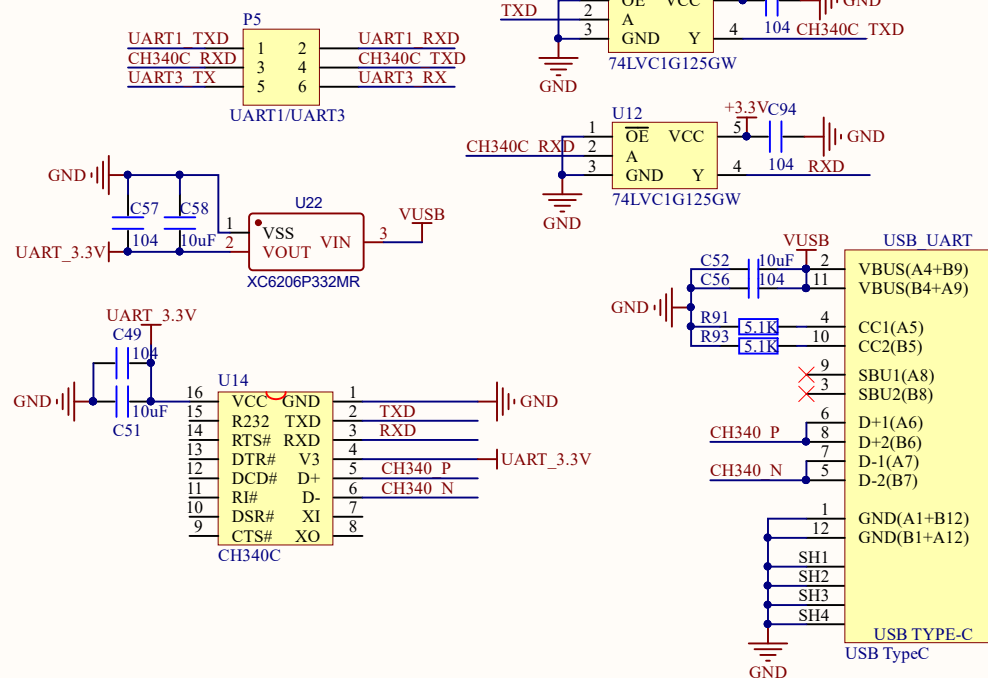
J3



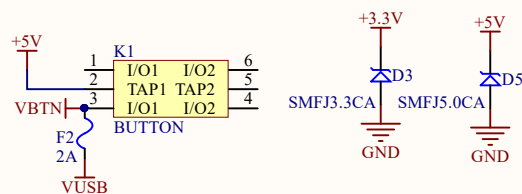
DC-DC POWER IN



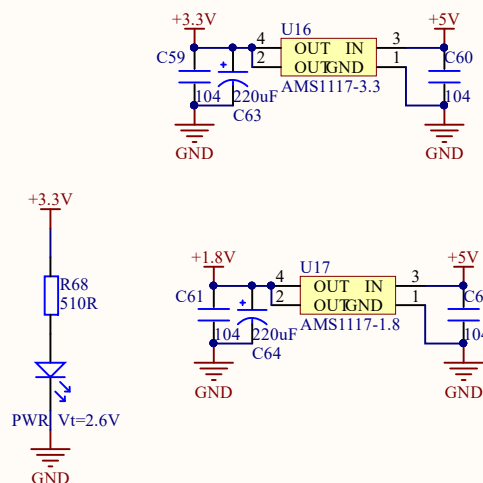
USB UART&USB POWER



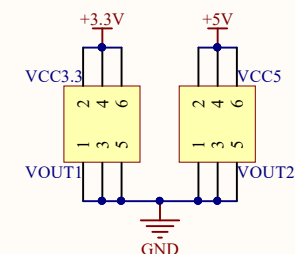
POWER SWITCH



1.8V/3.3V POWER

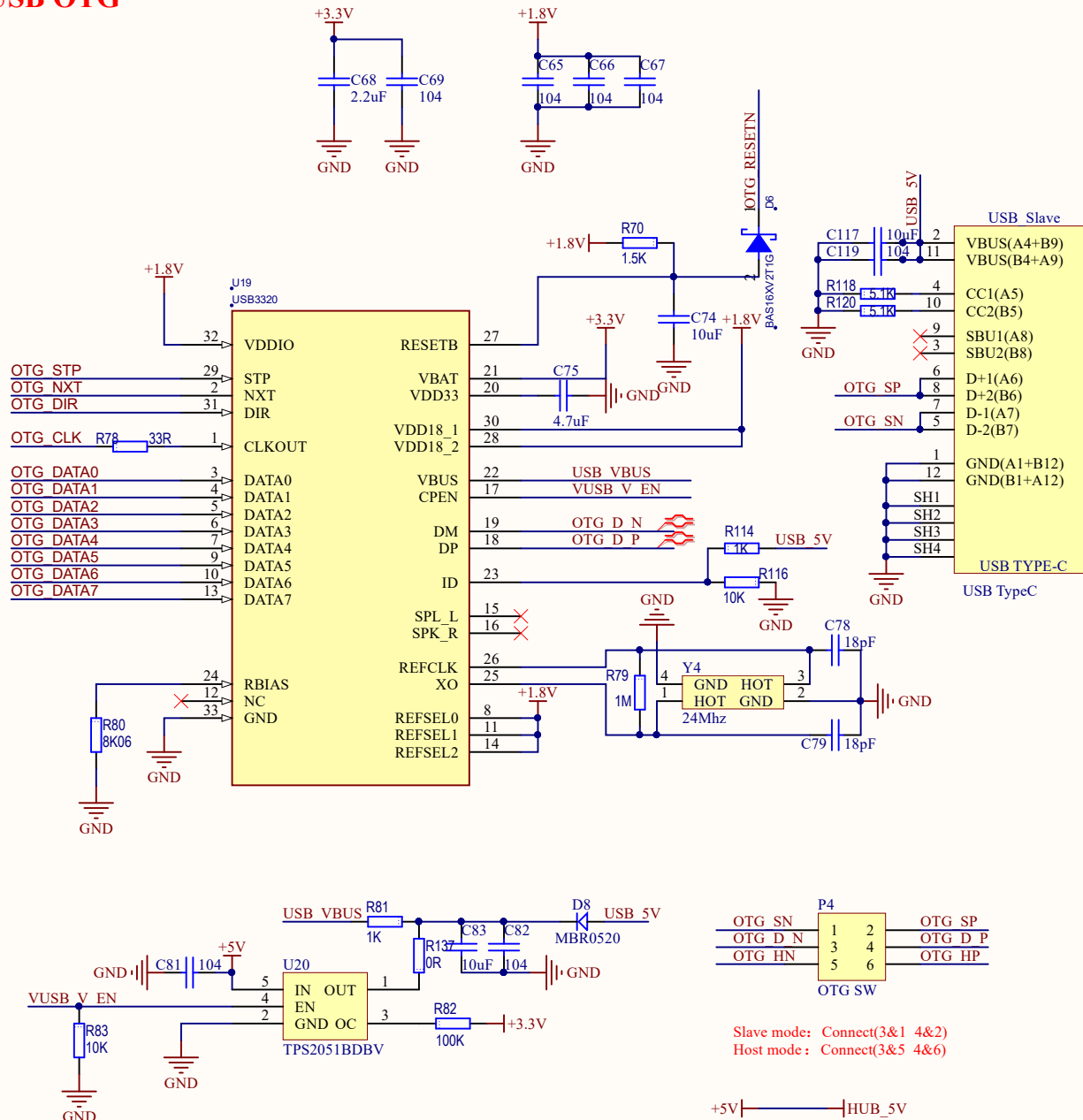


ON BOARD POWER SOURCE



Title		
Size	Number	Revision
A4		
Date:	2023/5/25	Sheet of
File:	E:\ZYNQ\POWER&UART.SchDoc	Drawn By:

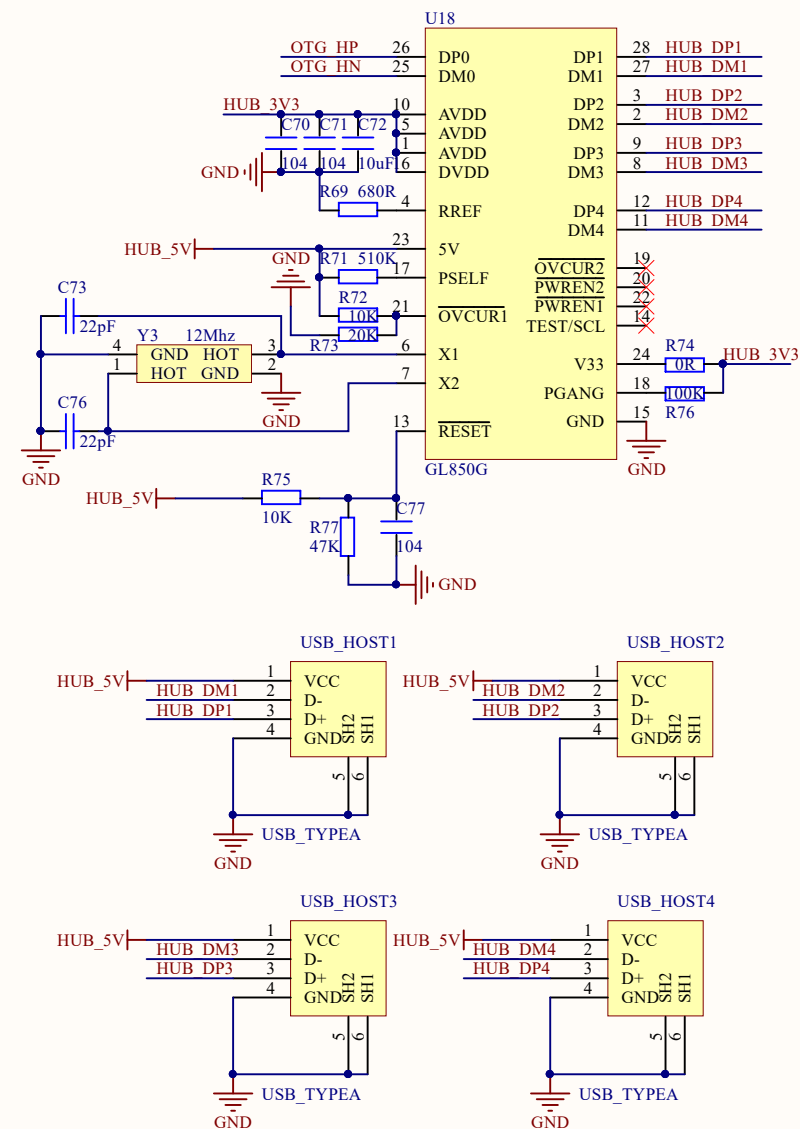
USB OTG



Slave mode: Connect(3&1 4&2)
Host mode: Connect(3&5 4&6)

+5V | HUB_5V

USB HUB



Title
USB OTG

Size	Number	V1.0	Revision
A4			
Date:	2023/5/25	Sheet	of
File:	E:\ZYNO\USB.SchDoc	Drawn By:	