

Demo Board For RV1106G

RV1106G_UVC_DEMO_V1.1

Reference Design Main Functions Introduction

Power	3 DCDC + 4 LDO (default)
RAM	SPI FLASH
Interface	MIPI_CSI/USB/AUDIO

Rockchip Confidential

 Rockchip	Rockchip Electronics Co., Ltd
Project:	RV1106G UVC DEMO
File:	00.Cover Page
Date:	Saturday, July 15, 2023
Designed by:	whb
Reviewed by:	Default
Rev:	V1.1
Sheet:	1 of 15

Index and Notes

Catalogue

Page	Description
Page01	00.Cover Page
Page02	01.Index and Notes
Page03	02.Revision History
Page04	03.Block Diagram
Page05	04.Power Diagram and Sequence
Page06	06.IO Power Domain Map
Page07	10.RV1106G Power/Codec/ETH/USB
Page08	11.RV1106G OSC/RTC/PMU/VCCIO
Page09	12.RV1106G ADC/FLASH/SDMMC
Page10	13.RV1106G SDIO/LCD/VICAP/MIPI
Page11	20.Power-System
Page12	25.USB Port
Page13	47.VI-Camera-SC530AI
Page14	70.Audio Port/ALARM
Page15	99.Mark/Hole
Page16	
Page17	
Page18	

Note

NOTE 1:

Component parameter description

1. DNP stands for component not mounted temporarily
2. If Value or option is DNP, which means the area is reserved without being mounted

NOTE 2:

Please use our recommended components to avoid too many changes.

For more informations about the second source,please refer to our AVL.

Generate Bill of Materials

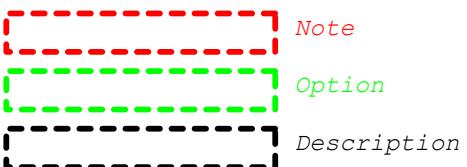
Header:

Item\tPart\tDescription\tPCB Footprint\tReference\tQuantity\tOption

Combined property string:

{Item}\t{Value}\t{Description}\t{PCB Footprint}\t{Reference}\t{Quantity}\t{Option}

Graphic Description



Rockchip Confidential

Rockchip	Rockchip Electronics Co., Ltd
Project:	RV1106G UVC DEMO
File:	01.Index and Notes
Date:	Saturday, July 15, 2023
Designed by:	whb
Reviewed by:	Default
Rev:	V1.1
Sheet:	2 of 15

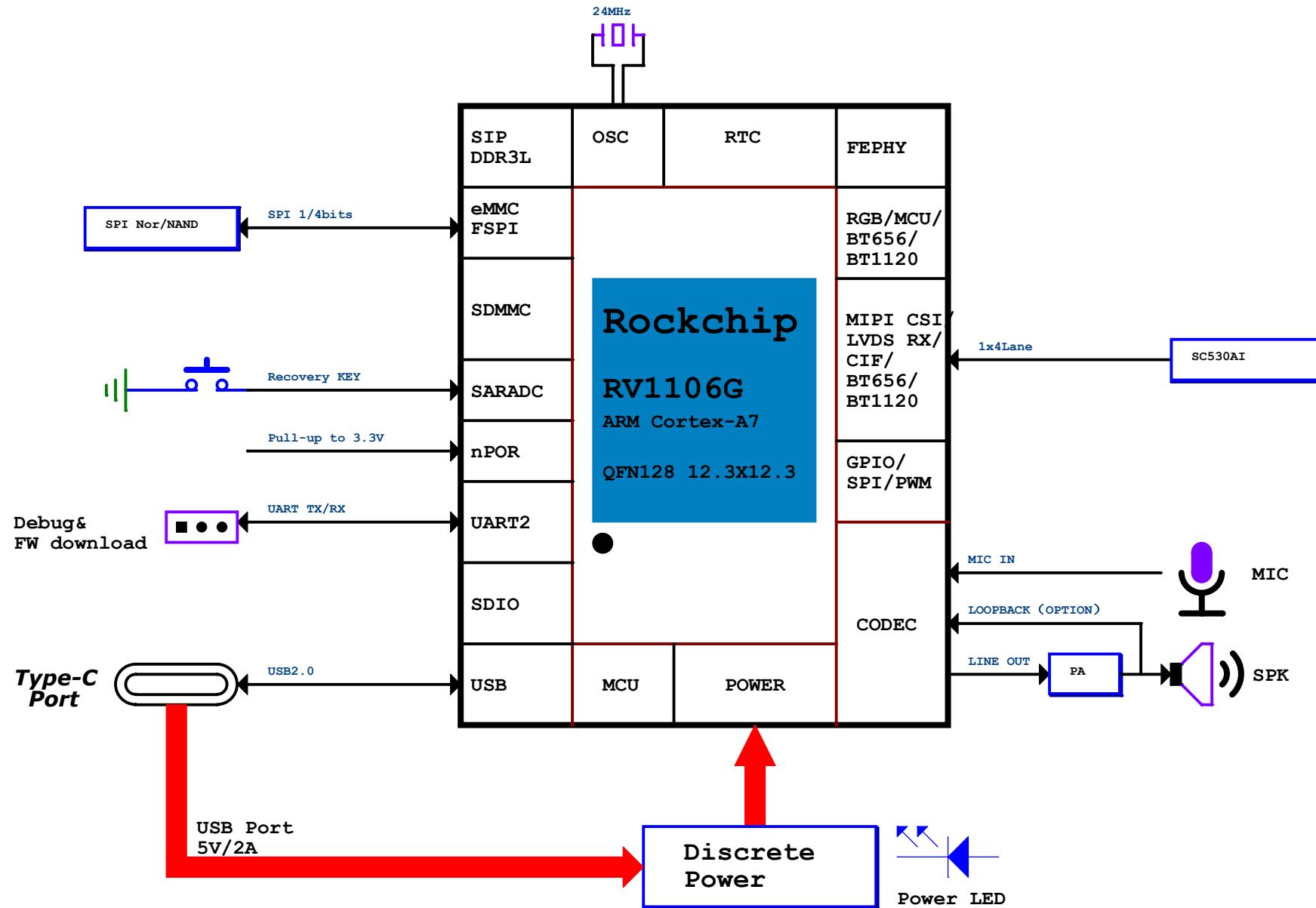
Revision History

Version	Date	Author	Change Note	Approved
V1.0	2022.05.09	whb	First edition.	
V1.1	2023.07.15	whb	1.Modify the RC parameters of the nPOR. 2.Modify the default voltage of VDD_0V9 to 0.88V 3.Modify the voltage regulator circuit parameters for VDD_ARM.	

Rockchip Confidential

 Rockchip	Rockchip Electronics Co., Ltd
Project:	RV1106G UVC DEMO
File:	02.Revision History
Date:	Saturday, July 15, 2023
Designed by:	whb
Reviewed by:	Default
Rev:	V1.1
Sheet:	3 of 15

Block Diagram



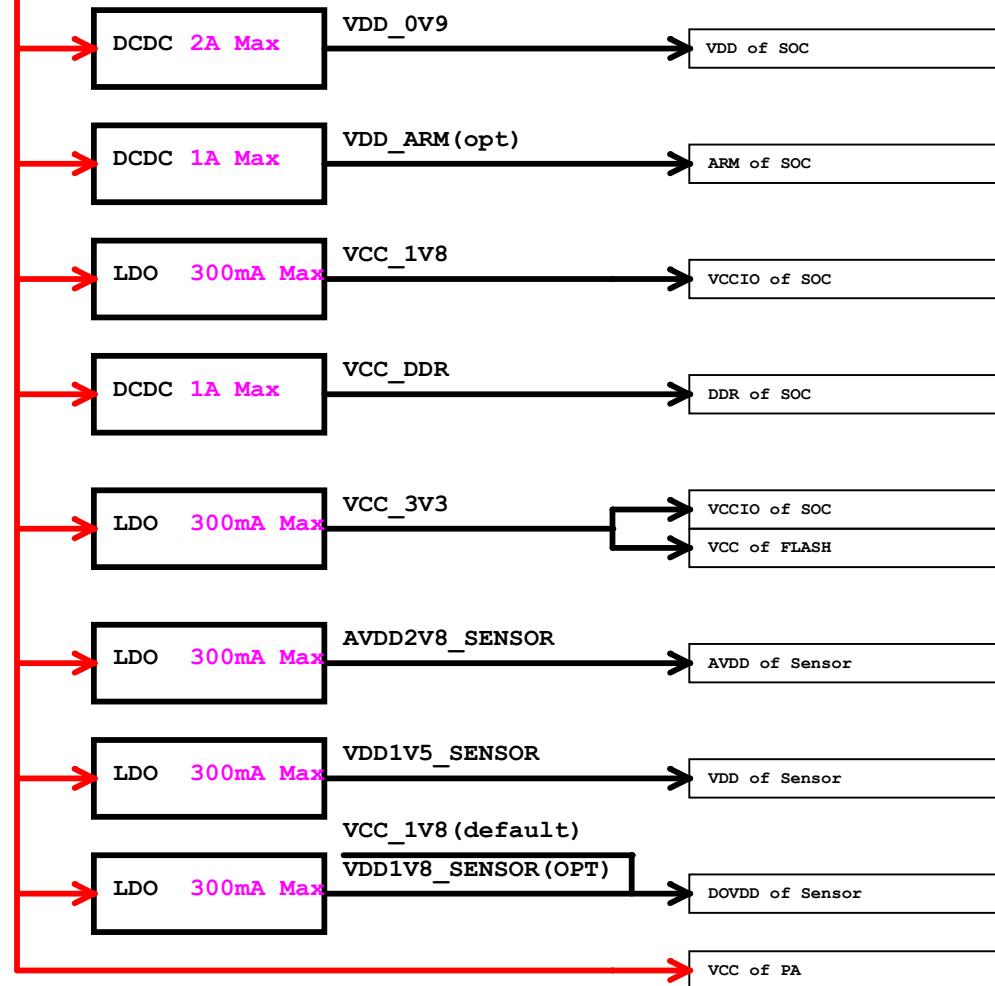
Rockchip Confidential

Rockchip		Rockchip Electronics Co., Ltd
Project:	RV1106G UVC DEMO	
File:	03.Block Diagram	
Date:	Saturday, July 15, 2023	Rev: V1.1
Designed by:	wrb	Reviewed by: Default
Sheet:	4	of 15

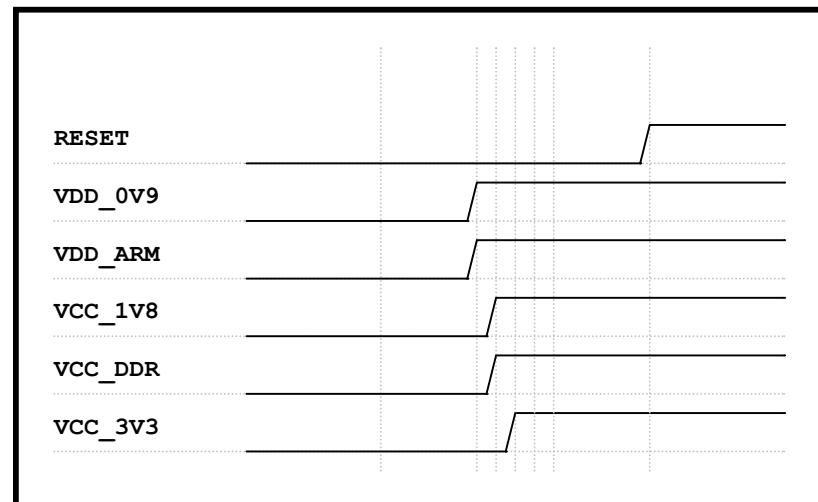
Power Diagram and Sequence

TYPE-C
5V/2A

VCC5V0_SYS



RV1106G Power-on Sequence				
Power Name	PMIC Channel	Time Slot	Default voltage	Peak Current
VDD_0V9	DC-DC BUCK	Slot: 1	0.93V	1.6A
VDD_ARM	DC-DC BUCK	Slot: 1	0.93V	0.5A
VCC_1V8	LDO	Slot: 2	1.8V	0.1A
VCC_DDR	DC-DC BUCK	Slot: 2	1.35V	0.5A
VCC_3V3	LDO	Slot: 3	3.3V	0.2A
RESET	Finally , nPOR RESET 10ms after VCC_3V3 is ready			



Rockchip Confidential

Rockchip	Rockchip Electronics Co., Ltd
Project:	RV1106G UVC DEMO
File:	04.Power Diagram and Sequence
Date:	Saturday, July 15, 2023
Designed by:	whb
Reviewed by:	Default
Sheet:	5 of 15

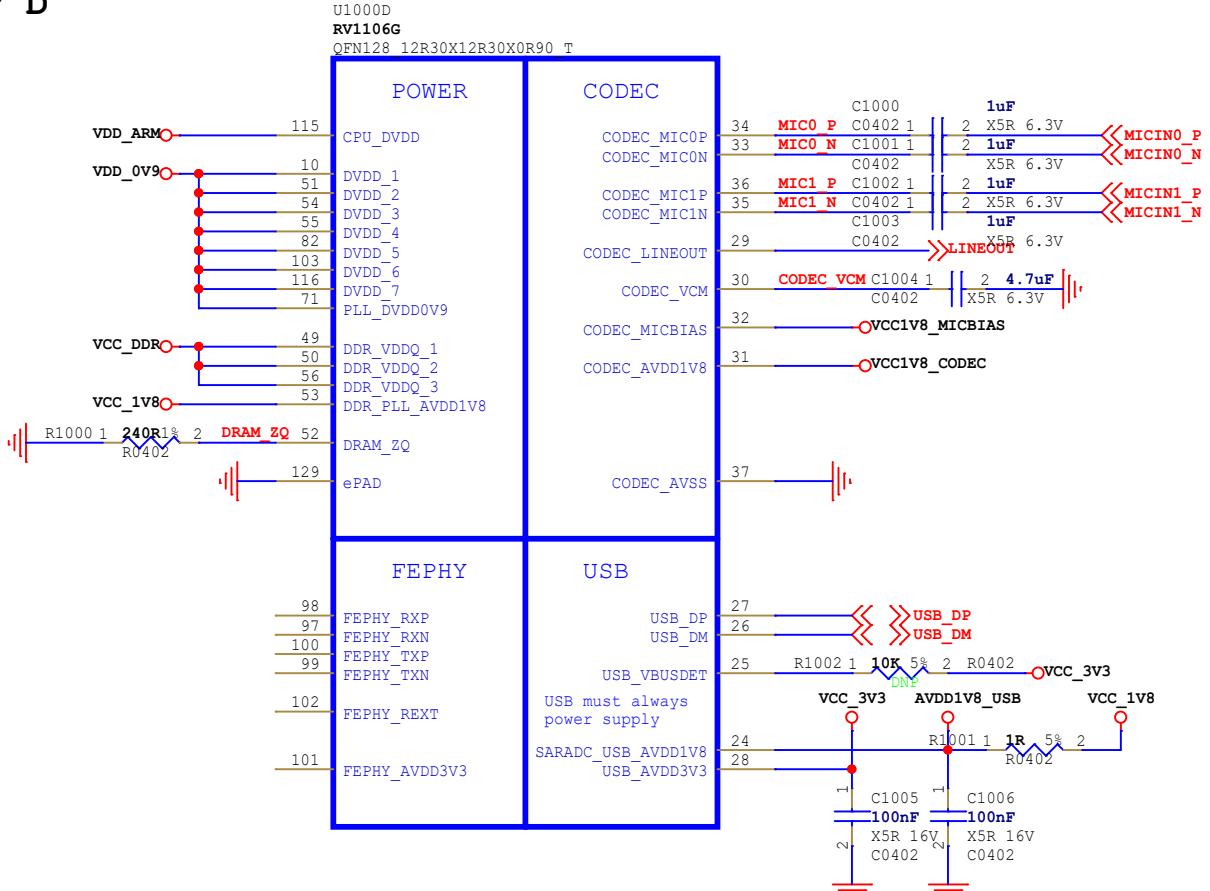
IO Power Domain Map

IO Domain	IO Group	Support of IO Voltage		Default Actual assigned IO Domain Voltage			Remark
		1.8V	3.3V	Net Name of Power Supply	Power Source	Voltage	
PMU	Group GPIO0_A		✓	VCC_3V3		3.3V	
VCCIO1	Group GPIO1_AB		✓	VCC_3V3		3.3V	
VCCIO2	Group GPIO4_C	✓		VCC_1V8		1.8V	
VCCIO3	Group GPIO4_AB	✓	✓	VCCIO_FLASH		1.8/3.3V	
VCCIO4	Group GPIO3_A	✓	✓	VCCIO_SD		1.8/3.3V	
VCCIO5	Group GPIO2_AB	✓	✓				
VCCIO6	Group GPIO1_CD	✓	✓				
VCCIO7	Group GPIO3_BCD	✓		VCC_1V8		1.8V	

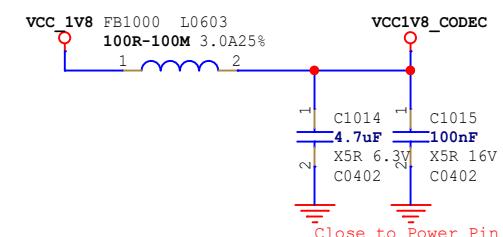
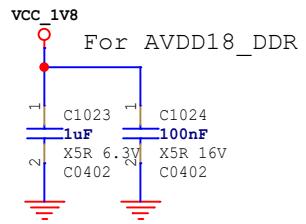
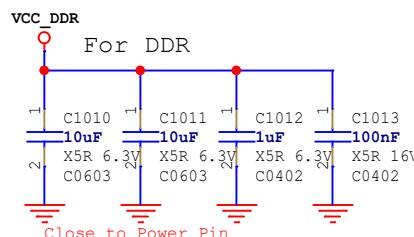
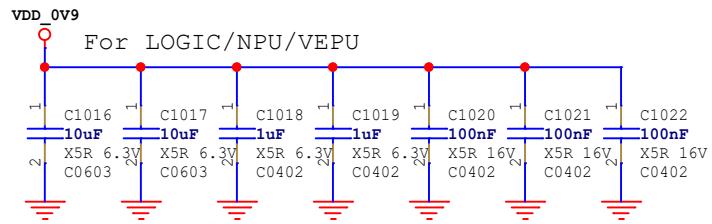
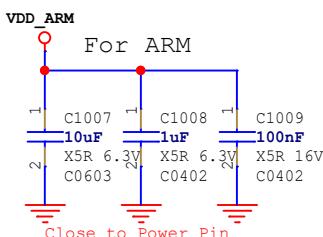
Rockchip Confidential

 Rockchip	Rockchip Electronics Co., Ltd
Project:	RV1106G UVC DEMO
File:	06.IO Power Domain Map
Date:	Saturday, July 15, 2023
Designed by:	whb
Reviewed by:	Default
Sheet:	6 of 15

PART D



POWER



Rockchip Confidential

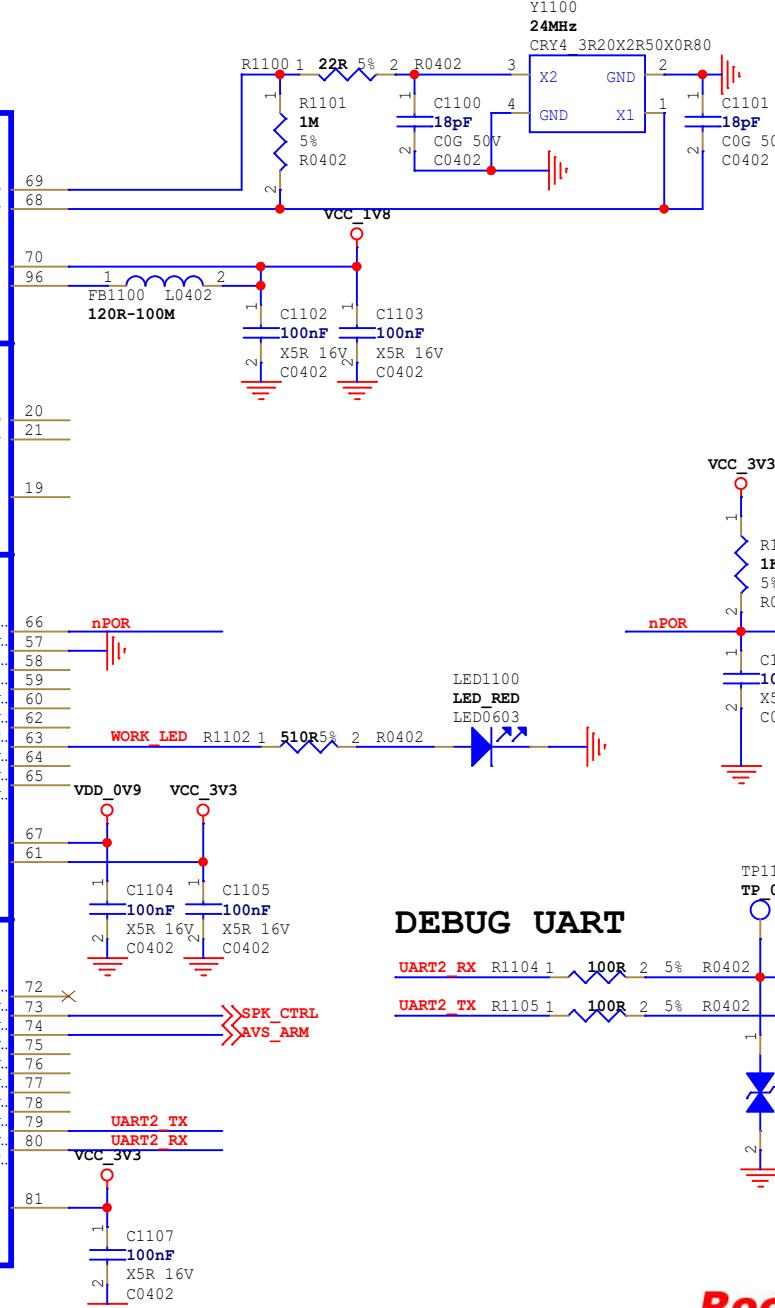
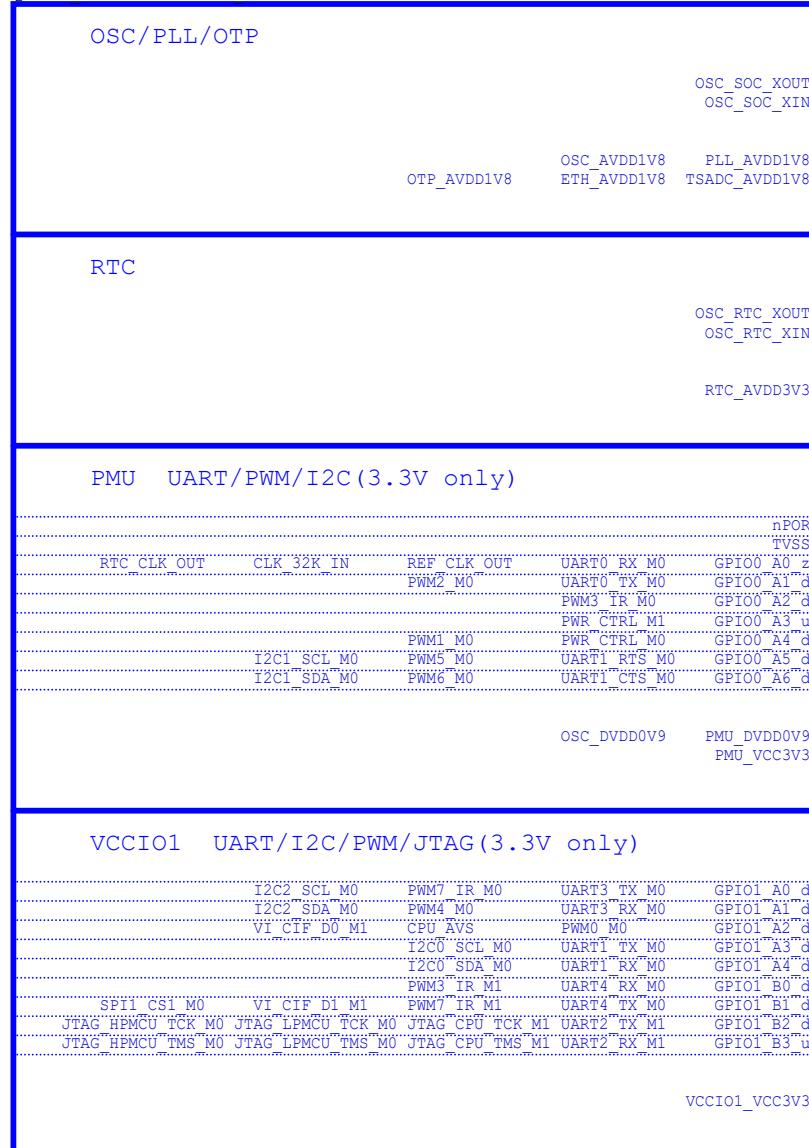


Rockchip Electronics Co., Ltd

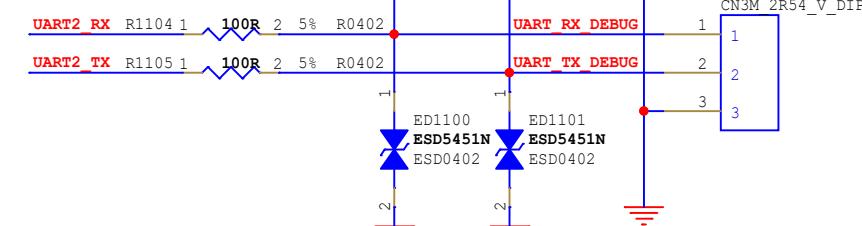
Project:	RV1106G UVC DEMO		
File:	10.RV1106G Power/Codec/ETH/USB		
Date:	Saturday, July 15, 2023	Rev:	V1.1
Designed by:	whb	Reviewed by:	Default
Sheet:	7	of	15

PART A

U1000A
RV1106G
QFN128 12R30X12R30X0R90 T



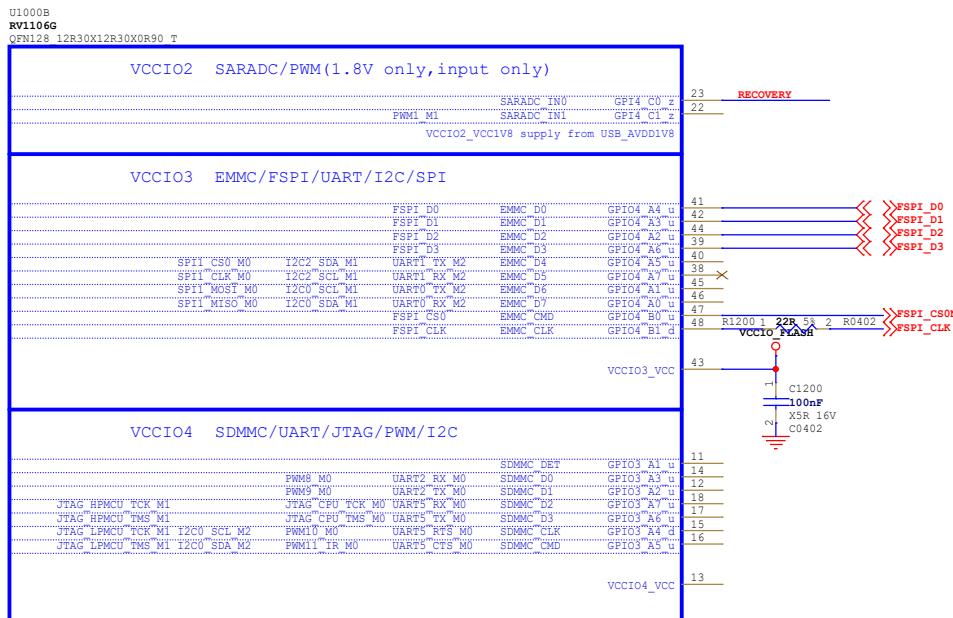
DEBUG UART



Rockchip Confidential

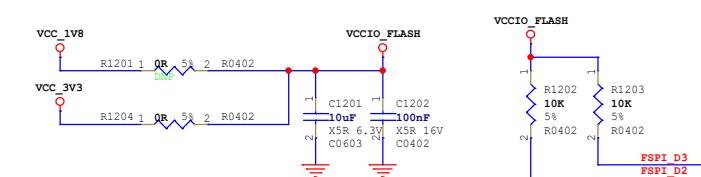
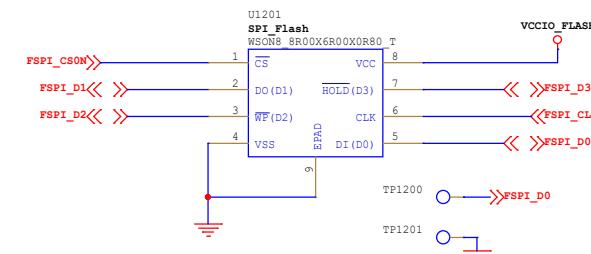
Rockchip	Rockchip Electronics Co., Ltd		
Project:	RV1106G UVC DEMO		
File:	11.RV1106G OSC/RTC/PMU/VCCIO		
Date:	Thursday, July 27, 2023	Rev:	V1.1
Designed by:	whb	Reviewed by:	Default
Sheet:	8	of	15

PART B

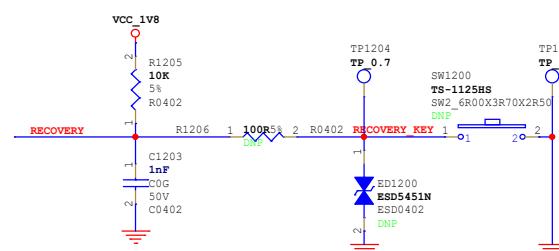


SPI Flash

NOTE:
Refer to the latest AVL for parts selection.



RECOVERY Key



Rockchip Confidential

Rockchip Rockchip Electronics Co., Ltd

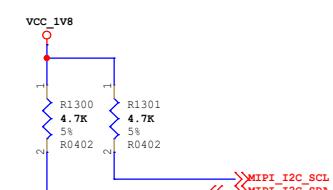
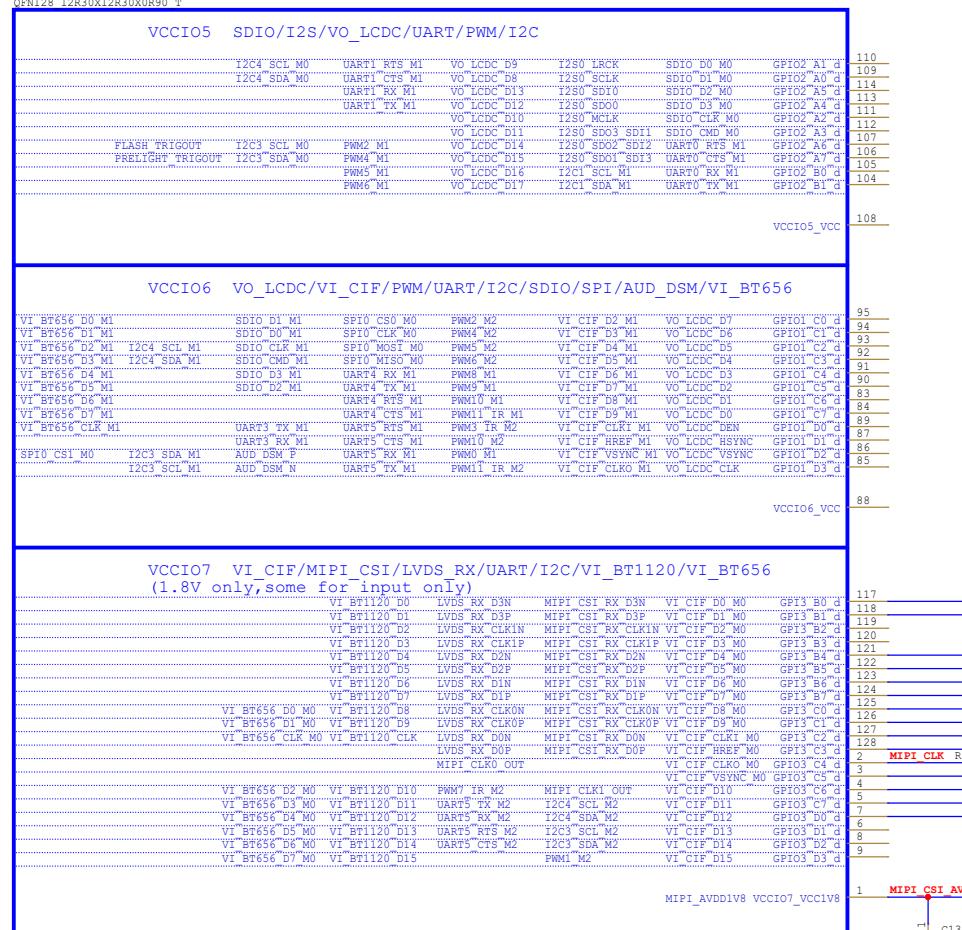
Project: RV1106G UVC DEMO

File: 12.RV1106G ADC/FLASH/SDMMC

Date: Thursday, July 27, 2023 Rev: V1.1

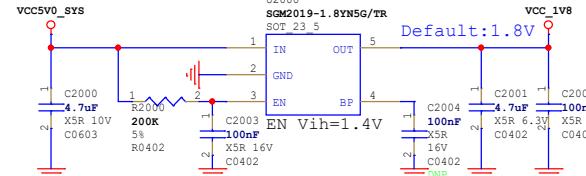
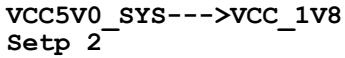
Designed by: whb Reviewed by: Default Sheet: 9 of 15

U1000C
RV1106G
QEN128 12R30X12R30X0R90

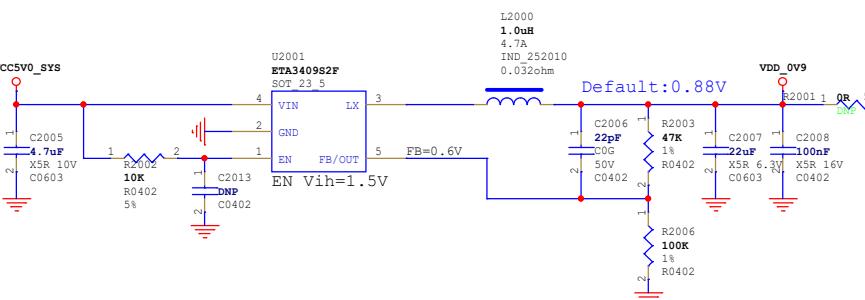


MIPI AVDD1V8 VCCIO7 VCC1V8 1 MIPI CSI AVDD 1V8 R1303 1 1R 5% 2 R0402 OVCC 1

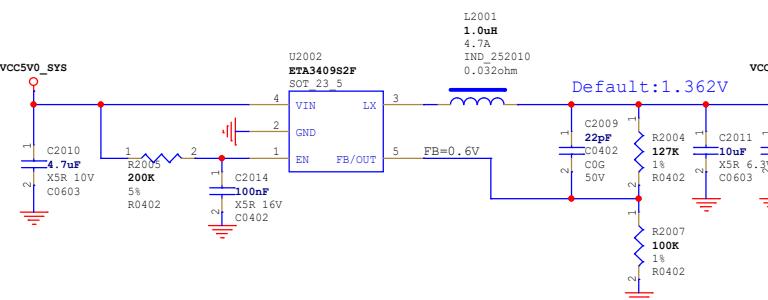
Project:	RV1106G UVC DEMO		
File:	13.RV1106G SDIO/LCD/VICAP/MIPI		
Date:	Saturday, July 15, 2023	Rev:	V1.1
Designed by:	wbh	Reviewed by:	Default
Sheet:	10 of 15		



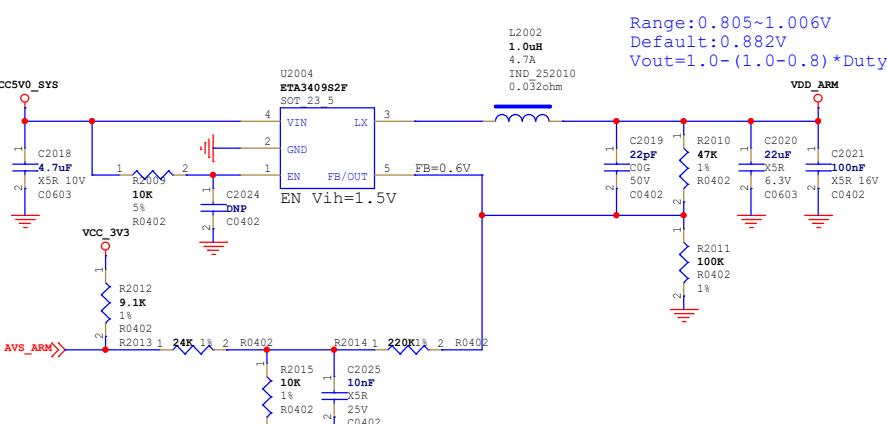
VCC5V0_SYS--->VDD_0V9
Setup 1



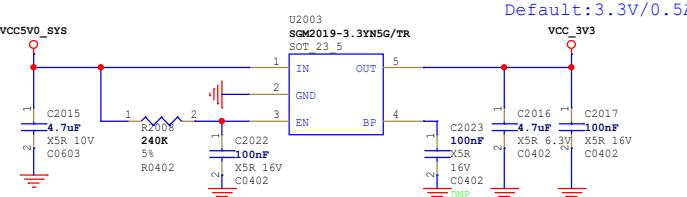
VCC5V0_SYS--->VCC_DDR
Setp 2



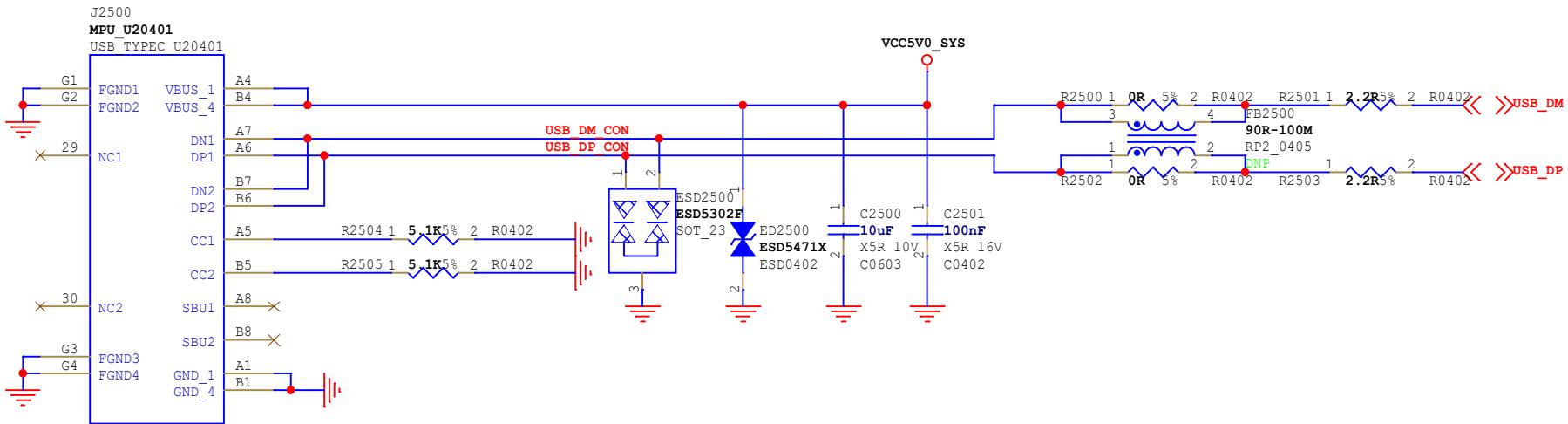
VCC5V0_SYS--->VDD_ARM
Setup 1



VCC5V0_SYS--->VCC_3V3
Setup 3



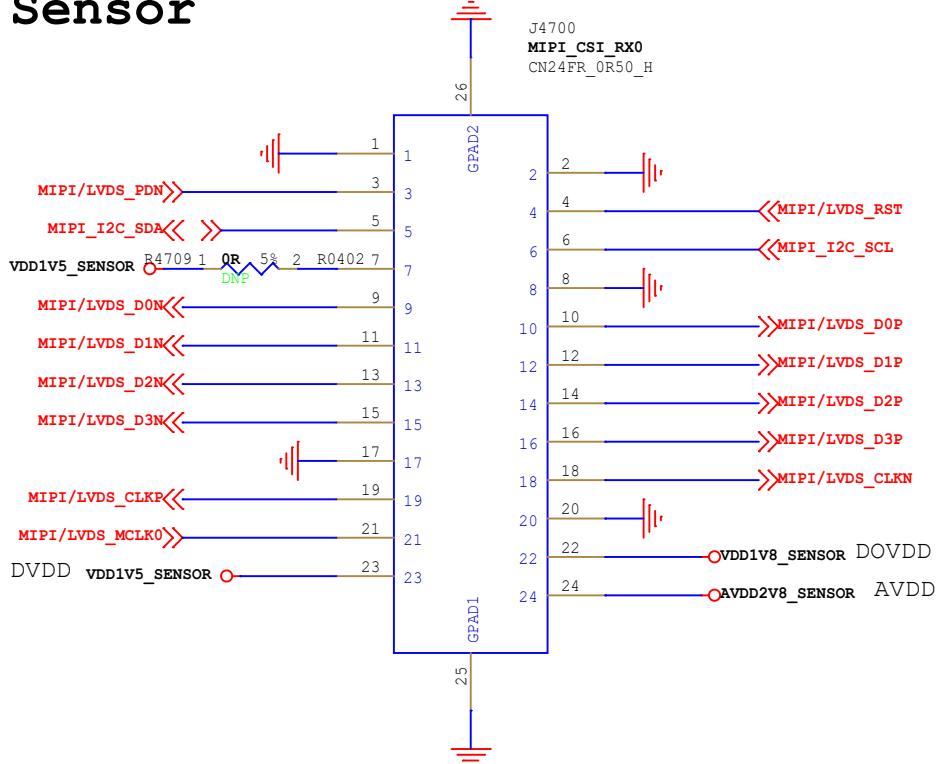
USB Port



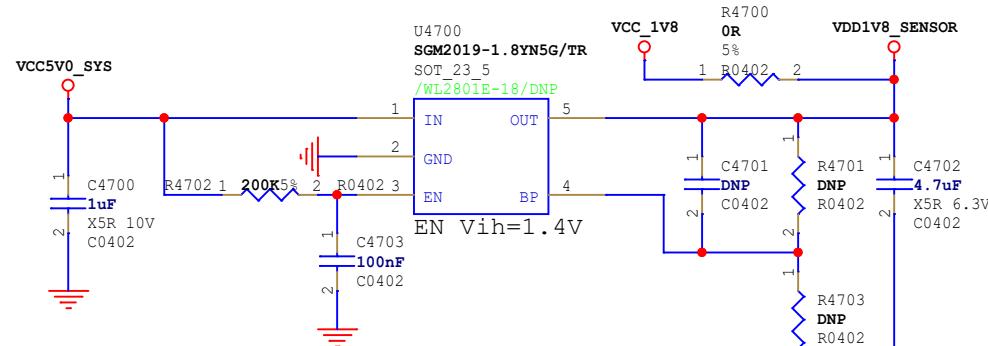
Rockchip Confidential

Rockchip		Rockchip Electronics Co., Ltd	
Project:	RV1106G UVC DEMO		
File:	25.USB Port		
Date:	Saturday, July 15, 2023	Rev:	V1.1
Designed by:	whb	Reviewed by:	Default
Sheet:	12 of 15		

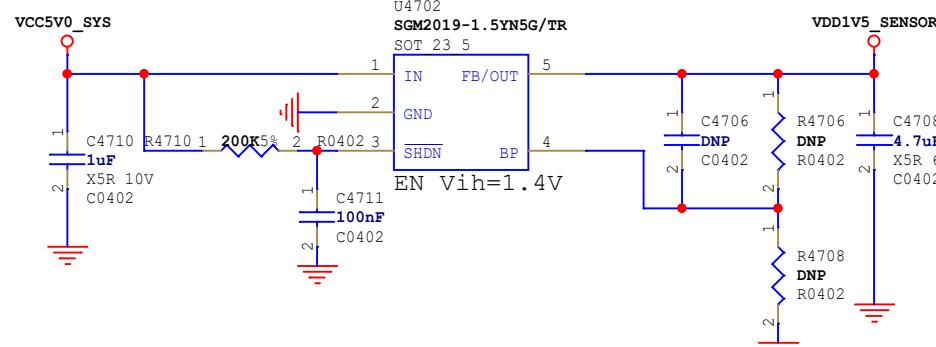
Sensor



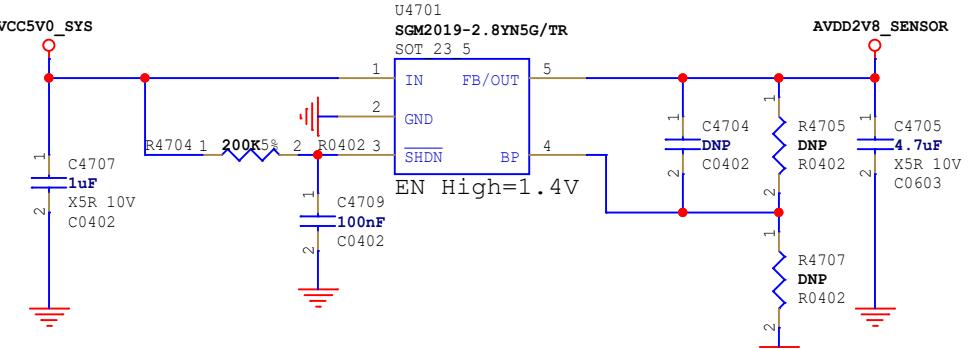
VCC5V0_SYS--->VDD1V8_SENSOR



VCC5V0_SYS--->VDD1V5_SENSOR



VCC5V0_SYS--->AVDD2V8_SENSOR



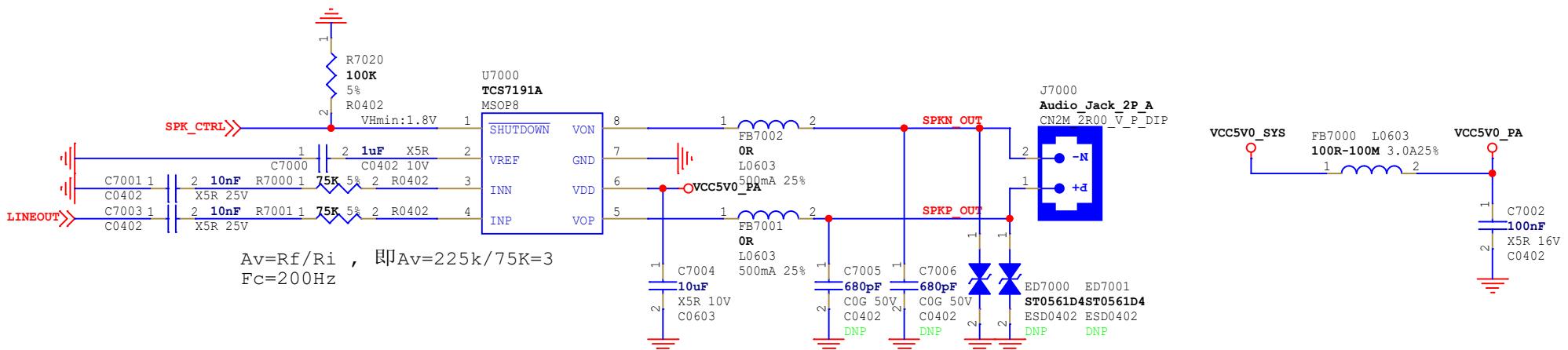
Note:
Default power-on timing:
All three power on at the same time.
Or DOVDD (VCC_1V8) --> DVDD (VCC_1V5) --> AVDD (VCC_2V8)

Rockchip Confidential

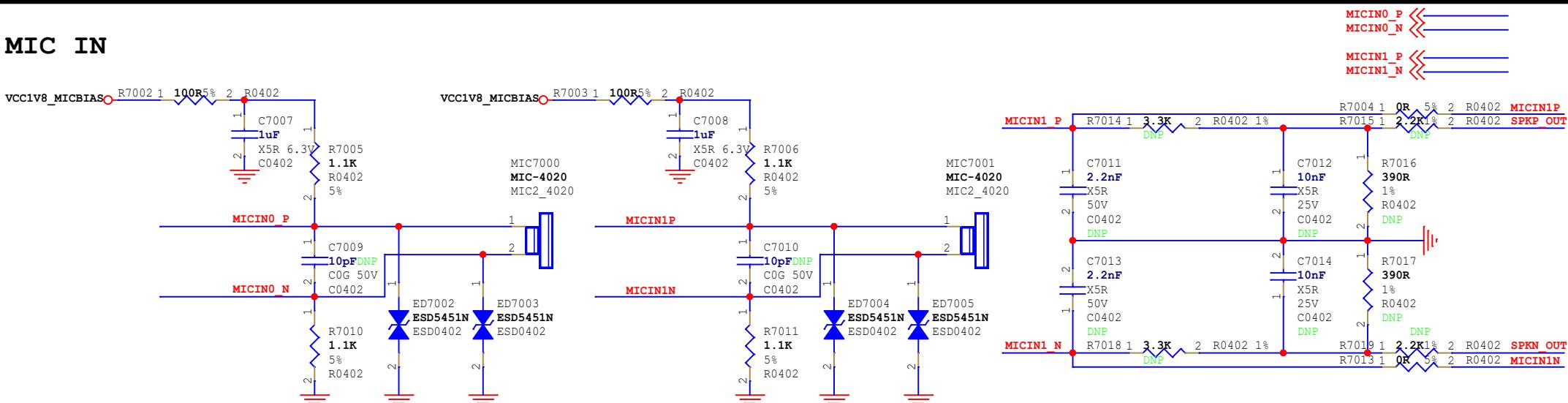
Rockchip		Rockchip Electronics Co., Ltd
Project:	RV1106G UVC DEMO	
File:	47.VI-Camera	
Date:	Thursday, July 27, 2023	Rev:
Designed by:	whb	Reviewed by:
Sheet:	Default	13 of 15

Speaker Out

NOTE: If there is no EMI problem, the ferrite beads can be replaced by 0ohm resistors;

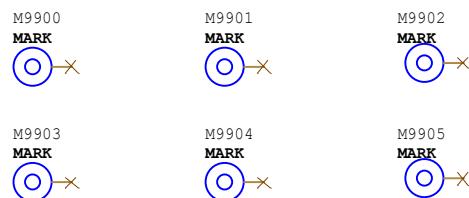
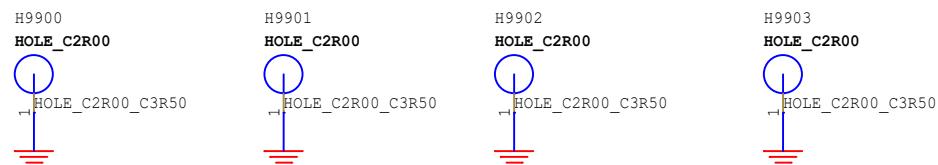


MIC IN



Rockchip Confidential

Rockchip		Rockchip Electronics Co., Ltd					
Project:	RV1106G UVC DEMO						
File:	70.Audio Port						
Date:	Thursday, July 27, 2023	Rev:	V1.1				
Designed by:	whb	Reviewed by:	Default	Sheet: 14 of 15			

HOLE***Rockchip Confidential***

Rockchip	Rockchip Electronics Co., Ltd
Project:	RV1106G UVC DEMO
File:	99.MARK/HOLE
Date:	Saturday, July 15, 2023
Designed by:	whb
Reviewed by:	Default
Rev:	V1.1
Sheet:	15 of 15