Label	Assembly	ADDR	Data	Comment
LOOP:	NOP	00	02	Test NOP
	LDIA	01	04	Init VAR_X:
	AA	02	AA	10101010
	STXA	03	08	
	VAR_X:	04	16	
	OUTA	05	3A	Display VAR_X:
	JMP	06	30	Test relative 0 jump.
	SKIP:	07	08	
SKIP:	LDIB	08	05	Load B with VAR_X: pointer
	VAR_X:	09	16	
	LDXA	0A	00	Load A with VAR_X: contents
	VAR_X:	0B	16	
	TAB	0C	0C	Copy A to B
	LDIA	0D	04	Load A with 01010101
	55	0E	55	
	STXA	0F	08	Save A to VAR_X:
	VAR_X:	10	16	
	LDXB	11	01	Load B with VAR_X:
	VAR_X:	12	16	
	OUTA	13	3A	Display VAR_X:
	JMP	14	30	Forever!
	LOOP:	15	00	
VAR_X:	55	16	55	