Circuits with Arrows

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We'll need delay to simulate gate delays. Let's redefine stream function arrows and introduce an ArrowCircuit class that supports delay.

```
{-# LANGUAGE Arrows #-}
module Circuits where
import Control.Arrow
import Control.Category(Category, (≫), (.), id)
import Data.List hiding(or)
import Prelude hiding((.), id, or)
newtype SF a b = SF { runSF :: [a] \rightarrow [b] }
instance Category SF where
  \verb"id" = \verb"arr" id"
  SF f \circ SF g = SF (f \circ g)
instance Arrow SF where
  arr f = SF (map f)
  first (SF f) = SF (unzip \gg first f \gg uncurry zip)
class ArrowLoop a \Rightarrow ArrowCircuit a where
  \mathtt{delay} \, :: \, \mathtt{b} \, \to \, \mathtt{a} \, \, \mathtt{b} \, \, \mathtt{b}
instance ArrowLoop SF where
  loop (SF f) = SF \$ \lambdaas 	o
       let (bs, cs) = unzip (f (zip as (stream cs))) in bs
     where stream (x:xs) = x : stream xs
instance ArrowCircuit SF where
  \texttt{delay} \ \mathtt{x} = \mathtt{SF} \ (\mathtt{init} \circ (\mathtt{x} \ :))
    Now let's build some logic gates.
or :: Arrow a \Rightarrow a (Bool, Bool) Bool
or = arr $ uncurry (||)
nor :: Arrow a \Rightarrow a (Bool, Bool) Bool
\mathtt{nor} = \mathtt{or} \gg \mathtt{arr} \ \mathtt{not}
```

```
flipflop :: ArrowCircuit a \Rightarrow a (Bool, Bool) (Bool, Bool)
flipflop = loop (arr (\lambda((a,b), \tilde{(c,d)}) \rightarrow ((a,d), (b,c)))
        ≫ nor *** nor
                            -- flip the flop
        >>> delay (False, True) -- initialize c low, d high
        \gg arr id &&& arr id) -- duplicate output for feedback
-- detect rising edges
edge :: SF Bool Bool
edge = arr id &&& delay False
   \gg arr (\lambda(a,b) \rightarrow a && not b)
class Signal a where
  showSignal :: [a] \rightarrow String
instance Signal Bool where
  showSignal bs = concat top++"\lambdan"++concat bot++"\lambdan"
    where (top,bot) = unzip (zipWith sh (False:bs) bs)
           sh True True = ("__"," ")
           sh True False = (" ","|_")
           sh False True = (" _"," | ")
           {\tt sh}\ {\tt False}\ {\tt False}=("\ ","_-")
instance (Signal a, Signal b) \Rightarrow Signal (a,b) where
  showSignal xys = showSignal (map fst xys)
                 ++ showSignal (map snd xys)
instance Signal a \Rightarrow Signal [a] where
  showSignal = concat \circ map showSignal \circ transpose
sig = concat o map (uncurry replicate)
flipflopInput = sig
  [(5,(False,False)),(2,(False,True)),(5,(False,False)),
  (2, (True, False)), (5, (False, False)), (2, (True, True)),
  (6,(False,False))]
-- to test: putStrLn \ "input: \lambdan" ++ showSignal flipflopInput ++ "output: \lambdan" ++ (
    showSignal $ runSF flipflop flipflopInput)
```