DUNK Micro-Codes

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1 Micro-instructions

- `0x0000` - do nothing
- `0x0001` - begin instruction, and save `*(pk+1)` to `tmpC`
- `0xN002` - output `srN` to data bus
- `0xN003` - output `srN` to addr bus
- `0xN004` - output *`srN` to data bus
- `0xN005` - output *(`srN`+`offs`) to data bus
- `0xN006` - write to `srN` from data bus
- `0xN01M` - Special register N special function M
  - `0x0N10` - add N+1 to `pk`
  - `0x0N11` - output `*(pk+N+1)` to data bus and add N+1 to `pk`
  - `0x0012` - write to `pk` from `tmpA`
  - `0x0013` - write to `pk` from `tmpB`
  - `0x0014` - write to `pk` from `tmpA` if data bus is zero; increment otherwise
  - `0x0015` - write to `pk` from `tmpA` if data bus is nonzero; increment otherwise
  - `0x0016` - write to `pk` from `tmpA` if data bus is negative; increment otherwise
  - `0x0017` - write to `pk` from `tmpA` if data bus is non-negative; increment otherwise
  - `0x0018` - write to `pk` from `tmpA` if data bus is positive; increment otherwise
  - `0x0019` - write to `pk` from `tmpA` if data bus is non-positive; increment otherwise
  - `0x1010` - increment `sp`
  - `0x1011` - decrement `sp`
  - `0x1N12` - put `sp-(N+1)` on the addr bus, and substract N+1 from `sp`
  - `0x1N13` - put `*sp` on the data bus and add N+1 to `sp`
- `0x2010` - output `tmpA` to `pk` directly
- `0x2010` - output `tmpB` to `pk` directly
- `0xN020` - output `rN` to data bus
- `0xN021` - output `rN` to addr bus
- `0xN022` - output *`rN` to data bus
- `0xN023` - output *(`rN`+`offs`) to data bus
- `0xN024` - write to `rN` from data bus
- `0x0025` - write data bus to *(addr bus)
- `0x0026` - write data bus to *(addr bus+`offs`)
- `0x0027` - output *(addr bus) RAM to data bus

- `0x0028` - output *(addr bus+`offs`) RAM to data bus
- `0xNM4A` - output ALU operation A on (`rN`, `rM`) to data bus
- `0xN030` - set I/O pin `N` to input mode
- `0xN031` - set I/O pin `N` to output mode
- `0xN032` - set I/O pin `N` to low
- `0xN033` - set I/O pin `N` to high
- `0xN034` - output I/O pin `N` to data bus
- `0xN035` - write data bus to I/O pin `N`
- `0x0050` - prevent data bus from updating on next rising edge
- `0x0051` - prevent addr bus from updating on next falling edge
- `0x0052` - move the value on the addr bus to the data bus
- `0x0053` - move the value on the data bus to the addr bus
- `0xfffe` - end instruction
- `0xffff` - reset everything