

Course Information		
Course title		Computer Architecture
Semester		108-1
Designated for		COLLEGE OF ELECTRICAL ENGINEERING AND COMPUTER SCIENCE DEPARTMENT OF ELECTRICAL ENGINEERING
Curriculum Number		EE4039
Curriculum Identity Number		901 43200
Course Syllabus		
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Course Description		1. Computer Abstractions and Technology 2. Instructions 3. Arithmetics 4. Processors 5. Memory Hierarchy 6. Parallel Processors
References		J. L. Hennessy, D. A. Patterson, Computer Architecture: A Quantitative Approach, Morgan Kaufmann, 2013.
Progress		
Week	Date	Topic
第 1 週	09/10	Abstractions and Technology
第 2 週	09/17	Instructions (1)
第 3 週	09/24	Instructions (2)
第 4 週	10/01	Instructions (3) / Arithmetic (1)
第 5 週	10/08	Arithmetics (2) (no new slides this week)
第 6 週	10/15	Arithmetics (3) / Processors (1)
第 7 週	10/22	Processors (2)
第 8 週	10/29	Processors (3)
第 9 週	11/05	Midterm

第 10 週	11/12	Verilog-HDL
第 11 週	11/19	Processors (4) (no new slides this week)
第 12 週	11/26	Memory (1)
第 13 週	12/03	Memory (2)
第 14 週	12/10	Memory (3)
第 15 週	12/17	Paralle Processors (1)
第 16 週	12/24	Parallel Processors (2)
第 17 週	12/31	Interconnection Networks
第 18 週	01/07	Final Exam