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| |  | | --- | | **Course Information** |  |  |  | | --- | --- | | Course title | Computer Architecture | | Semester | 108-1 | | Designated for | COLLEGE OF ELECTRICAL ENGINEERING AND COMPUTER SCIENCE  DEPARTMENT OF ELECTRICAL ENGINEERING | | Curriculum Number | EE4039 | | Curriculum Identity Number | 901 43200 | | **Course Syllabus** | | | **Please respect the intellectual property rights of others and do not copy any of the course information without permission** | | | Course Description | 1. Computer Abstractions and Technology 2. Instructions 3. Arithmetics 4. Processors 5. Memory Hierarchy 6. Parallel Processors | | References | J. L. Hennessy, D. A. Patterson, Computer Architecture: A Quantitative Approach, Morgan Kaufmann, 2013. |  |  | | --- | | **Progress** |  |  |  |  | | --- | --- | --- | | Week | Date | Topic | | 第1週 | 09/10 | Abstractions and Technology | | 第2週 | 09/17 | Instructions (1) | | 第3週 | 09/24 | Instructions (2) | | 第4週 | 10/01 | Instructions (3) / Arithmetic (1) | | 第5週 | 10/08 | Arithmetics (2) (no new slides this week) | | 第6週 | 10/15 | Arithmetics (3) / Processors (1) | | 第7週 | 10/22 | Processors (2) | | 第8週 | 10/29 | Processors (3) | | 第9週 | 11/05 | Midterm | | 第10週 | 11/12 | Verilog-HDL | | 第11週 | 11/19 | Processors (4) (no new slides this week) | | 第12週 | 11/26 | Memory (1) | | 第13週 | 12/03 | Memory (2) | | 第14週 | 12/10 | Memory (3) | | 第15週 | 12/17 | Paralle Processors (1) | | 第16週 | 12/24 | Parallel Processors (2) | | 第17週 | 12/31 | Interconnection Networks | | 第18週 | 01/07 | Final Exam | |

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