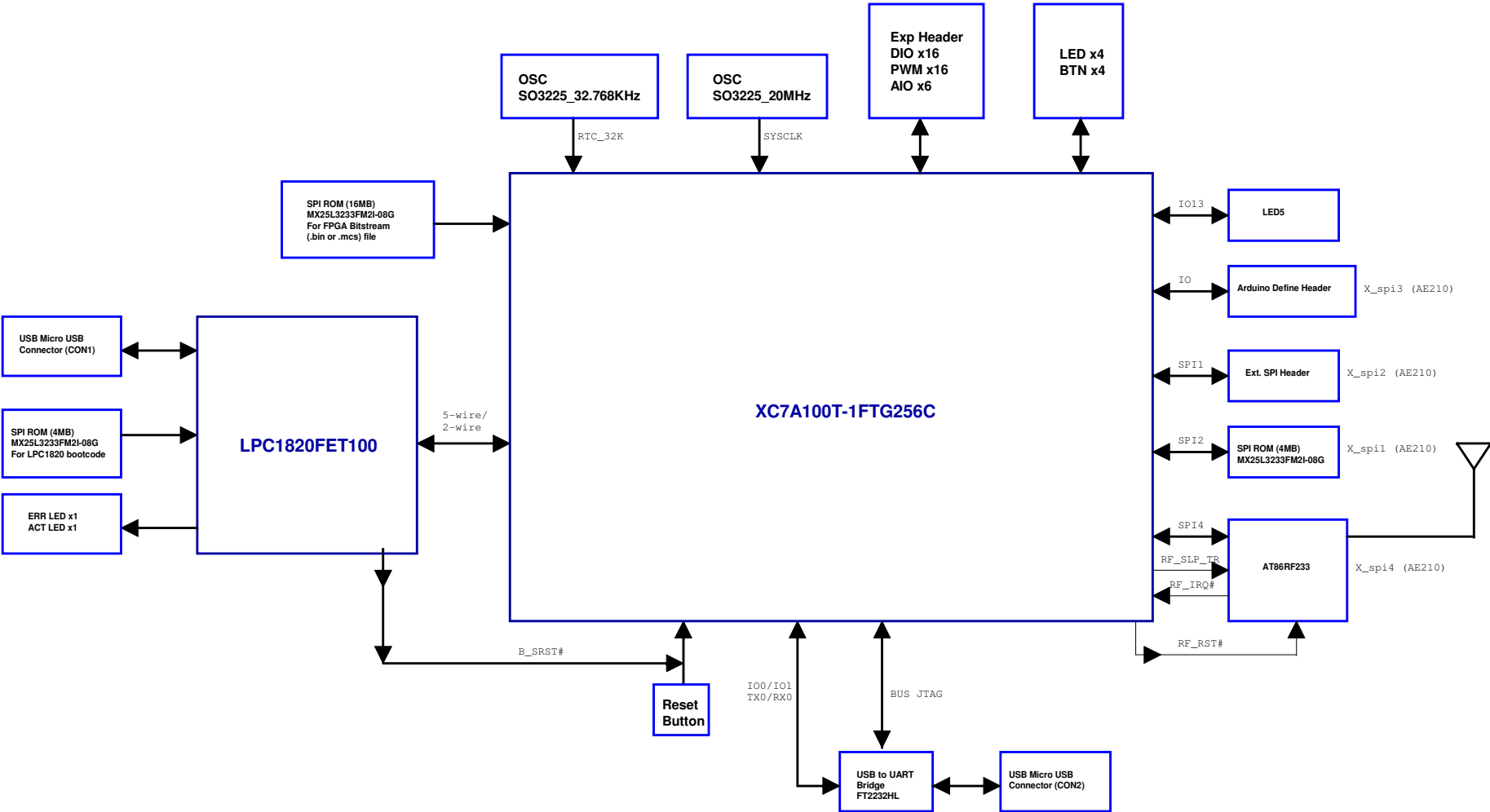
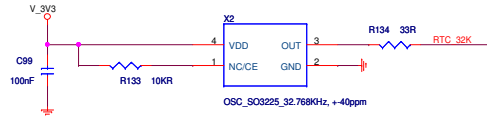
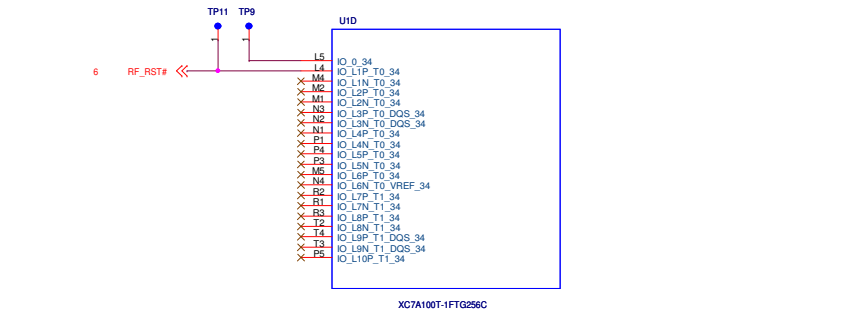
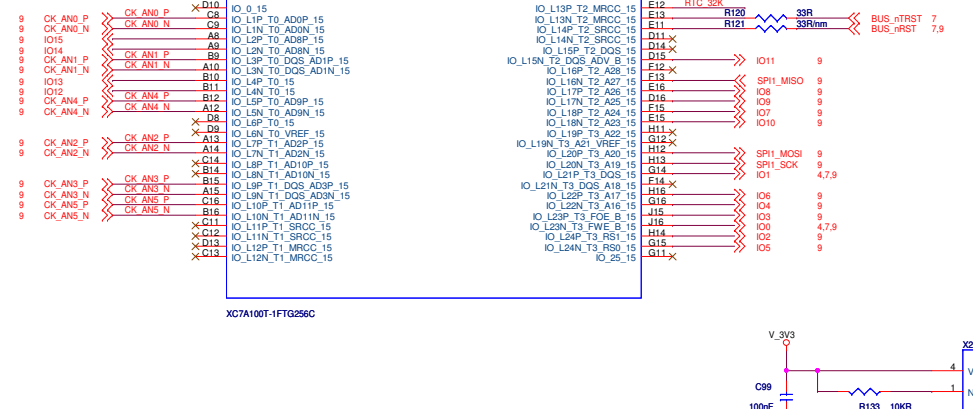
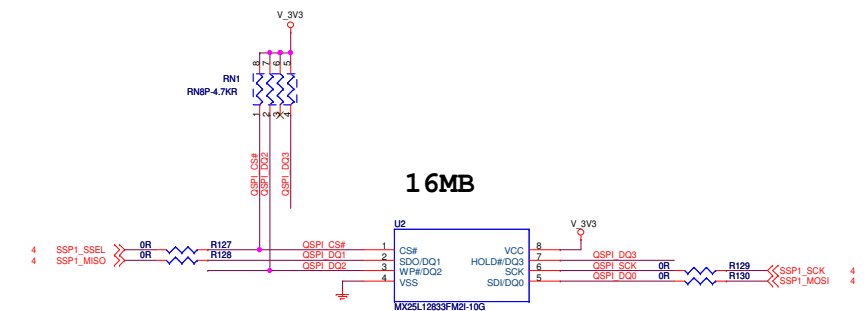
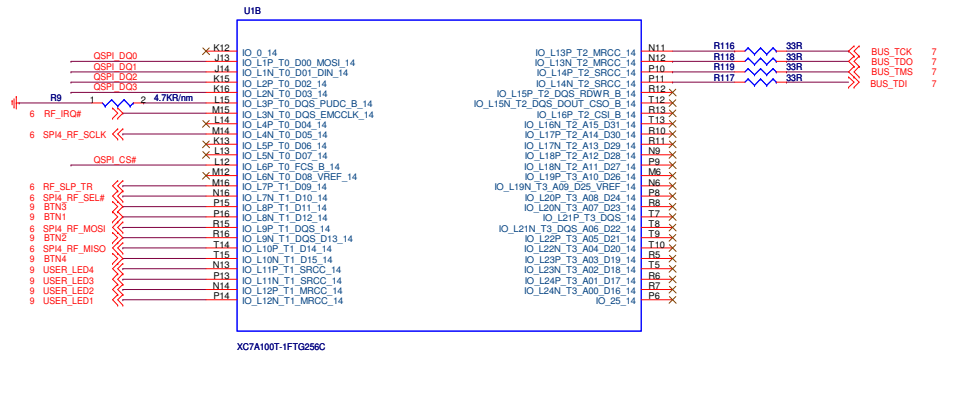
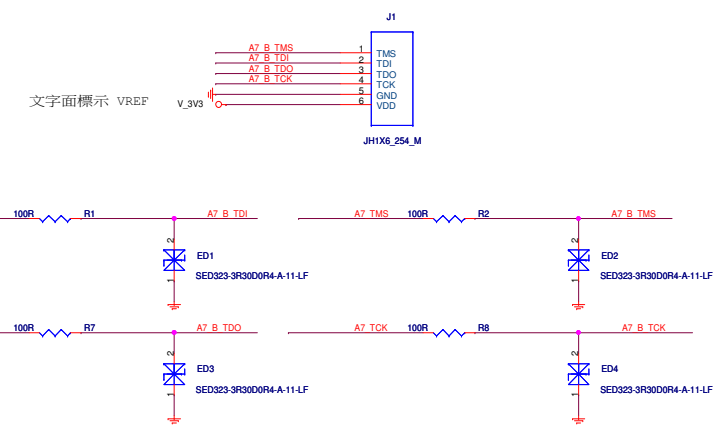
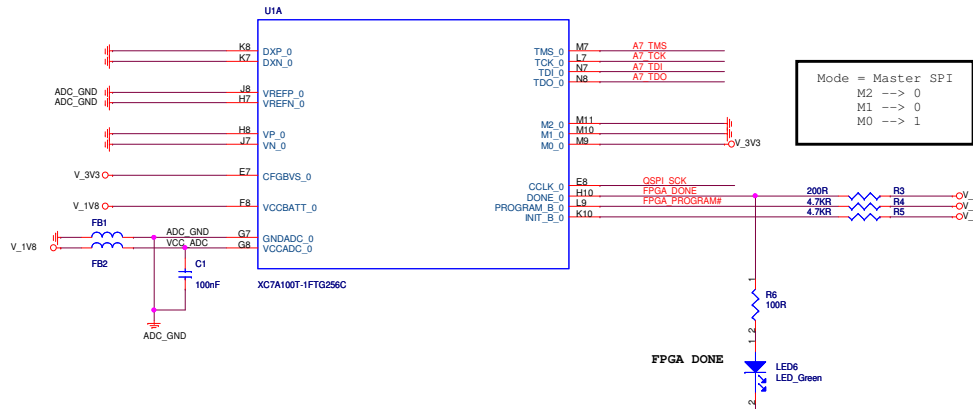


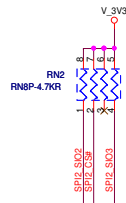
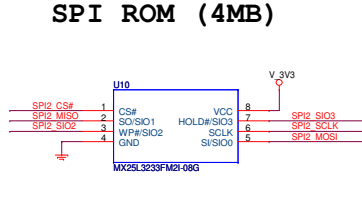
PAGE	DESCRIPTION
1	Block Diagram
2	XC7A100T Bank0_14_15_34, QSPI
3	XC7A100T Bank_35, Flash, SRAM
4	NXP LPC1820-1
5	NXP LPC1820-2
6	AT86RF233
7	MISC
8	Power
9	History



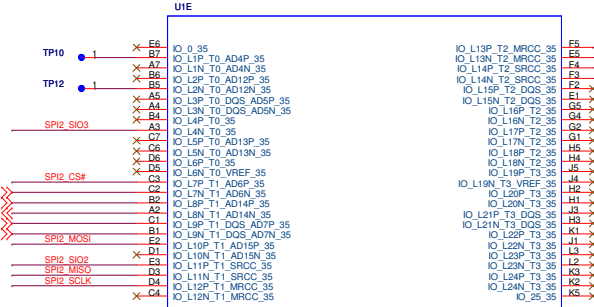
# JTAG-CON



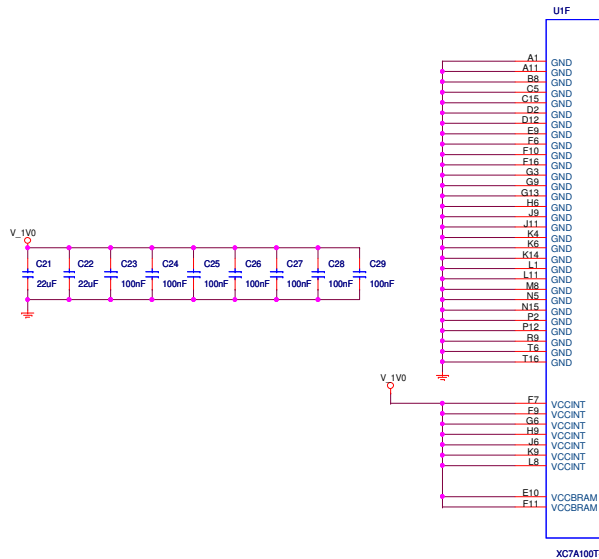
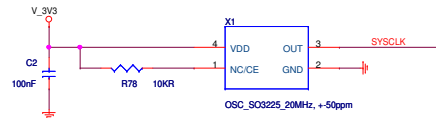
# SPI ROM (4MB)



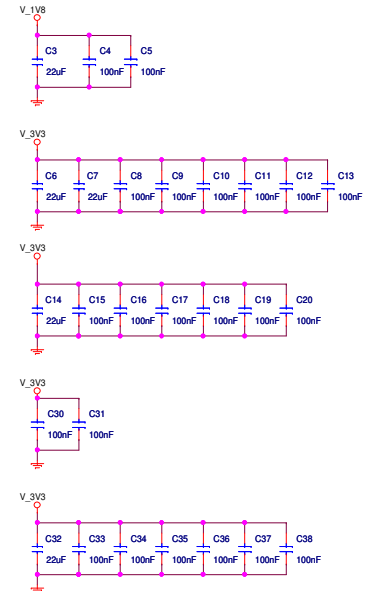
4,7.9 B.SRST#  
4 B.TRST#  
4 B.TDI  
4 B.TMS  
4 B.TDO



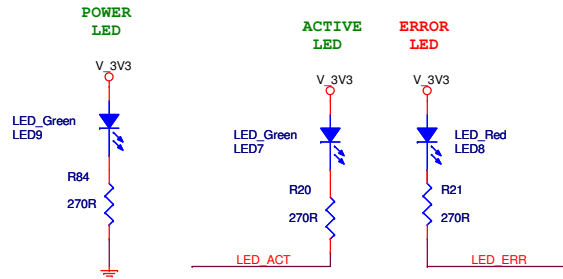
XC7A100T-1FTG256C



XC7A100T-1FTG256C



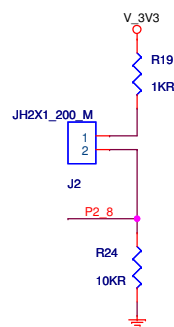
# SPI ROM (4MB)



P2\_9



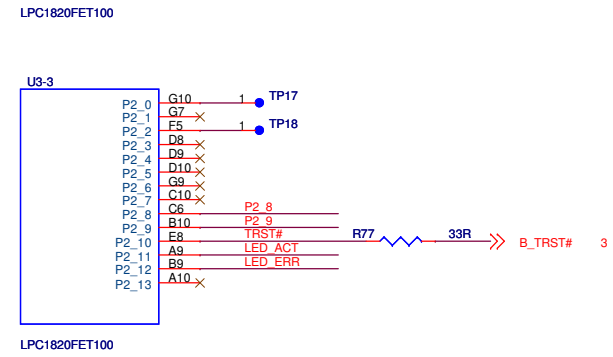
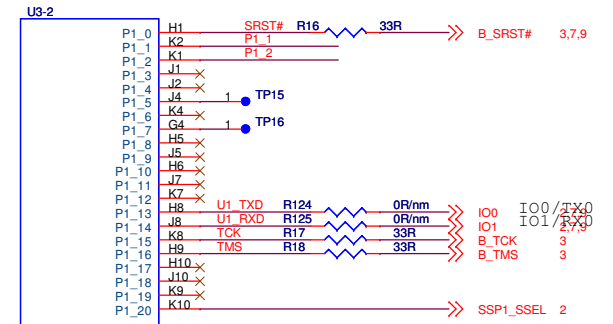
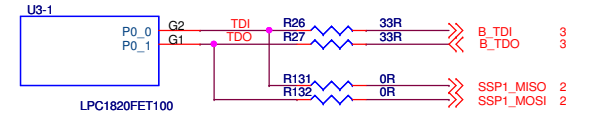
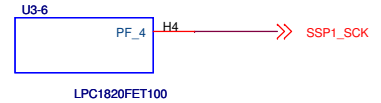
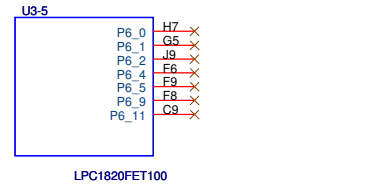
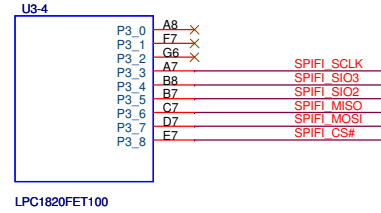
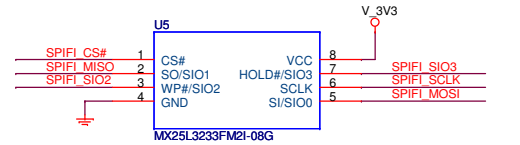
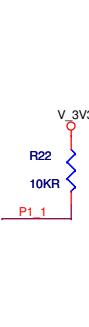
P2\_8



P1\_2



P1\_1



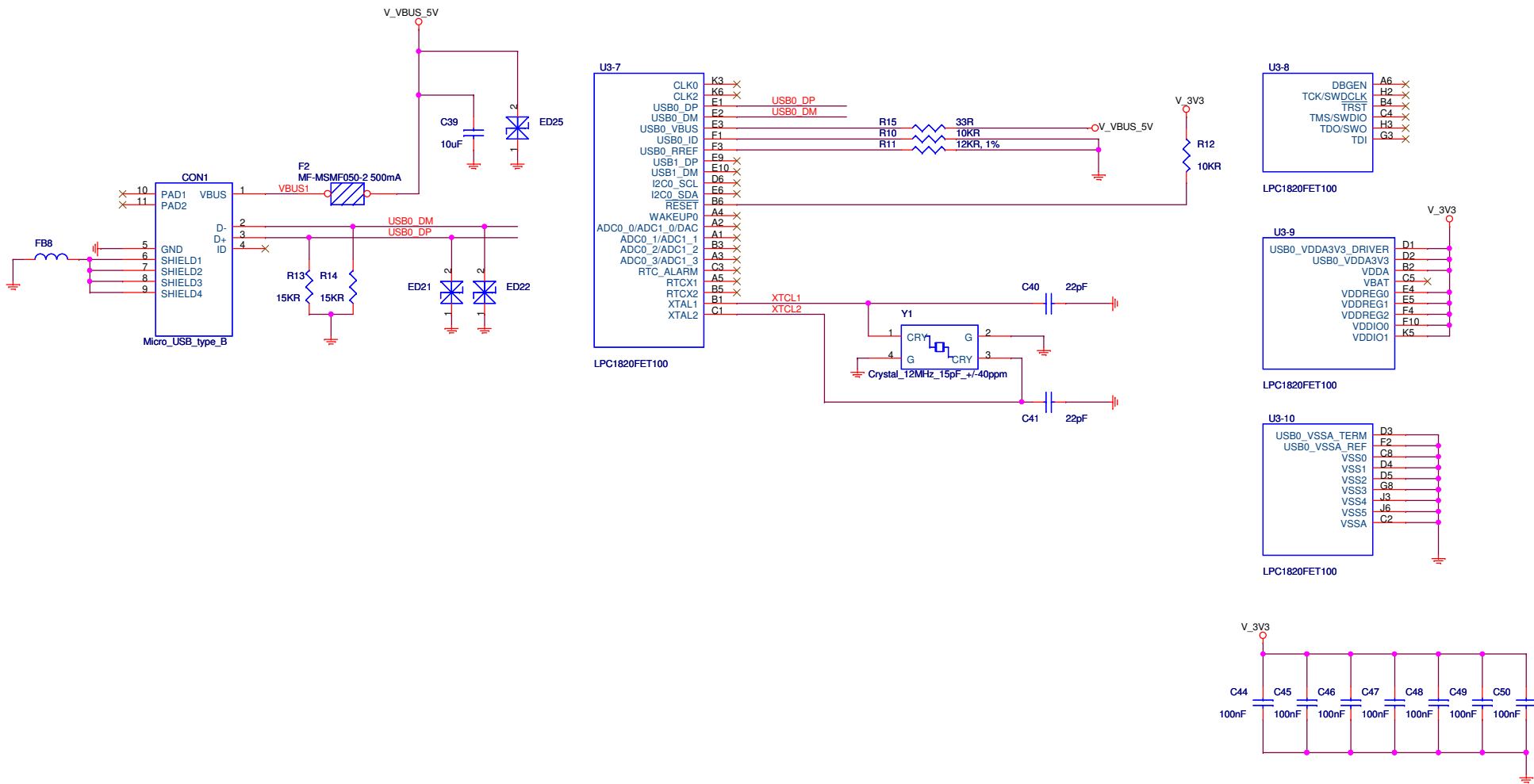
Pin Number	P2_9	P2_8	P1_2	P1_1
USB0 Boot	LOW	HIGH	LOW	HIGH
SPIFI Boot	LOW	LOW	LOW	HIGH

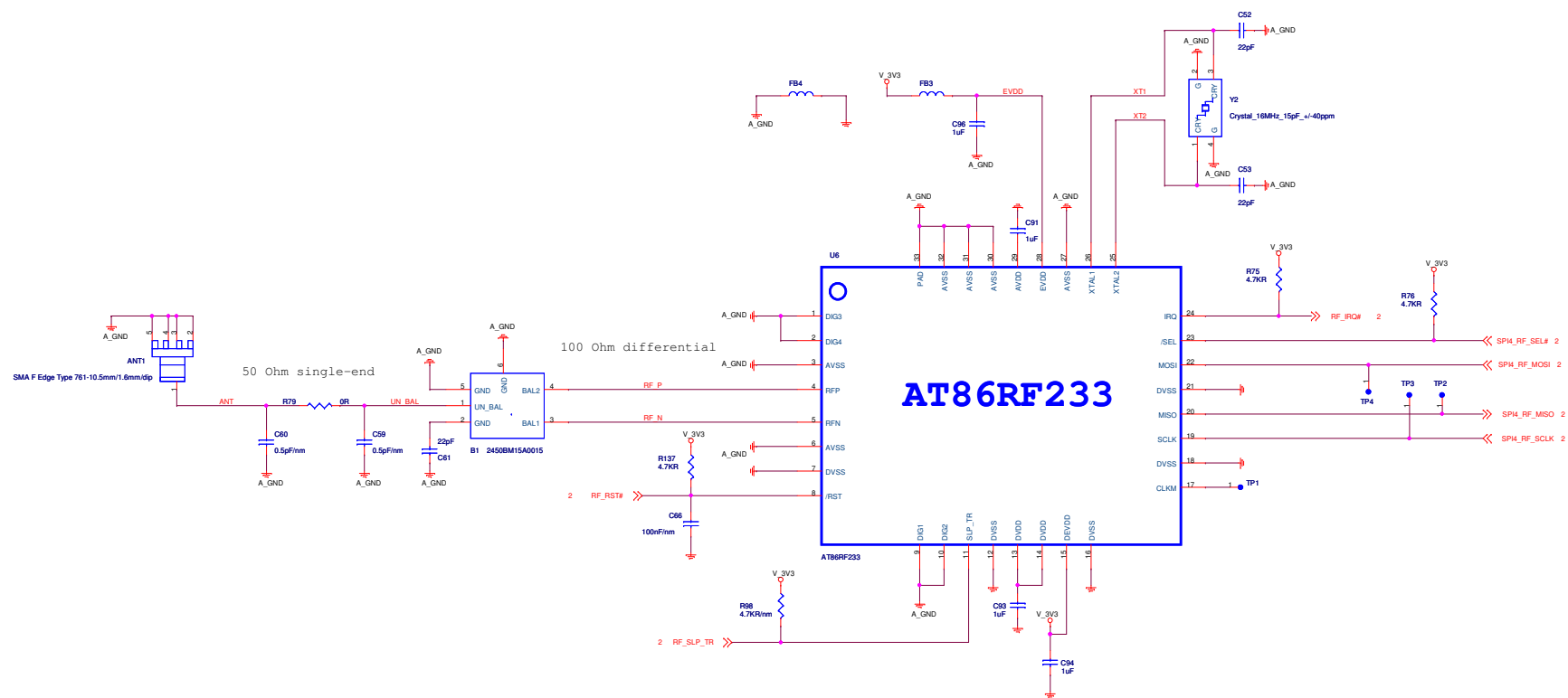
**ANDES TECHNOLOGY CORP.**

Title: NXP LPC1820-1

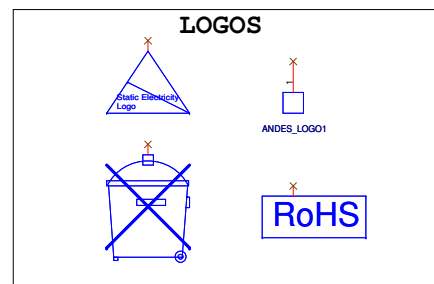
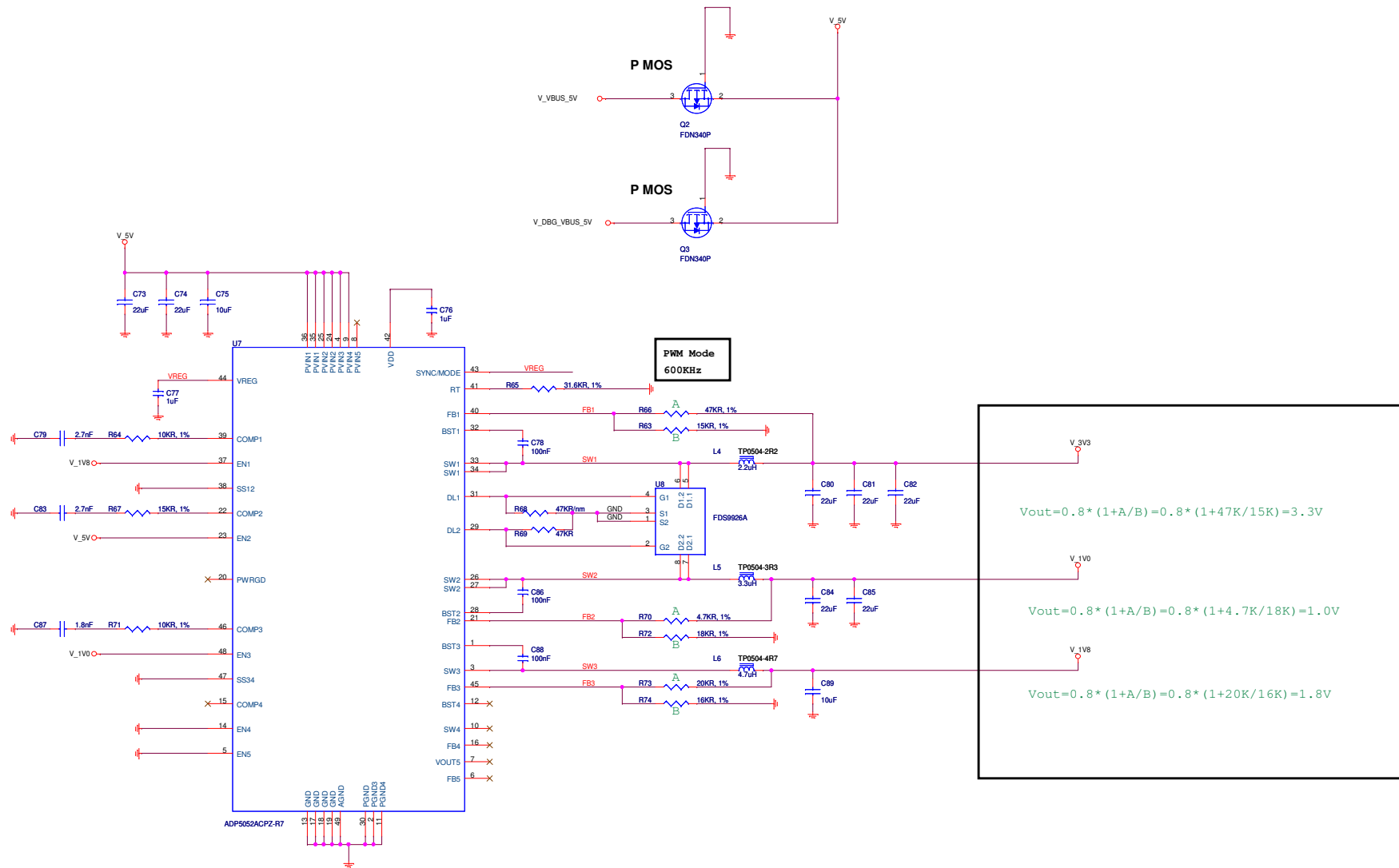
Size: B Document Number: Corvette F1 Designer: Jason Rev: 1.0

Date: Friday, April 27, 2018 Sheet: 4 of 10



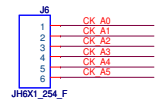
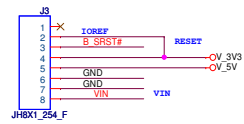




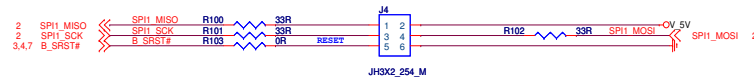




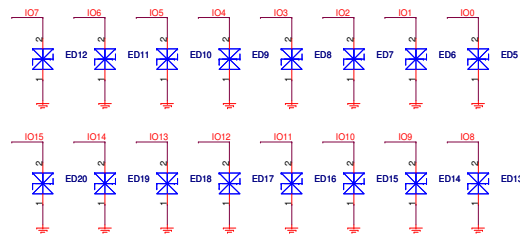
## Arduino Connectors



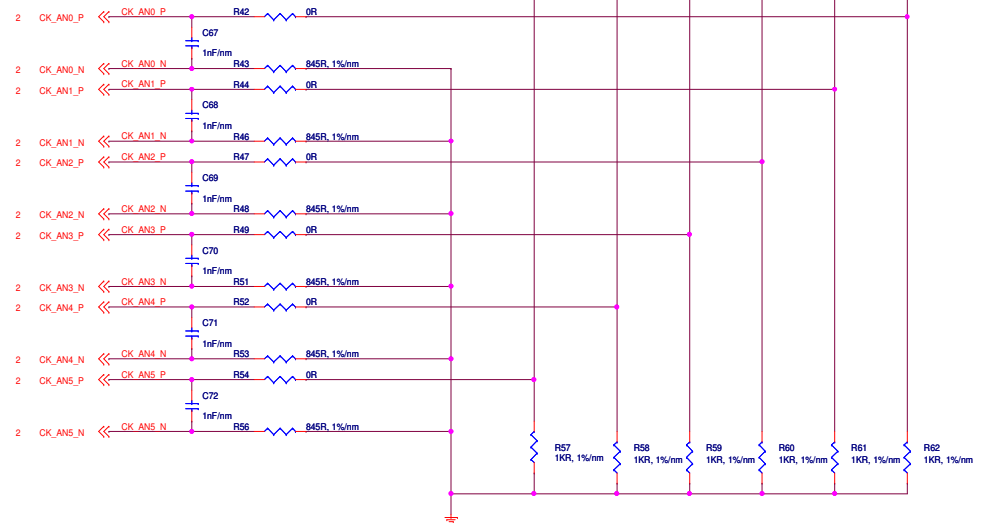
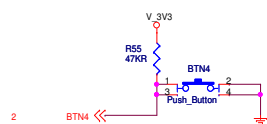
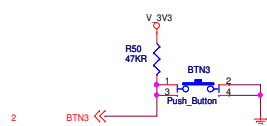
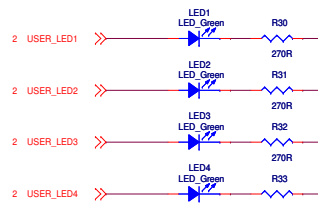
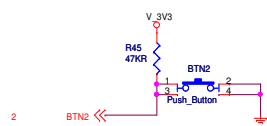
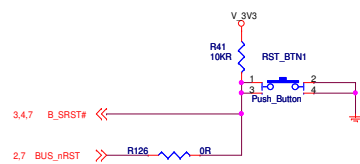
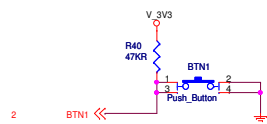
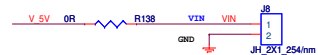
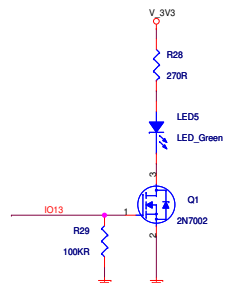
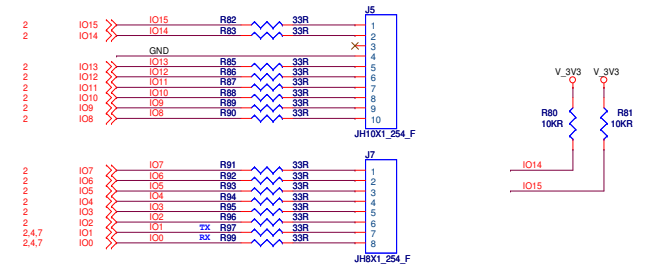
## Arduino Connectors (SPI Connector)



ESD DIODE



## Arduino Connectors



Revision History

V1.0 (Initial Version)	2018/4/24