S1985 I/O port mirroring

Α7	A6	A5	A4	А3	A2	A1
	•					
rinter – pr			1			
1	0	0	1 1	0	0	0
T	U	0	1	0	U	0
1	0	0	1	0	0	1
Printer – alt - probably usable					A2	
1	0	0	1	0	1	0
1	0	0	1	0	1	0
_	-					
1	0	0	1	0	1	1
			_			
DP – prim	ary					
1	0	0	1	1	0	0
1	0	0	1	1	0	0
1	0	0	1	1	0	1
1	0	0	1	1	0	1
	fully ucable				Λ2	
	fully usable		1	1	A2	0
1	0	0	1	1	1	0
1	0	0 0	1	1	1	0
1 1 1	0 0 0	0 0 0	1	1 1	1 1 1	0
1	0	0 0	1	1	1	0
1 1 1 1 2SG – prim	0 0 0 0	0 0 0 0	1 1 1	1 1 1	1 1 1	0 1 1
1 1 1 1 2 SG – prim	0 0 0 0	0 0 0 0 0	1 1 1	1 1 1	1 1 1 1	0 1 1
1 1 1 1 2 SG – prim 1	0 0 0 0	0 0 0 0 0	1 1 1 0 0	1 1 1 0 0	1 1 1 1	0 1 1 0 0
1 1 1 1 2 SG – prim	0 0 0 0	0 0 0 0 0	1 1 1	1 1 1	1 1 1 1	0 1 1
1 1 1 2 2 3 5 6 7 7 1 1	0 0 0 0	0 0 0 0 0	1 1 1 0 0	1 1 1 0 0	1 1 1 1	0 1 1 0 0
1 1 1 2 2 3 5 6 7 7 1 1	0 0 0 0	0 0 0 0 0	1 1 1 0 0	1 1 1 0 0	1 1 1 1 0 0	0 1 1 0 0
1 1 1 2SG – prim 1 1 1	0 0 0 0 ary 0 0	0 0 0 0 0	1 1 1 0 0 0	1 1 1 0 0 0	1 1 1 1 0 0 0	0 1 1 0 0 1
1 1 1 2SG – prim 1 1 1 2SG – alt -	0 0 0 0 ary 0 0 0 0	0 0 0 0 0	1 1 1 0 0 0	1 1 1 0 0 0	1 1 1 1 0 0 0	0 1 1 0 0 0 1
1 1 1 2SG – prim 1 1 1 2SG – alt –	0 0 0 0 ary 0 0 0 0 fully usable	0 0 0 0 0	1 1 1 0 0 0 0	1 1 1 0 0 0	1 1 1 1 1 0 0 0	0 1 1 0 0 1
1 1 1 1 2SG – prim 1 1 1 2SG – alt - 1 1 2PI – prima	0 0 0 0 ary 0 0 0 fully usable 0 0	0 0 0 0 0	1 1 1 0 0 0 0	1 1 1 1 0 0 0 0	1 1 1 1 1 0 0 0 0	0 1 1 0 0 1
1 1 1 2SG – prim 1 1 1 2SG – alt –	0 0 0 0 ary 0 0 0 0 fully usable	0 0 0 0 0	1 1 1 0 0 0 0	1 1 1 0 0 0	1 1 1 1 1 0 0 0	0 1 1 0 0 1

SI	ne.	Δt	1
וכי	16	C:I	- 1

1	0	1	0	1	0	1	
						-	
PPI – alt - f	ully usable				A2		
1	0	1	0	1	1	0	
1	0	1	0	1	1	0	
1	0	1	0	1	1	1	
1	0	1	0	1	1	1	
RTC – prim	RTC – primary						
1	0	1	1	0	1	0	
1	0	1	1	0	1	0	
	•						
RTC – alt - fully usable						A1	
1	0	1	1	0	1	1	
1	0	1	1	0	1	1	

(*) You can write (w), read (r) or both (w/r) to this port according to 1

Sheet1

	Dout	Dood / Muito (t)	7
Α0	Port	Read / Write (*)	_
0	90	W/R	Print strobe write, printer status read
1	91	W/R	Printer data write/read
1	93	W	Printer bus direction
	T		
0	94	W/R	Print strobe write, printer status read
1	95	W/R	Printer data write/read
1	97	W	Printer bus direction
	•	1	-
0	98	W/R	
1	99	W/R	
0	9a	W/R	
1	9b	W/R	
0	90	W/P	
0	9c	W/R	
1			
	9d	W/R	
0	9d 9e	W/R W/R	
0	+		
	9e	W/R	
	9e	W/R	
1	9e 9f	W/R W/R	PSG address latch
0	9e 9f a0	W/R W/R	PSG address latch PSG data write
0 1	9e 9f a0 a1	W/R W/R	PSG data write
0	9e 9f a0	W/R W/R	
0 1	9e 9f a0 a1	W/R W/R	PSG data write
0 1	9e 9f a0 a1	W/R W/R	PSG data write
0 1 0	9e 9f a0 a1 a2	W/R W/R w w r	PSG data write PSG data read
0 1 0	9e 9f a0 a1 a2	W/R W/R w w r	PSG data write PSG data read PSG address latch
0 1 0 0	9e 9f a0 a1 a2 a4 a5	W/R W/R w w r	PSG data write PSG data read PSG address latch PSG data write
0 1 0 0 1 0	9e 9f a0 a1 a2 a4 a5 a6	W/R W/R W W r	PSG data write PSG data read PSG address latch PSG data write PSG data read
1 0 1 0	9e 9f a0 a1 a2 a4 a5	W/R W/R w w r	PSG data write PSG data read PSG address latch PSG data write

Sheet1

1	ab	W	Mode designation
0	ac	W/R	slot designation
1	ad	R	Keyboard return signal read
0	ae	W/R	Keyboard drive, cassette and PPI souind write/read
1	af	W	Mode designation
0	b4	W	Clock and backup RAM address latch
1	b5	W/R	Clock and backup RAM write/read
0	b6	W	Clock and backup RAM address latch
1	b7	W/R	Clock and backup RAM write/read

the S1985 datasheet