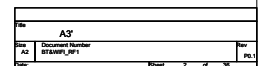


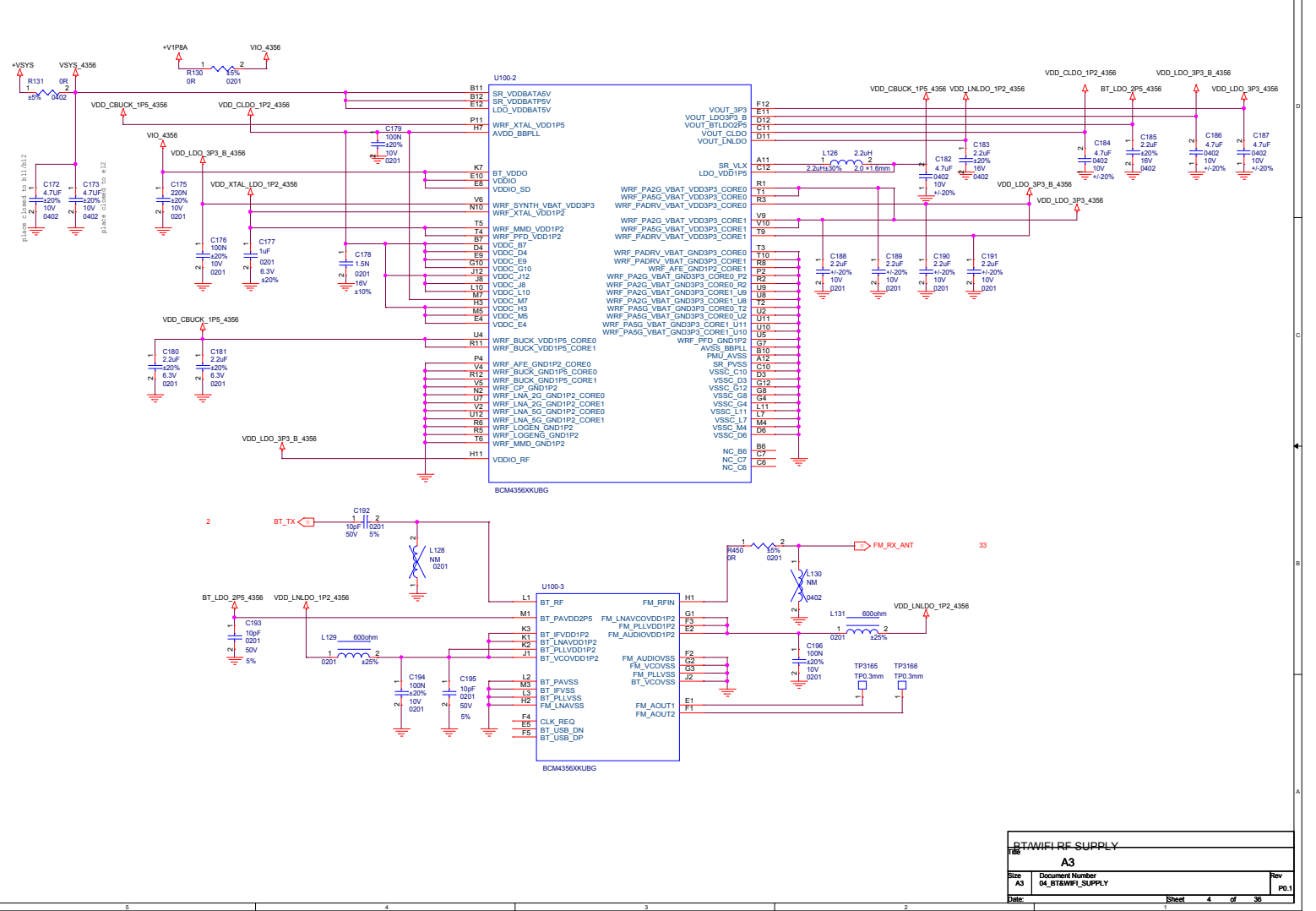
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03	03_BT&WIFI_RF2
04	04_BT&WIFI_SUPPLY
05	05_GPS
06	06_SOC_LPDDR3 CH0
07	07_SOC_LPDDR3 CH1
08	08_SOC_DISPLAY CAMERA
09	09_SOC_STORAGE I2S
10	10_SOC_USB I2C GPIO
11	11_SOC_UART SPI GPIO
12	12_SOC_GND
13	13_SOC_POWER 1
14	14_SOC_POWER 2
15	15_SOC_DECOUPLING
16	16_SOC_SIGNALS
17	17_PMU1
18	18_PMU2
19	19_PMU3
20	20_PMU4
21	21_PMU5
22	22_EMMC
23	23_DDR1
24	24_DDR2
25	25_LPDDR3 CH0 DE-CAP TERMI
26	26_LPDDR3 CH1 DE-CAP TERMI
27	27_LCD CONNECTOR
28	28_SD CONNECTOR
29	29_CHARGER
30	30_SPI_FLASH
31	31_SENSOR
32	32_Fuel Gauge
33	33_AUDIO
34	34_CAMERA
35	35_TYPE_C
36	36_CHANGE_LIST

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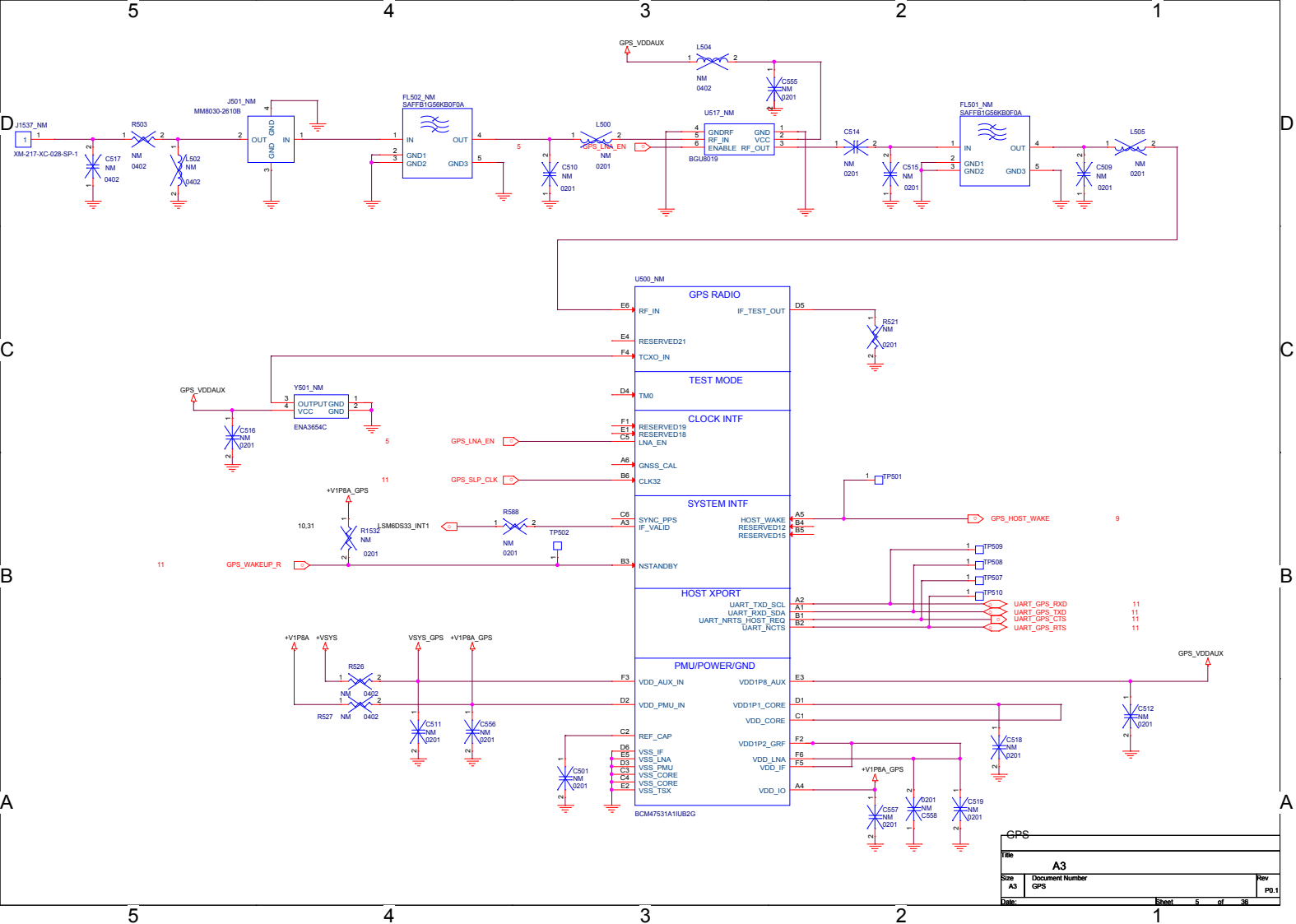






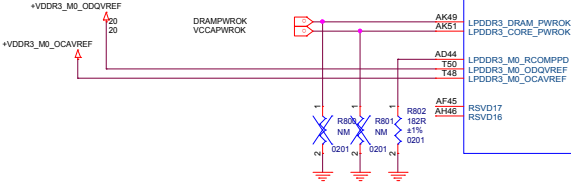


BTAWIFI RF SUPPLY		
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MEMORY CH0

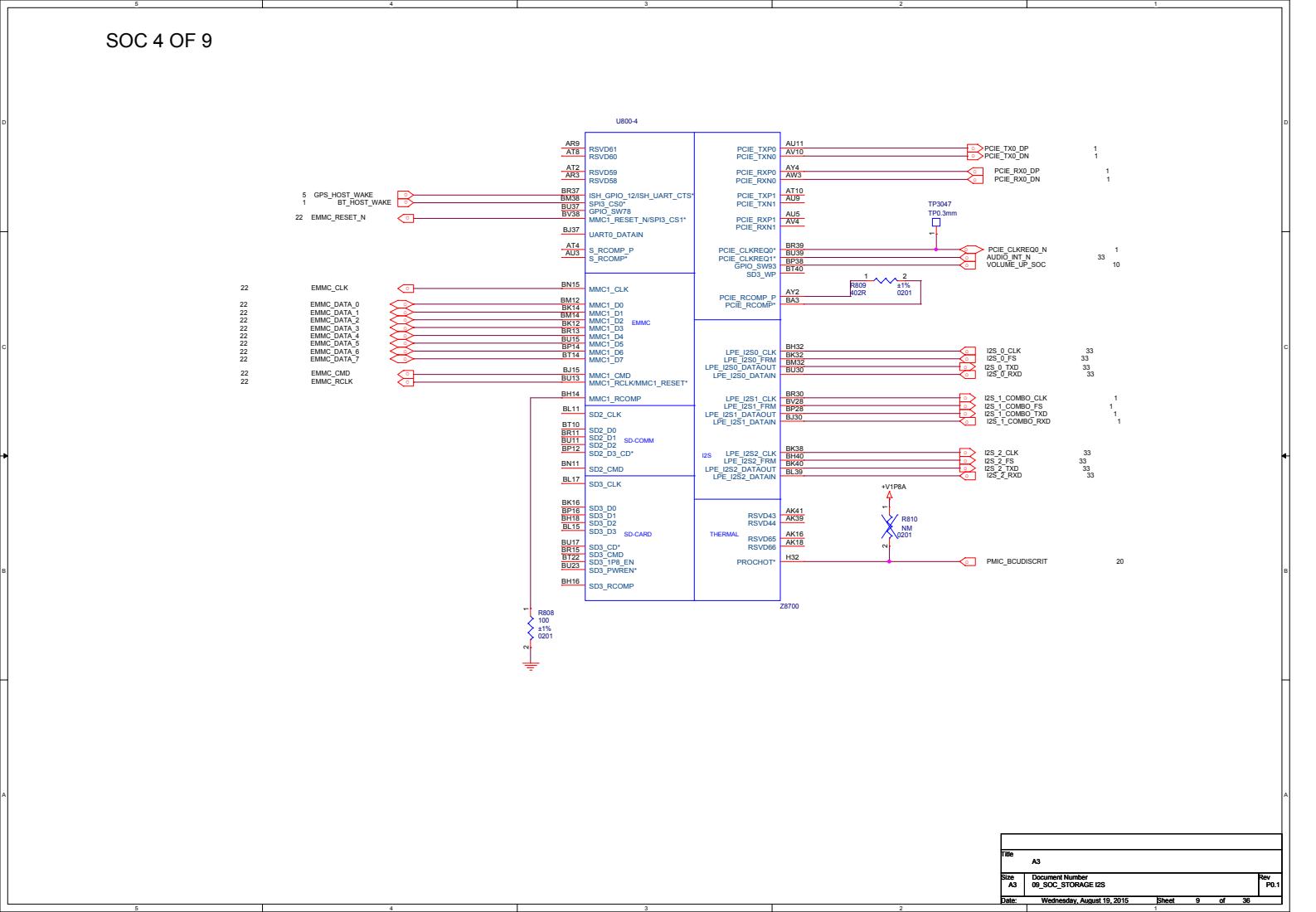


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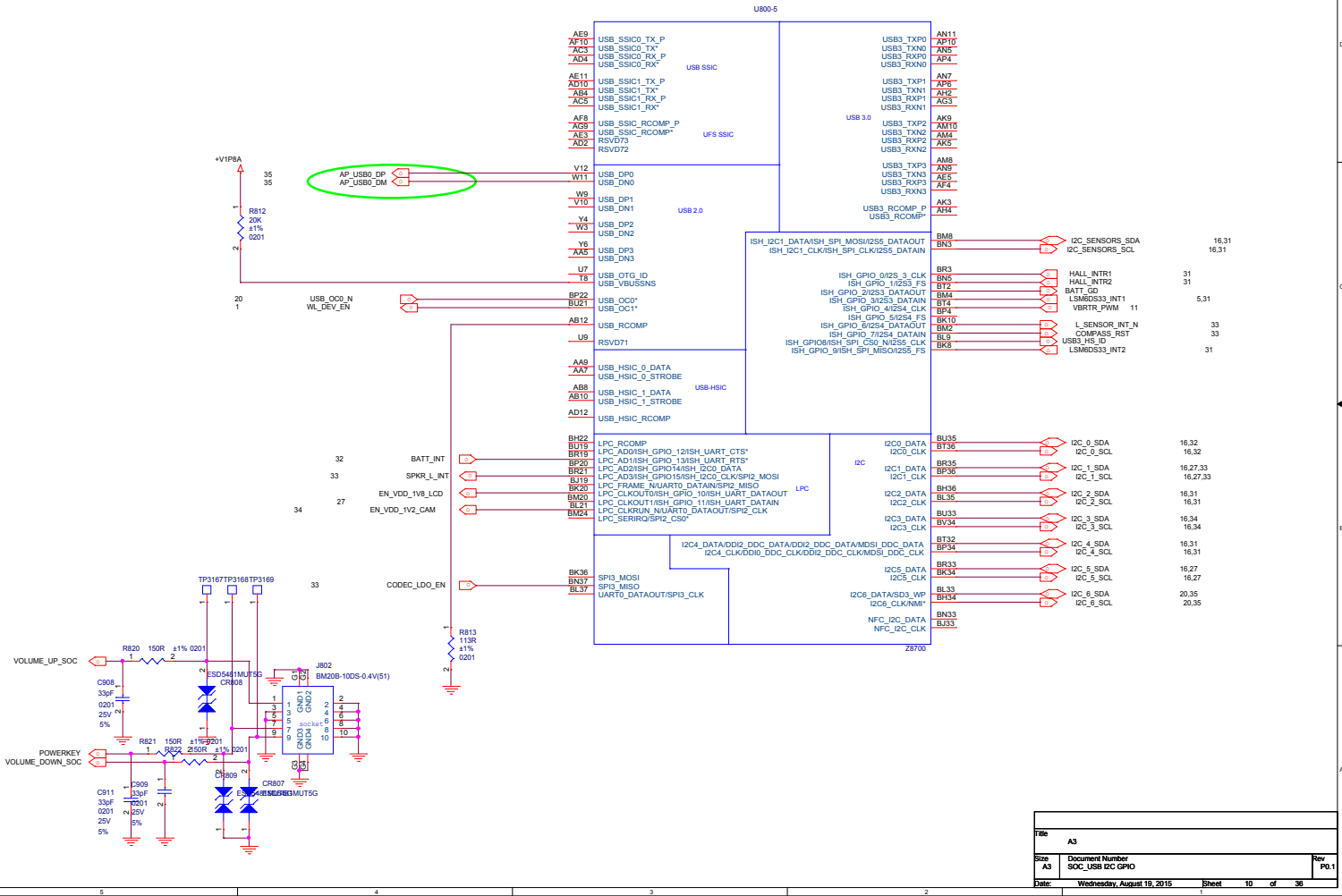




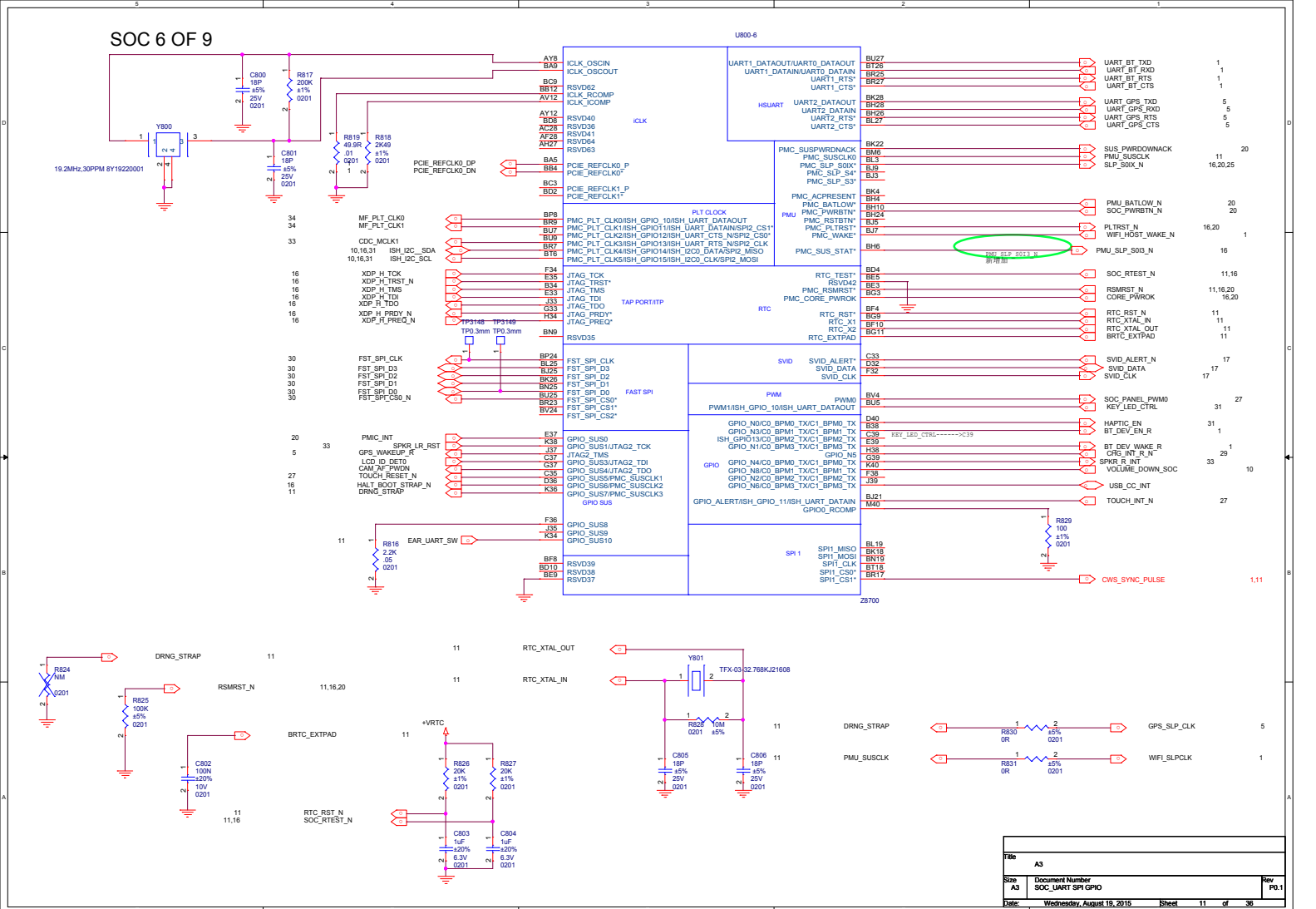


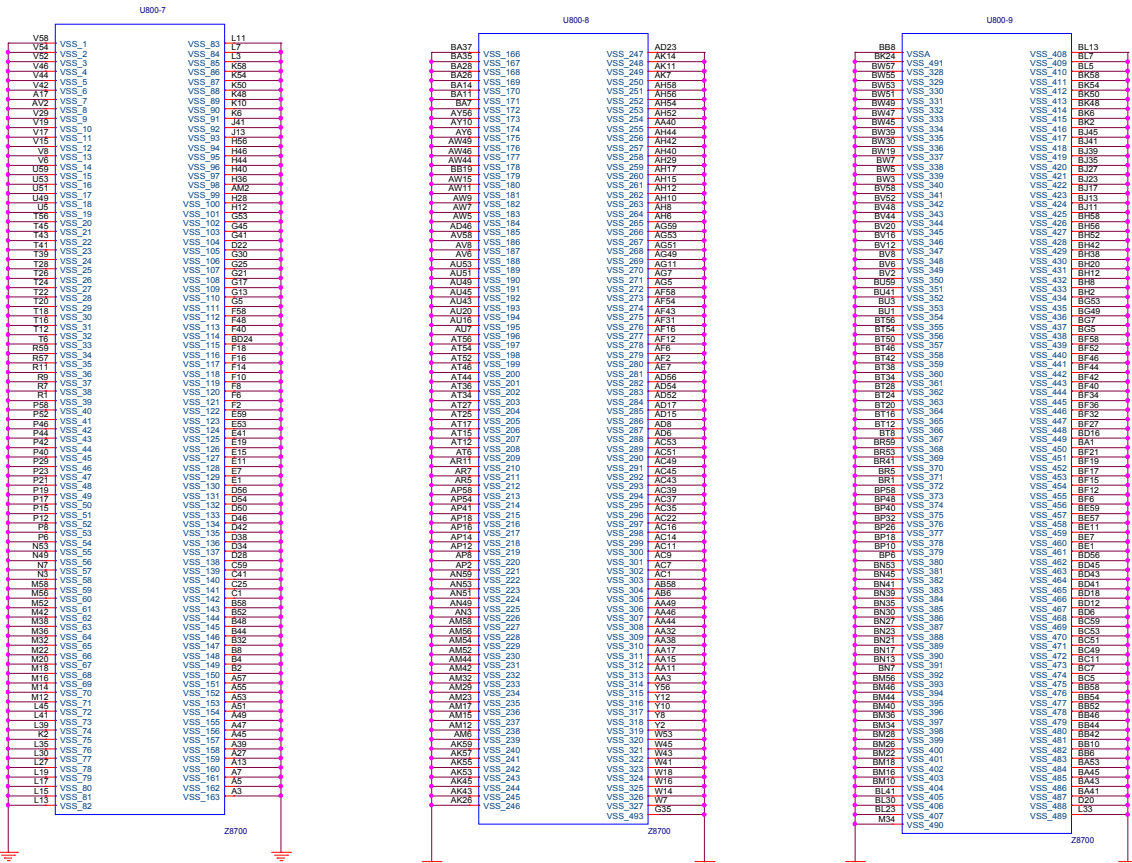


CAO NOTE:  
TEST POINTS AND COMPONENTS  
ON SOC PAGES SHOULD BE PLACED CLOSE TO RELEVANT COMPONENTS

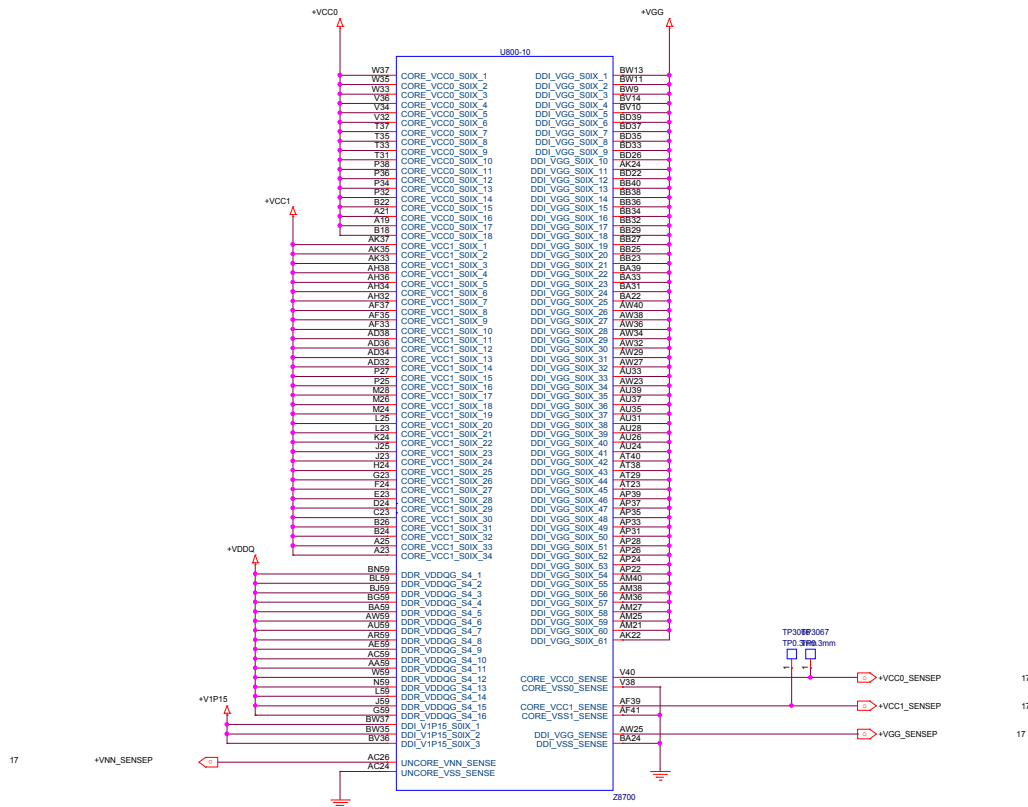


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Size	A3
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Size	Document Number	SOC_GND	
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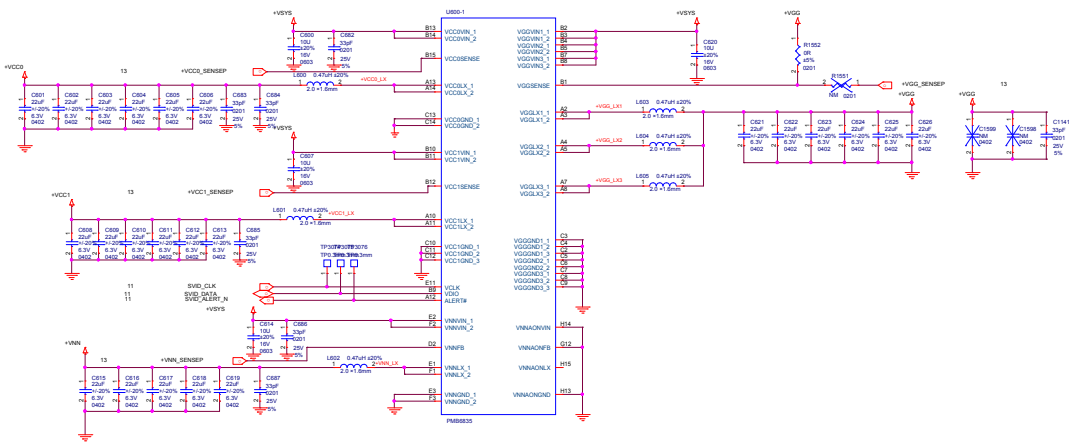
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Document Number	SOC_POWER 1
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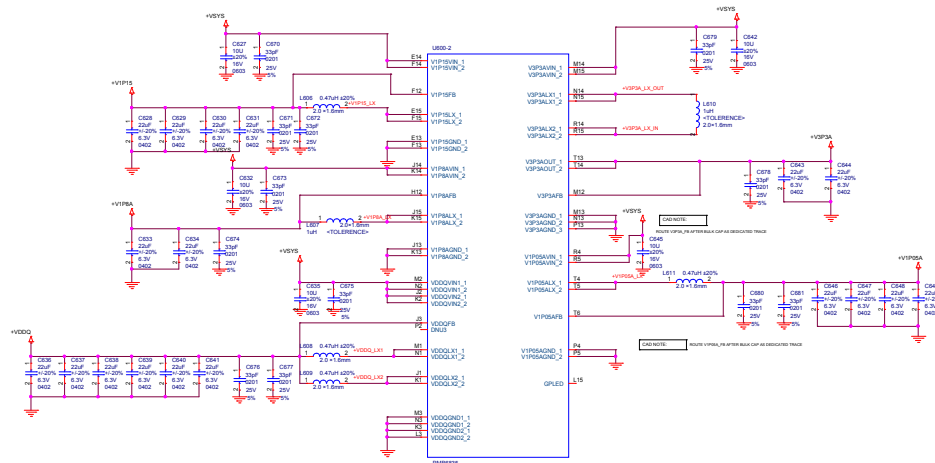


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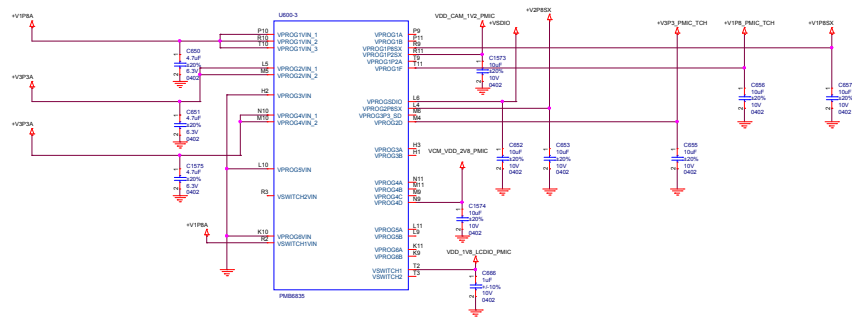






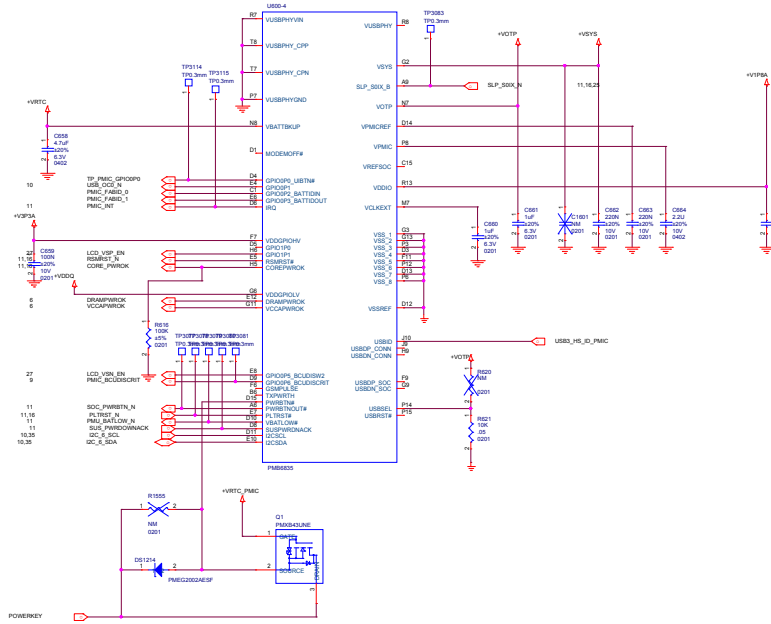
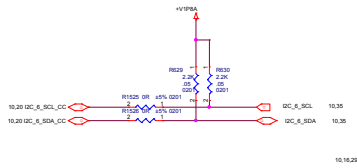
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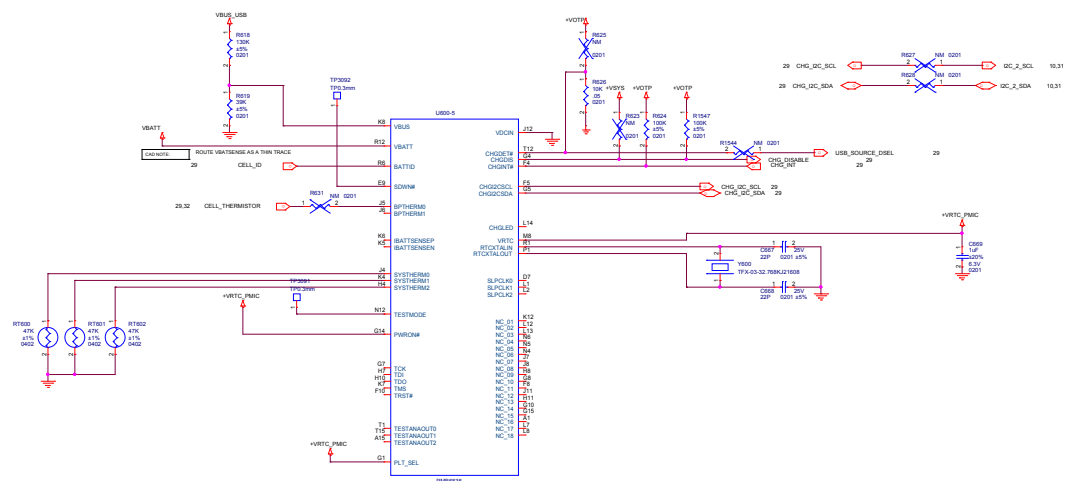
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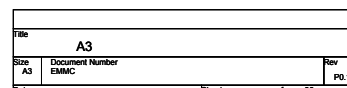
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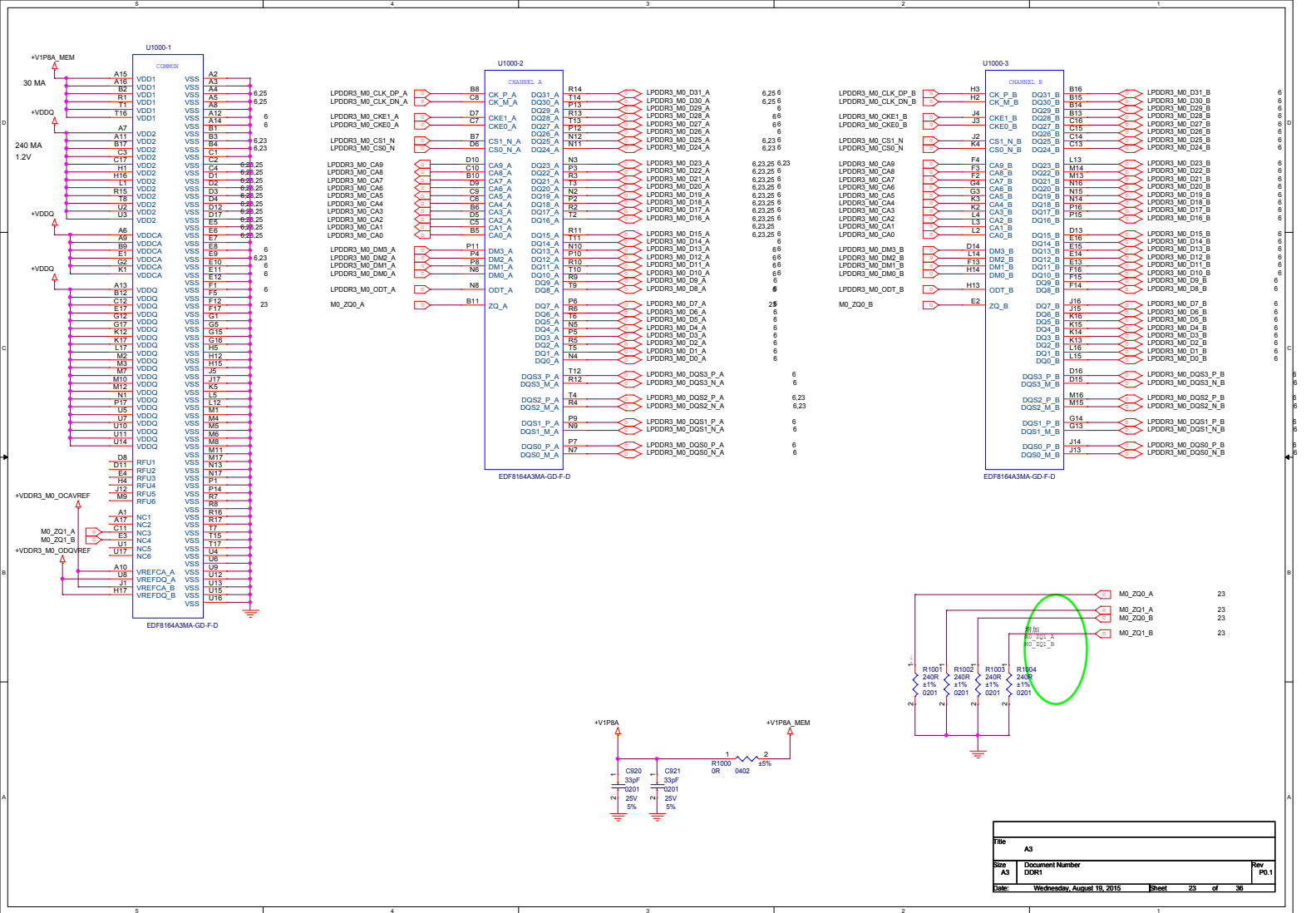
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L	L	P1
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L	H	P3
H	H	P4



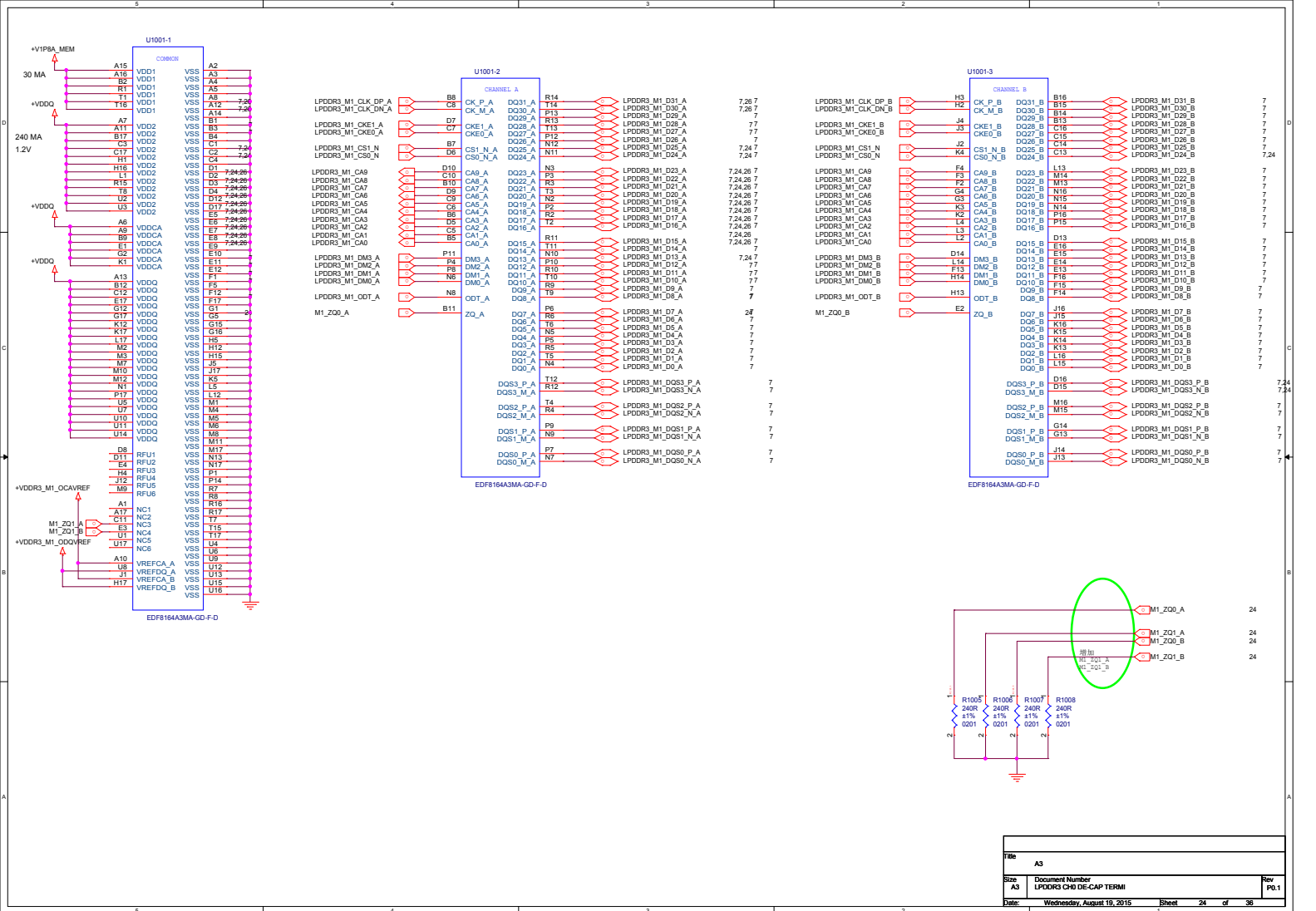


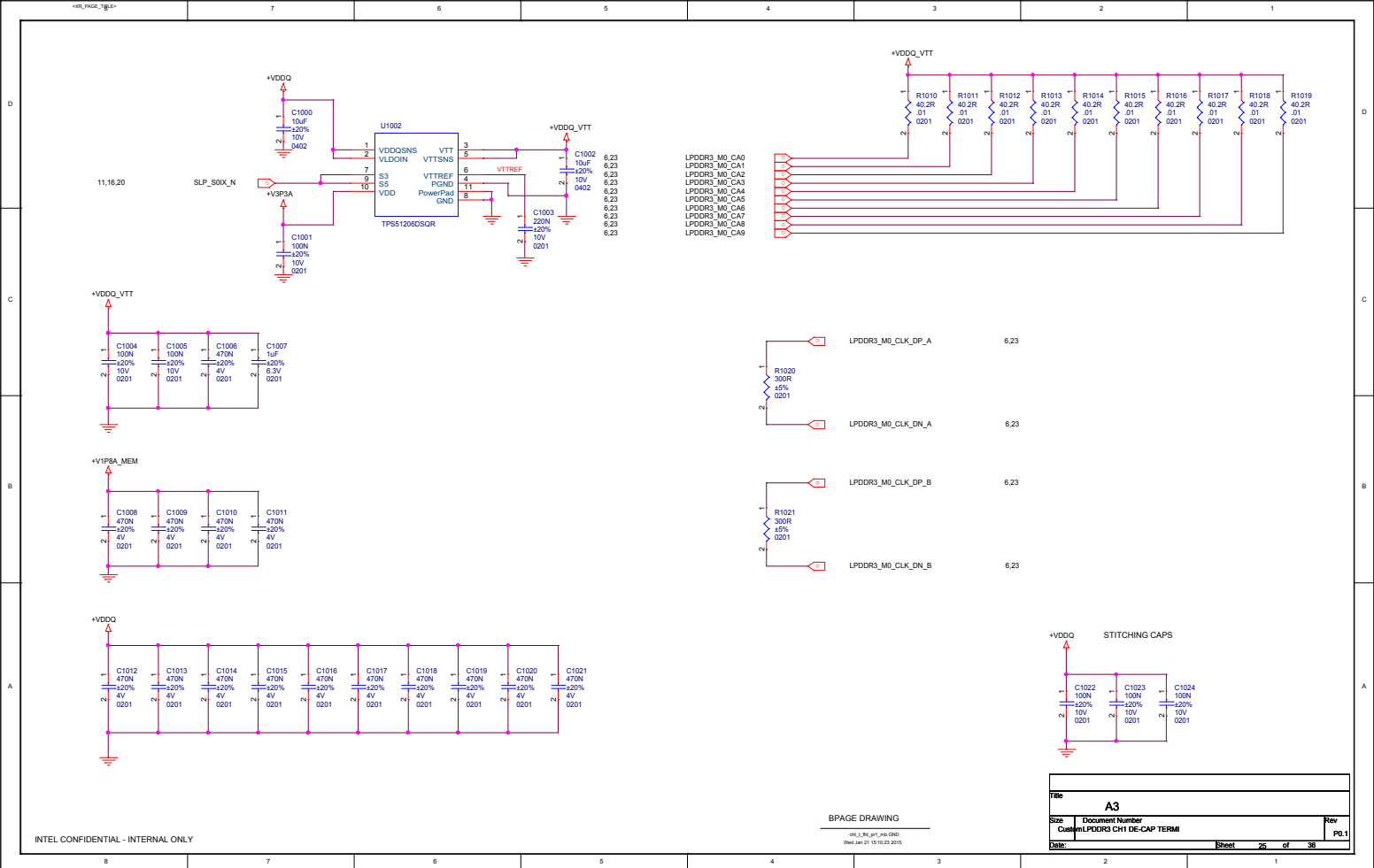
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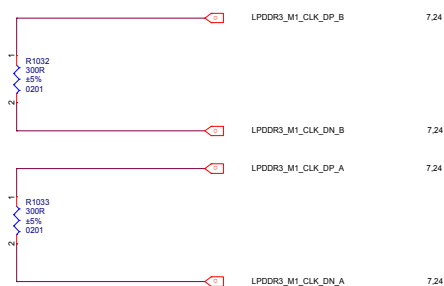
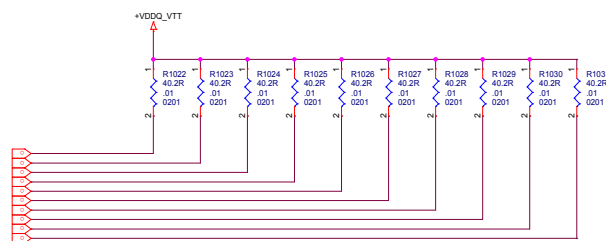
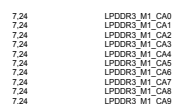
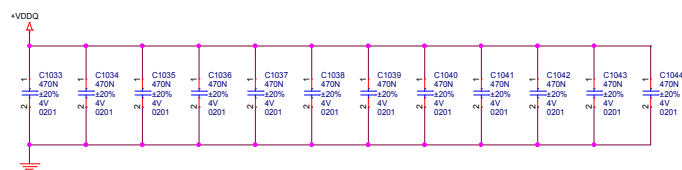
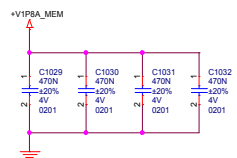
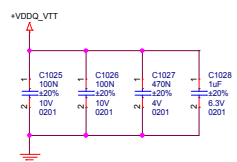


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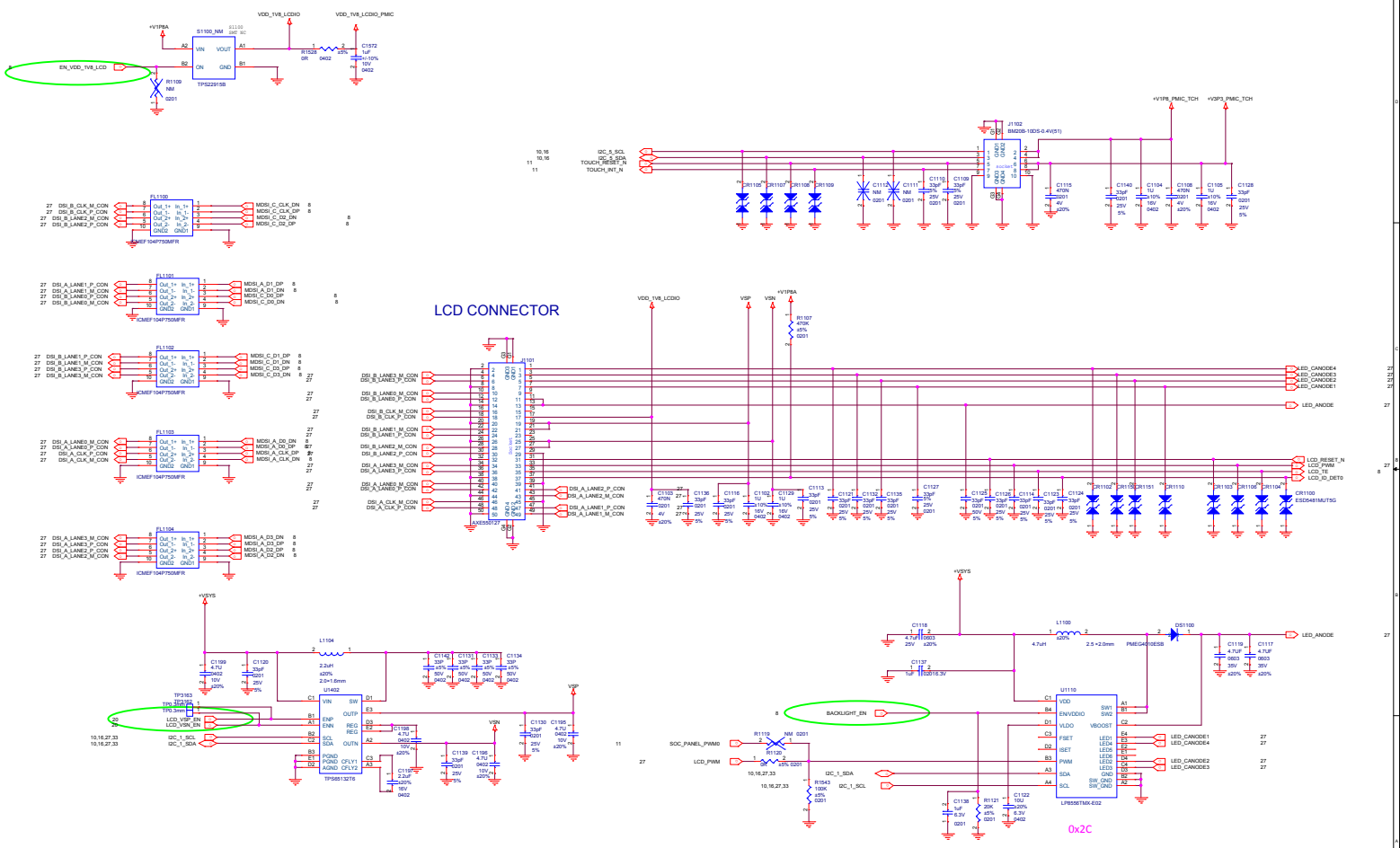


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chill\_981\_1mb + VDDQ 20  
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A3		Rev	1.0
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LCD CONNECTOR		Rev	
Date		Rev	

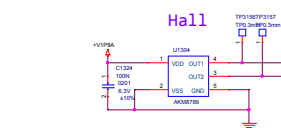
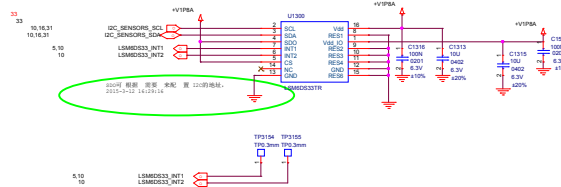
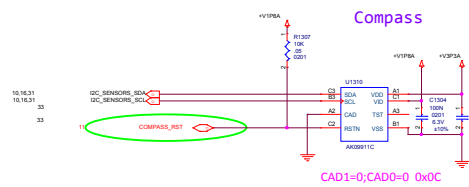
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SD CONNECTION	
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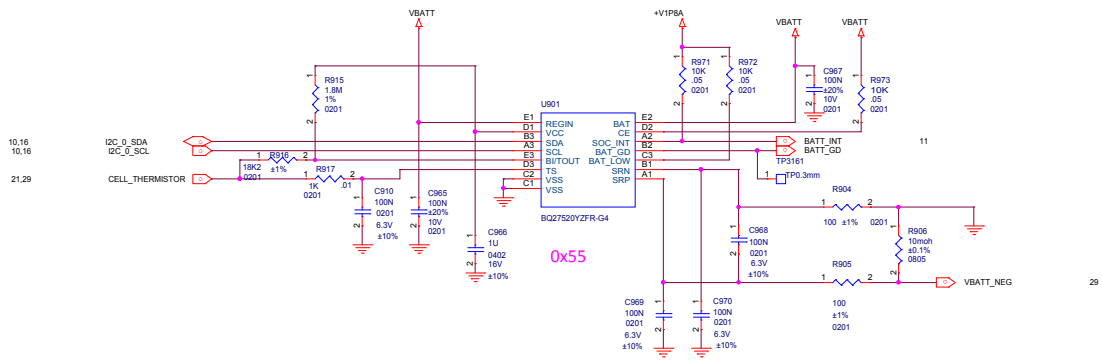


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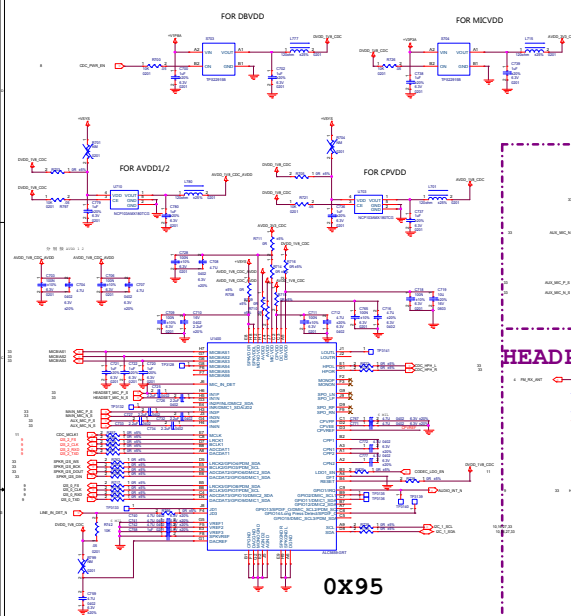
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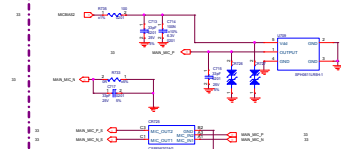


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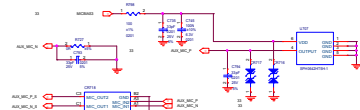
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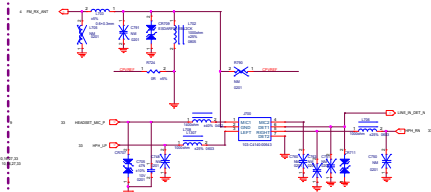
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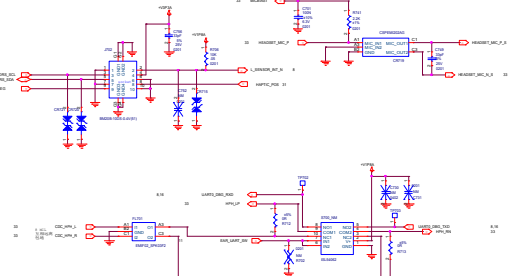
AUX Mic



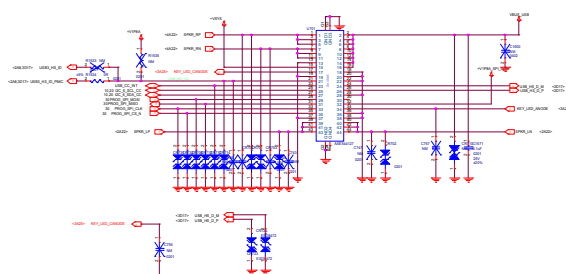
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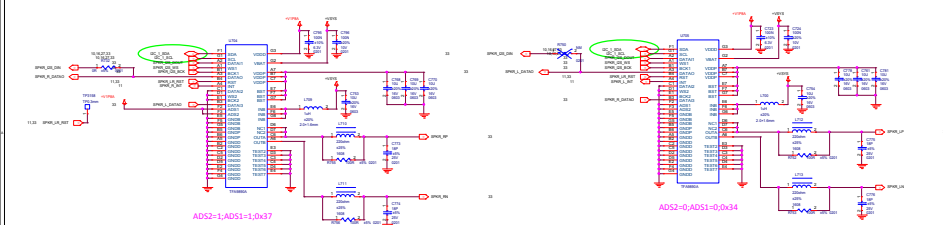
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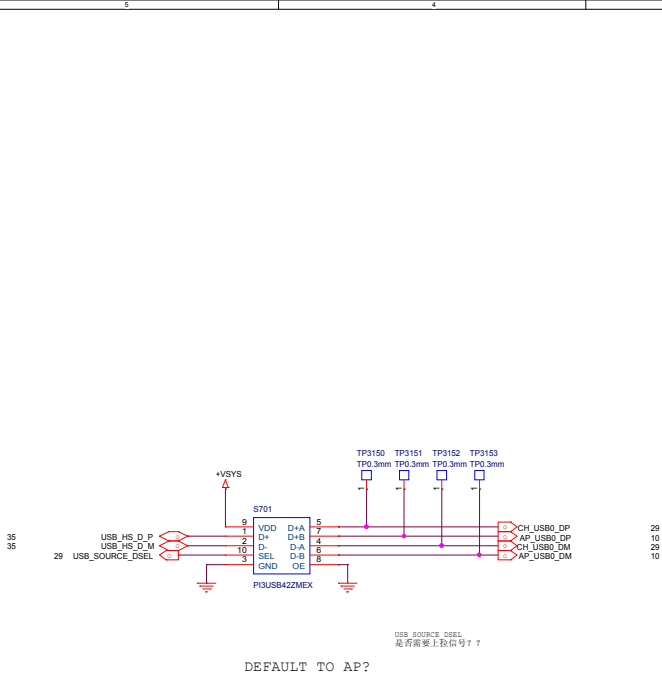
此处缺少sp:烧录时候1.8v的逻辑电压  
从J701上应输出另一1.8v电压做sp:烧录切换  
2015-4-2 17:03:41



**SPEAKER**







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D	DATA	PAGE	DESCRIPTION	DATA	PAGE	DESCRIPTION															
	4.15	10	DELL R815 R814	6.08	31	ADD R1549 R1550															
	4.15	11	ADD R816	4.03	33	DEL S700,C730, C731 ADD R767															
	4.15	14	DELL AK1/AG1 M2/L1 BW17/BV18/BF25 POWER NET	4.03	31	DEL CR730, C764 C763 CR729 CR728 C760;															
	4.15	31	CHANGE DS101 DESIGN	4.07	10	DEL R811															
	4.20	14	DEL R840 REMOVE C812 C813 CLOSE TO AW1/AU1	4.07	35	CHANGE NET AP_USB0_DP & AP_USB0_DN TO USB0															
	4.20	20	EXCHANGE S1101 A2/C2 POWER NET	4.07	33	ADD C760 C763;															
	5.15	14	Add AK1/AG1. L1/M2 Power net	4.08	19	ADD C1574 C173 C666															
	5.15	29		4.08	34	ADD R1530 R1531															
	5.15	20	Add R1538 R1539	4.08	28	ADD R1528															
C	5.22	33	Cross I2S_0 and I2S_2 path to codec Connect J700 Pin7 to GND Change U704 U705 to TFA9890AUK	4.08	33	ADD S700 C730 C731 DEL L1302															
	5.22	17	Add C1599 C1598	4.09	33	ADD DS1210															
	5.22	15	Add C1577 C1578 C1584 NM C1585 NM C1586 NM C1587 NM C1588 NMC1589 NM;	4.10	28	DEL SD CARD I130 J1131 R1150 C1160 C1150 C1151 C1152 C1153 C1154 C1115 C1156 C1157 FL1130															
	5.22	21	Add R1544 NM	4.10	09	DEL R807															
	5.22	8	Add TP3170 for OS Type	4.10	19	DEL C654															
	5.25	8	DelTP3170 for OS Type	4.13	11	CHANGE NET KEY_LED_CTRL TO BALL NUMBER BU5															
	5.28	14	Add R1545 R1546	4.13	10	CHANGE NET VBRTR_PWM TO BALL NUMBER BT4															
	6.04	33	add c1600	4.15	33	ADD R1533, R1534 R1535 DEL +VIP8A NET FROM U701															
	6.04	21	ADD r1547	4.15	29	ADD R1213 R1214 R1215															
	B																				
A																					
						<table><tr><td colspan="2">Title</td><td colspan="2" rowspan="2">&lt;Title&gt;</td></tr><tr><td>Size A</td><td colspan="3">Document Number &lt;Doc&gt;</td></tr><tr><td>Date:</td><td colspan="2">Wednesday, August 19, 2015</td><td>Sheet 36 of 36</td></tr></table>				Title		<Title>		Size A	Document Number <Doc>			Date:	Wednesday, August 19, 2015		Sheet 36 of 36
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5		4		3		2		1													